



# SPEC<sup>®</sup> CINT2006 Result

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## Itaotec

## SPECint<sup>®</sup>\_rate2006 = 224

### Servidor Itaotec LX205 (Intel Xeon E5-2630)

## SPECint\_rate\_base2006 = 214

CPU2006 license: 9001

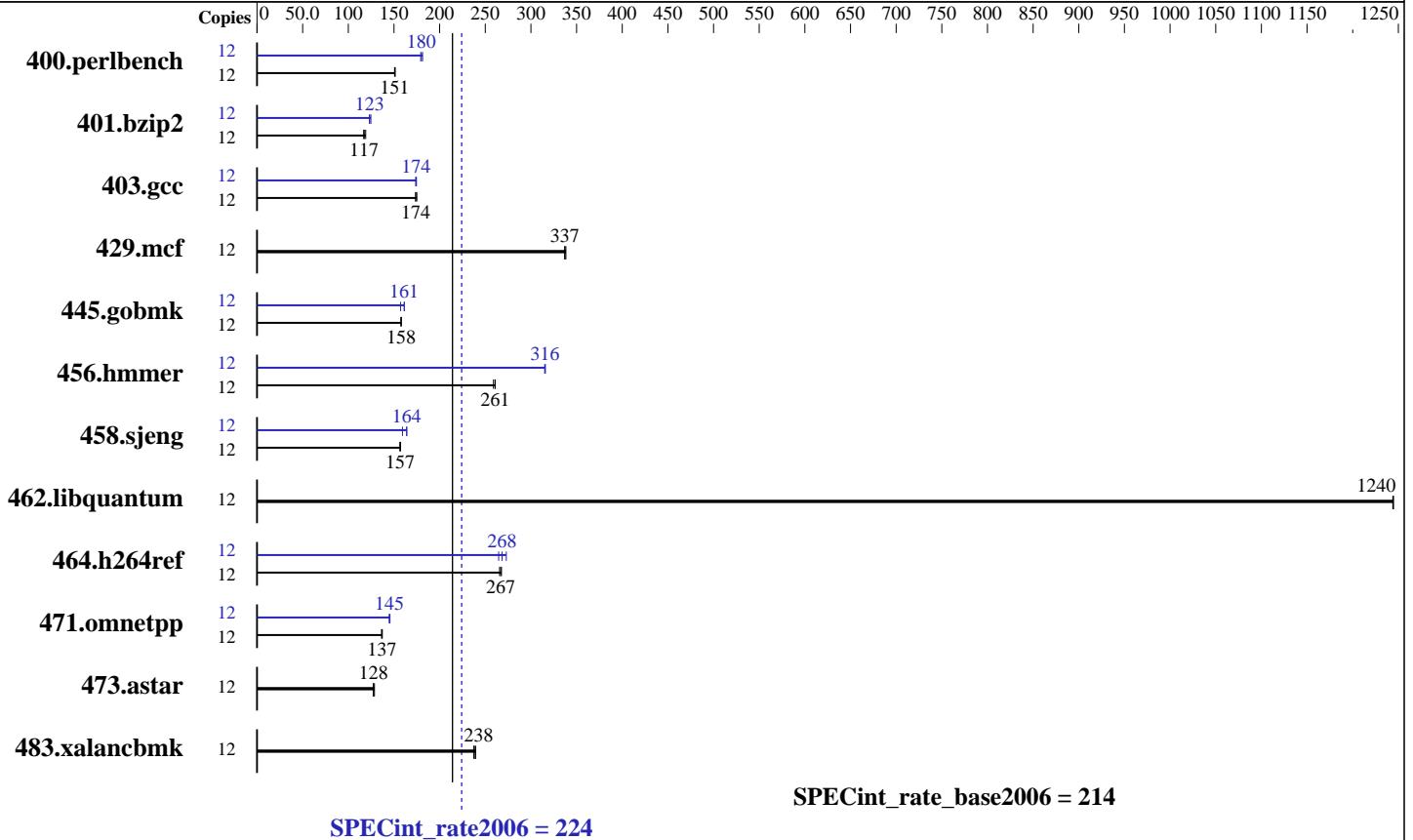
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Jan-2012

Hardware Availability: Jun-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-2630  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (8 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 500 GB, SATA-2, 7200RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server Release 6.3, 2.6.32-279.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	<b>776</b>	<b>151</b>	778	151	775	151	12	646	181	654	179	<b>651</b>	<b>180</b>
401.bzip2	12	973	119	<b>986</b>	<b>117</b>	991	117	12	941	123	929	125	<b>938</b>	<b>123</b>
403.gcc	12	552	175	556	174	<b>556</b>	<b>174</b>	12	556	174	553	175	<b>555</b>	<b>174</b>
429.mcf	12	<b>324</b>	<b>337</b>	324	338	325	337	12	<b>324</b>	<b>337</b>	324	338	325	337
445.gobmk	12	797	158	798	158	<b>798</b>	<b>158</b>	12	<b>781</b>	<b>161</b>	780	161	801	157
456.hammer	12	432	259	429	261	<b>429</b>	<b>261</b>	12	355	315	355	316	<b>355</b>	<b>316</b>
458.sjeng	12	<b>925</b>	<b>157</b>	928	156	924	157	12	885	164	<b>886</b>	<b>164</b>	911	159
462.libquantum	12	200	1240	<b>200</b>	<b>1240</b>	200	1240	12	200	1240	<b>200</b>	<b>1240</b>	200	1240
464.h264ref	12	992	268	1000	266	<b>995</b>	<b>267</b>	12	<b>989</b>	<b>268</b>	973	273	1003	265
471.omnetpp	12	547	137	<b>548</b>	<b>137</b>	550	136	12	515	146	<b>517</b>	<b>145</b>	518	145
473.astar	12	655	129	<b>660</b>	<b>128</b>	662	127	12	655	129	<b>660</b>	<b>128</b>	662	127
483.xalancbmk	12	<b>348</b>	<b>238</b>	346	239	349	237	12	<b>348</b>	<b>238</b>	346	239	349	237

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## Platform Notes

Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo  
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c  
running on itaotec2fam5 Fri Jan 6 17:13:49 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Genuine Intel(R) CPU @ 2.30GHz  
1 "physical id"s (chips)  
12 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 6  
siblings : 12

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## Platform Notes (Continued)

physical 0: cores 0 1 2 3 4 5  
cache size : 15360 KB

From /proc/meminfo  
MemTotal: 32850880 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
Red Hat Enterprise Linux Server release 6.3 (Santiago)

From /etc/\*release\* /etc/\*version\*  
redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)  
system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:  
Linux itaotec2fam5 2.6.32-279.el6.x86\_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Jan 6 17:08

SPEC is set to: /home/rcaneca/cpu2006  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/vg\_itaotec2fam5-lv\_home  
ext4 94G 2.1G 87G 3% /home

(End of data from sysinfo program)

## General Notes

This result was measured on the Servidor Itaotec MX205.  
The Servidor Itaotec LX205, Servidor Itaotec MX205 and the Servidor Itaotec MX225+ are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

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## Base Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
400.perlbench: icc -m64  
401.bzip2: icc -m64  
456.hmmer: icc -m64  
458.sjeng: icc -m64  
C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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Itautec

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Servidor Itautec LX205 (Intel Xeon E5-2630)

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CPU2006 license: 9001  
Test sponsor: Itautec  
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## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.html](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.html)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.xml](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.xml)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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