



# SPEC® CINT2006 Result

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## Supermicro

SuperServer 5037A-iL (X9SAE, Intel Xeon E3-1290 v2)

SPECint®2006 = 58.9

SPECint\_base2006 = 55.5

CPU2006 license: 001176

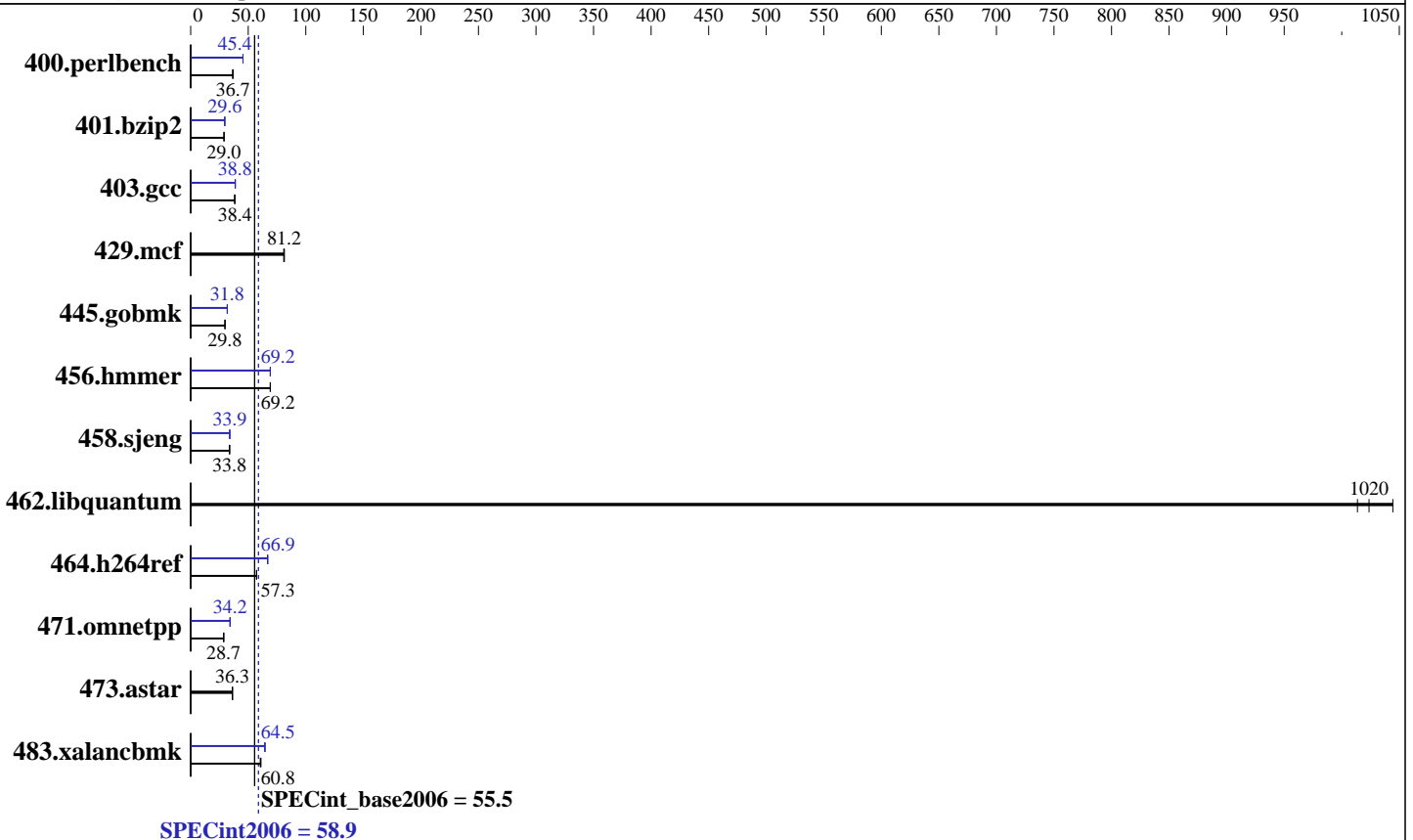
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Sep-2012

Hardware Availability: Apr-2012

Software Availability: Jun-2012



### Hardware

CPU Name: Intel Xeon E3-1290 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.10 GHz  
 CPU MHz: 3700  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3-12800U-11)  
 Disk Subsystem: 1 x 300 GB SATA II, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server Release 6.3, Kernel 2.6.32-279.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	266	36.7	<b><u>266</u></b>	<b><u>36.7</u></b>	268	36.5	215	45.4	215	45.4	<b><u>215</u></b>	<b><u>45.4</u></b>
401.bzip2	333	29.0	<b><u>333</u></b>	<b><u>29.0</u></b>	333	29.0	326	29.6	326	29.6	<b><u>326</u></b>	<b><u>29.6</u></b>
403.gcc	<b><u>210</u></b>	<b><u>38.4</u></b>	210	38.4	211	38.1	207	38.8	<b><u>208</u></b>	<b><u>38.8</u></b>	209	38.6
429.mcf	112	81.4	<b><u>112</u></b>	<b><u>81.2</u></b>	113	80.8	112	81.4	<b><u>112</u></b>	<b><u>81.2</u></b>	113	80.8
445.gobmk	351	29.9	352	29.8	<b><u>352</u></b>	<b><u>29.8</u></b>	330	31.8	330	31.8	<b><u>330</u></b>	<b><u>31.8</u></b>
456.hammer	135	69.2	135	69.1	<b><u>135</u></b>	<b><u>69.2</u></b>	135	69.1	<b><u>135</u></b>	<b><u>69.2</u></b>	135	69.2
458.sjeng	359	33.7	358	33.8	<b><u>358</u></b>	<b><u>33.8</u></b>	<b><u>356</u></b>	<b><u>33.9</u></b>	356	34.0	357	33.9
462.libquantum	20.4	1010	<b><u>20.2</u></b>	<b><u>1020</u></b>	19.8	1040	20.4	1010	<b><u>20.2</u></b>	<b><u>1020</u></b>	19.8	1040
464.h264ref	387	57.2	386	57.3	<b><u>386</u></b>	<b><u>57.3</u></b>	<b><u>331</u></b>	<b><u>66.9</u></b>	331	66.8	331	66.9
471.omnetpp	219	28.5	217	28.8	<b><u>218</u></b>	<b><u>28.7</u></b>	183	34.2	182	34.3	<b><u>183</u></b>	<b><u>34.2</u></b>
473.astar	193	36.4	194	36.2	<b><u>193</u></b>	<b><u>36.3</u></b>	193	36.4	194	36.2	<b><u>193</u></b>	<b><u>36.3</u></b>
483.xalancbmk	114	60.4	113	61.0	<b><u>113</u></b>	<b><u>60.8</u></b>	107	64.4	107	64.6	<b><u>107</u></b>	<b><u>64.5</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

```
Sysinfo program /usr/cpu2006/Docs/sysinfo
$Rev: 6775 $ $Date:: 2011-08-16 #$ 8787f7622badcf24e01c368b1db4377c
running on localhost Sat Sep 22 17:58:26 2012
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1290 V2 @ 3.70GHz
1 "physical id"s (chips)
8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 4
siblings : 8
physical 0: cores 0 1 2 3
cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal: 16412704 kB
HugePages_Total: 0
```

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### Platform Notes (Continued)

Hugepagesize: 2048 kB

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.3 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Sep 21 14:01

```
SPEC is set to: /usr/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
                ext4      50G   36G   12G   76% /
```

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"
OMP_NUM_THREADS = "4"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

### Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

### Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

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## Base Portability Flags (Continued)

```

401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs -L/smarterheap -lsmarterheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64



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Tested by: Supermicro

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## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
            -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
           -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
           -ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2)
             -opt-ra-region-strategy=block -ansi-alias
             -Wl,-z,muldefs -L/smartheap -lsmartheap

```

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## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

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