



# SPEC® CINT2006 Result

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## Supermicro

Supermicro X9SAE-V motherboard (Intel Core i7-3770T, 2.50 GHz)

**SPECint®2006 = 53.5**

**SPECint\_base2006 = 49.8**

CPU2006 license: 001176

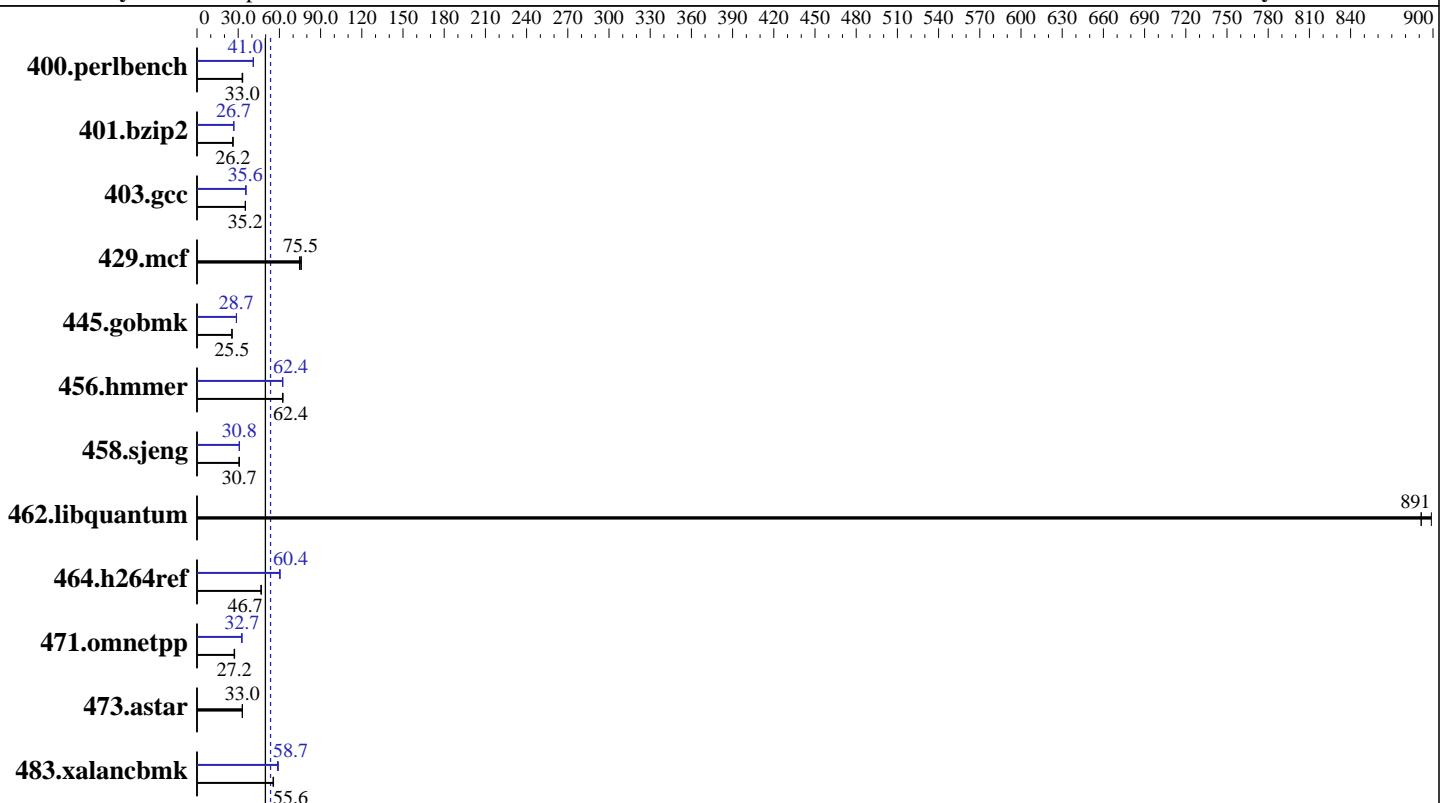
Test sponsor: Supermicro

Tested by: Supermicro

**Test date:** Sep-2012

**Hardware Availability:** Apr-2012

**Software Availability:** Jun-2012



### Hardware

CPU Name: Intel Core i7-3770T  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3-12800U-11)  
 Disk Subsystem: 1 x 300 GB SATA II, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server Release 6.3, Kernel 2.6.32-279.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	297	32.9	<b>296</b>	<b>33.0</b>	294	33.3	238	41.0	<b>238</b>	<b>41.0</b>	238	41.0
401.bzip2	369	26.1	368	26.2	<b>369</b>	<b>26.2</b>	361	26.7	360	26.8	<b>361</b>	<b>26.7</b>
403.gcc	228	35.2	<b>229</b>	<b>35.2</b>	229	35.2	<b>226</b>	<b>35.6</b>	227	35.5	226	35.6
429.mcf	122	74.6	<b>121</b>	<b>75.5</b>	120	76.0	122	74.6	<b>121</b>	<b>75.5</b>	120	76.0
445.gobmk	412	25.5	412	25.4	<b>412</b>	<b>25.5</b>	<b>365</b>	<b>28.7</b>	365	28.7	365	28.7
456.hmmer	149	62.4	<b>149</b>	<b>62.4</b>	149	62.5	150	62.3	<b>149</b>	<b>62.4</b>	149	62.4
458.sjeng	394	30.7	394	30.7	<b>394</b>	<b>30.7</b>	393	30.8	<b>392</b>	<b>30.8</b>	392	30.8
462.libquantum	<b>23.2</b>	<b>891</b>	23.1	899	23.2	891	<b>23.2</b>	<b>891</b>	23.1	899	23.2	891
464.h264ref	<b>474</b>	<b>46.7</b>	474	46.7	474	46.7	367	60.3	<b>367</b>	<b>60.4</b>	367	60.4
471.omnetpp	<b>230</b>	<b>27.2</b>	230	27.1	228	27.4	191	32.8	<b>191</b>	<b>32.7</b>	192	32.5
473.astar	212	33.1	<b>213</b>	<b>33.0</b>	213	33.0	212	33.1	<b>213</b>	<b>33.0</b>	213	33.0
483.xalancbmk	125	55.4	124	55.7	<b>124</b>	<b>55.6</b>	116	59.2	<b>118</b>	<b>58.7</b>	118	58.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

As tested, the system used a Supermicro CSE-732D4-500B chassis. The chassis is configured with a PWS-502-PQ power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0124L4 rear cooling fan.  
 Sysinfo program /usr/cpu2006/Docs/sysinfo  
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\\$ 8787f7622badcf24e01c368b1db4377c  
running on localhost Thu Sep 20 14:11:12 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Core(TM) i7-3770T CPU @ 2.50GHz
  1 "physical id"s (chips)
  8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings : 8
  physical 0: cores 0 1 2 3
cache size : 8192 KB
```

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## Platform Notes (Continued)

```
From /proc/meminfo
    MemTotal:       16340504 kB
    HugePages_Total:      0
    Hugepagesize:     2048 kB

/usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.3 (Santiago)

From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
    Linux localhost 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012
    x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 20 14:05

SPEC is set to: /usr/cpu2006
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/mapper/VolGroup-lv_root
        ext4          50G   27G   21G   57%  /


(End of data from sysinfo program)
```

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

OMP\_NUM\_THREADS = "4"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enable
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```



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## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64  
401.bzip2: -DSPEC_CPU_LP64  
403.gcc: -DSPEC_CPU_LP64  
429.mcf: -DSPEC_CPU_LP64  
445.gobmk: -DSPEC_CPU_LP64  
456.hammer: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX  
464.h264ref: -DSPEC_CPU_LP64  
471.omnetpp: -DSPEC_CPU_LP64  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs  
-L/smarterheap -lsmarterheap64
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32
```

```
445.gobmk: icc -m32
```

```
464.h264ref: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m32
```

```
473.astar: icpc -m64
```



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## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
   403.gcc: -DSPEC_CPU_LP64
   429.mcf: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
   473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
               -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
               -ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
               -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-ra-region-strategy=block           -ansi-alias
               -Wl,-z,muldefs -L/smartheap -lsmartheap

```

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## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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