



SPEC® CINT2006 Result

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Supermicro

Supermicro C7Q67 motherboard (Intel Core i7-2700K, 3.50 GHz)

SPECint®_rate2006 = 172

SPECint_rate_base2006 = 166

CPU2006 license: 001176

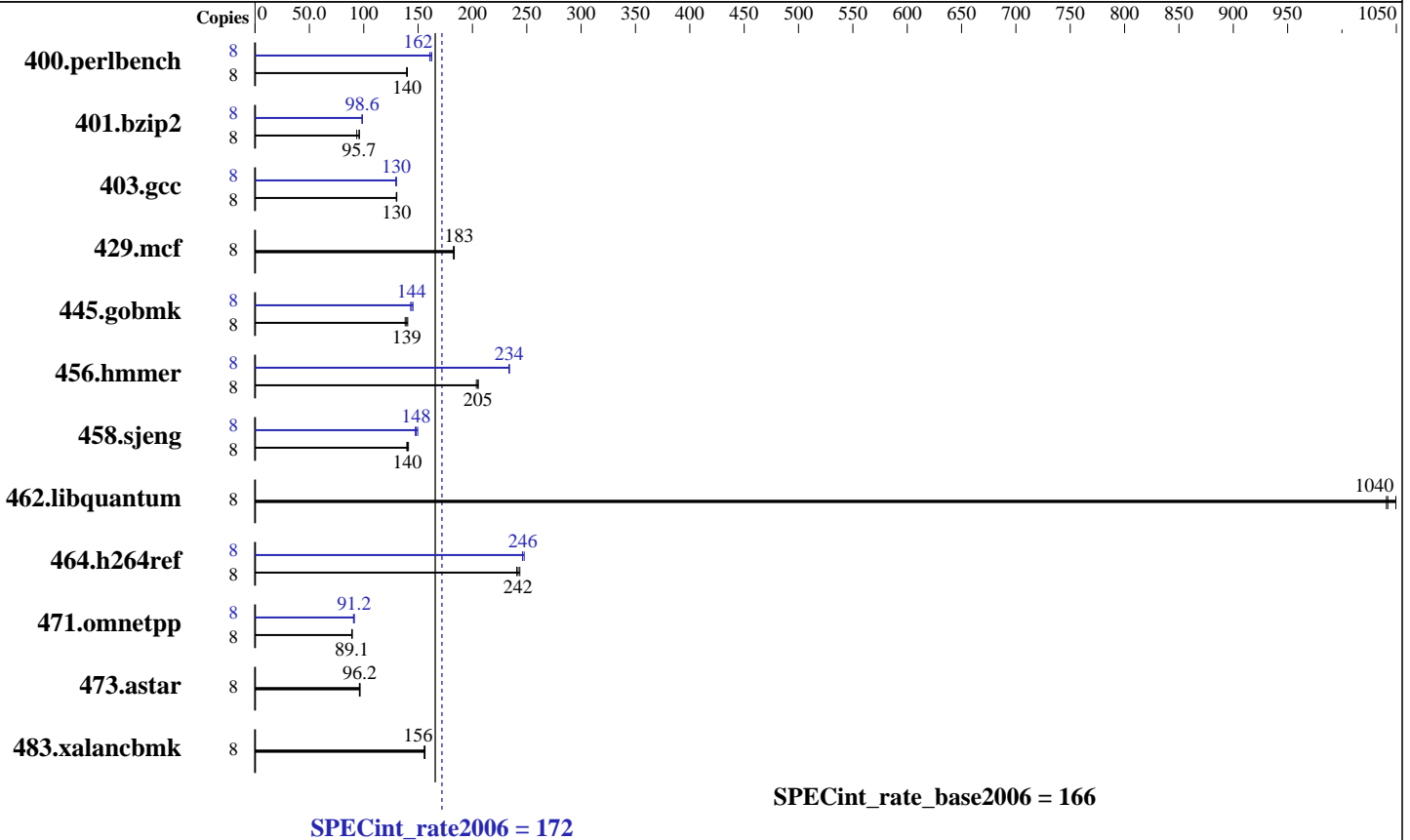
Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2012

Hardware Availability: Oct-2011

Software Availability: Dec-2011



Hardware

CPU Name: Intel Core i7-2700K
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3-12800E-11, ECC)
 Disk Subsystem: 1 x 600 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	560	139	<u>559</u>	<u>140</u>	558	140	8	<u>482</u>	<u>162</u>	486	161	481	162
401.bzip2	8	825	93.6	<u>807</u>	<u>95.7</u>	804	96.0	8	782	98.7	784	98.5	<u>783</u>	<u>98.6</u>
403.gcc	8	<u>495</u>	<u>130</u>	495	130	494	130	8	<u>496</u>	<u>130</u>	495	130	497	130
429.mcf	8	398	183	400	183	<u>399</u>	<u>183</u>	8	398	183	400	183	<u>399</u>	<u>183</u>
445.gobmk	8	608	138	597	140	<u>602</u>	<u>139</u>	8	578	145	586	143	<u>582</u>	<u>144</u>
456.hammer	8	363	205	<u>364</u>	<u>205</u>	367	204	8	319	234	319	234	<u>319</u>	<u>234</u>
458.sjeng	8	686	141	693	140	<u>691</u>	<u>140</u>	8	<u>653</u>	<u>148</u>	647	150	657	147
462.libquantum	8	159	1040	<u>159</u>	<u>1040</u>	158	1050	8	159	1040	<u>159</u>	<u>1040</u>	158	1050
464.h264ref	8	727	244	735	241	<u>732</u>	<u>242</u>	8	<u>719</u>	<u>246</u>	715	248	720	246
471.omnetpp	8	559	89.5	<u>561</u>	<u>89.1</u>	562	89.0	8	552	90.6	548	91.2	<u>548</u>	<u>91.2</u>
473.astar	8	585	96.0	<u>584</u>	<u>96.2</u>	581	96.6	8	585	96.0	<u>584</u>	<u>96.2</u>	581	96.6
483.xalancbmk	8	353	156	355	156	<u>354</u>	<u>156</u>	8	353	156	355	156	<u>354</u>	<u>156</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Platform Notes

As tested, the system used a Supermicro CSE-731i-300B chassis. The chassis is configured with a PWS-303-PQ power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0108L4 rear cooling fan and 1 FAN-0113L4 front intake fan.

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5



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Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
           -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
           -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
           -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
             -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalanbmk: basepeak = yes

```



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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