



# SPEC<sup>®</sup> CFP2006 Result

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## Bull SAS

SPECfp<sup>®</sup>2006 = **66.7**

NovaScale R450 F3 (Intel Xeon E5-2440, 2.40 GHz)

SPECfp\_base2006 = **63.7**

CPU2006 license: 20

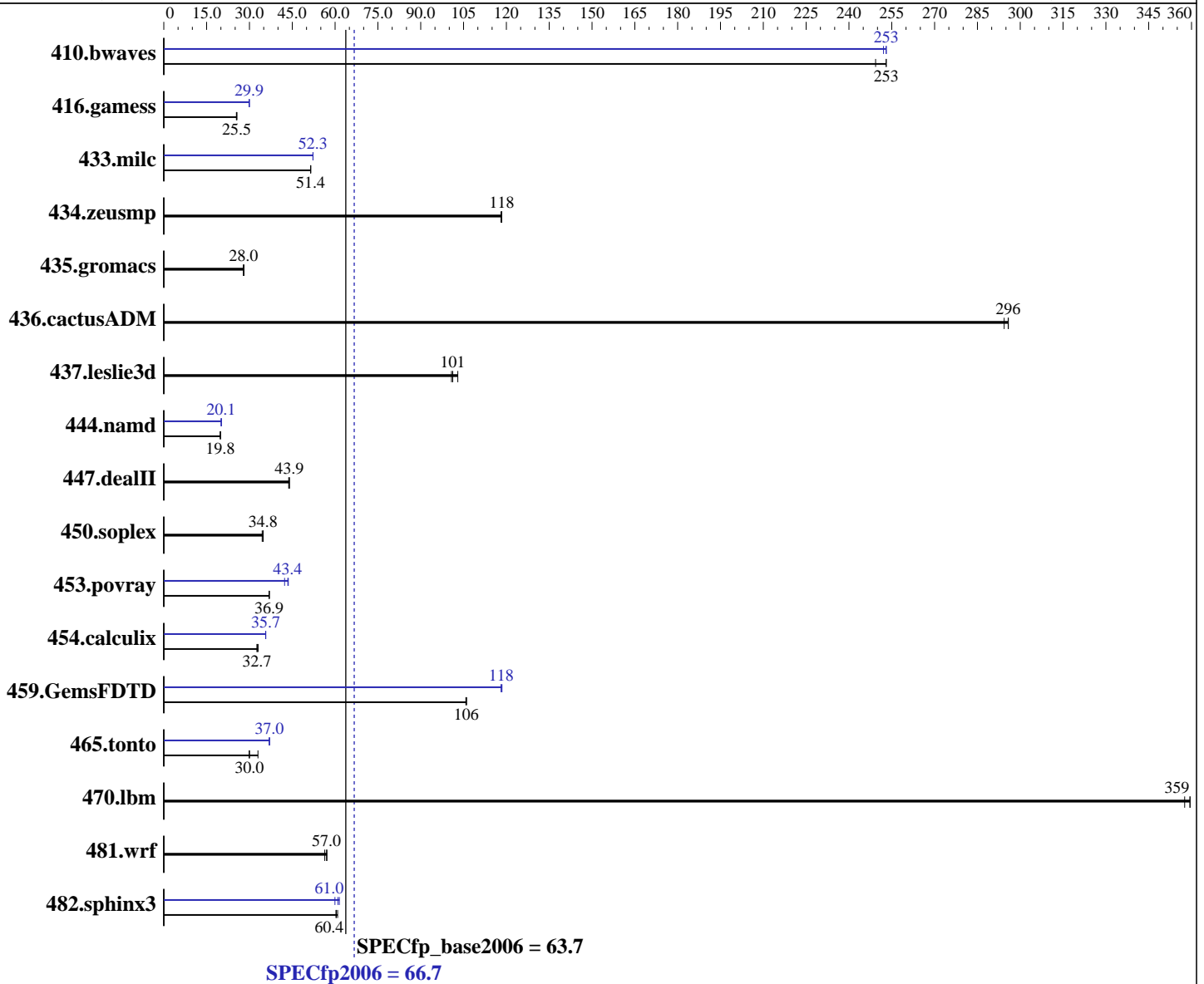
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Mar-2012

Hardware Availability: May-2012

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-2440  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64) 3.0.13-0.9-default  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (add definition here)

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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
 Disk Subsystem: 2 x 600 GB 15000 RPM SAS, RAID 1  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	54.5	249	<b>53.7</b>	<b>253</b>	53.7	253	<b>53.7</b>	<b>253</b>	53.7	253	53.9	252
416.gamess	770	25.4	764	25.6	<b>767</b>	<b>25.5</b>	<b>654</b>	<b>29.9</b>	654	30.0	654	29.9
433.milc	178	51.4	178	51.5	<b>178</b>	<b>51.4</b>	176	52.3	<b>176</b>	<b>52.3</b>	176	52.2
434.zeusmp	77.1	118	76.9	118	<b>76.9</b>	<b>118</b>	77.1	118	76.9	118	<b>76.9</b>	<b>118</b>
435.gromacs	254	28.1	256	27.9	<b>255</b>	<b>28.0</b>	254	28.1	256	27.9	<b>255</b>	<b>28.0</b>
436.cactusADM	40.4	296	<b>40.4</b>	<b>296</b>	40.6	294	40.4	296	<b>40.4</b>	<b>296</b>	40.6	294
437.leslie3d	<b>92.9</b>	<b>101</b>	91.3	103	93.3	101	<b>92.9</b>	<b>101</b>	91.3	103	93.3	101
444.namd	404	19.8	<b>405</b>	<b>19.8</b>	405	19.8	398	20.1	<b>398</b>	<b>20.1</b>	398	20.1
447.dealII	<b>260</b>	<b>43.9</b>	260	44.0	261	43.7	<b>260</b>	<b>43.9</b>	260	44.0	261	43.7
450.soplex	<b>240</b>	<b>34.8</b>	240	34.8	242	34.5	<b>240</b>	<b>34.8</b>	240	34.8	242	34.5
453.povray	144	37.0	<b>144</b>	<b>36.9</b>	144	36.9	<b>122</b>	<b>43.4</b>	122	43.6	126	42.3
454.calculix	<b>253</b>	<b>32.7</b>	250	33.0	253	32.7	231	35.7	<b>231</b>	<b>35.7</b>	231	35.7
459.GemsFDTD	<b>100</b>	<b>106</b>	100	106	100	106	89.6	118	<b>89.8</b>	<b>118</b>	89.8	118
465.tonto	298	33.0	<b>328</b>	<b>30.0</b>	329	29.9	266	37.0	<b>266</b>	<b>37.0</b>	266	36.9
470.lbm	38.2	359	<b>38.2</b>	<b>359</b>	38.4	358	38.2	359	<b>38.2</b>	<b>359</b>	38.4	358
481.wrf	<b>196</b>	<b>57.0</b>	196	57.1	198	56.4	<b>196</b>	<b>57.0</b>	196	57.1	198	56.4
482.sphinx3	320	60.9	323	60.3	<b>323</b>	<b>60.4</b>	325	59.9	<b>320</b>	<b>61.0</b>	317	61.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

CPU Power Management set to Maximum Performance  
 Memory Frequency set to Maximum Performance  
 Turbo Boost set to Enabled  
 C States/C1E set to Enabled  
 Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date: 2011-10-11 # \$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on Silk-2P Mon Mar 12 18:12:54 2012

This section contains SUT (System Under Test) info as seen by

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### Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2440 0 @ 2.40GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 6
siblings : 12
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal: 49381468 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2
```

```
uname -a:
Linux Silk-2P 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012
(54ddfaf) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 12 10:58 last=S
```

```
SPEC is set to: /root/CPU2006-1.2
Filesystem Type Size Used Avail Use% Mounted on
/dev/sdal ext3 493G 9.1G 459G 2% /
```

Additional information from dmidecode:

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"  
OMP\_NUM\_THREADS = "12"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
 Transparent Huge Pages disabled with:  
 echo never > /sys/kernel/mm/transparent\_hugepage/enabled  
 Filesystem page cache cleared with:  
 echo 1> /proc/sys/vm/drop\_caches  
 The Dell PowerEdge R520 and the Bull NovaScale R450 F3 models are electronically equivalent.  
 The results have been measured on a Dell PowerEdge R520 model

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>

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Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 05:47:52 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 5 June 2012.