



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = 64.7

### Huawei BH622 V2 (Intel Xeon E5-2650L)

SPECfp\_base2006 = 60.8

CPU2006 license: 3175

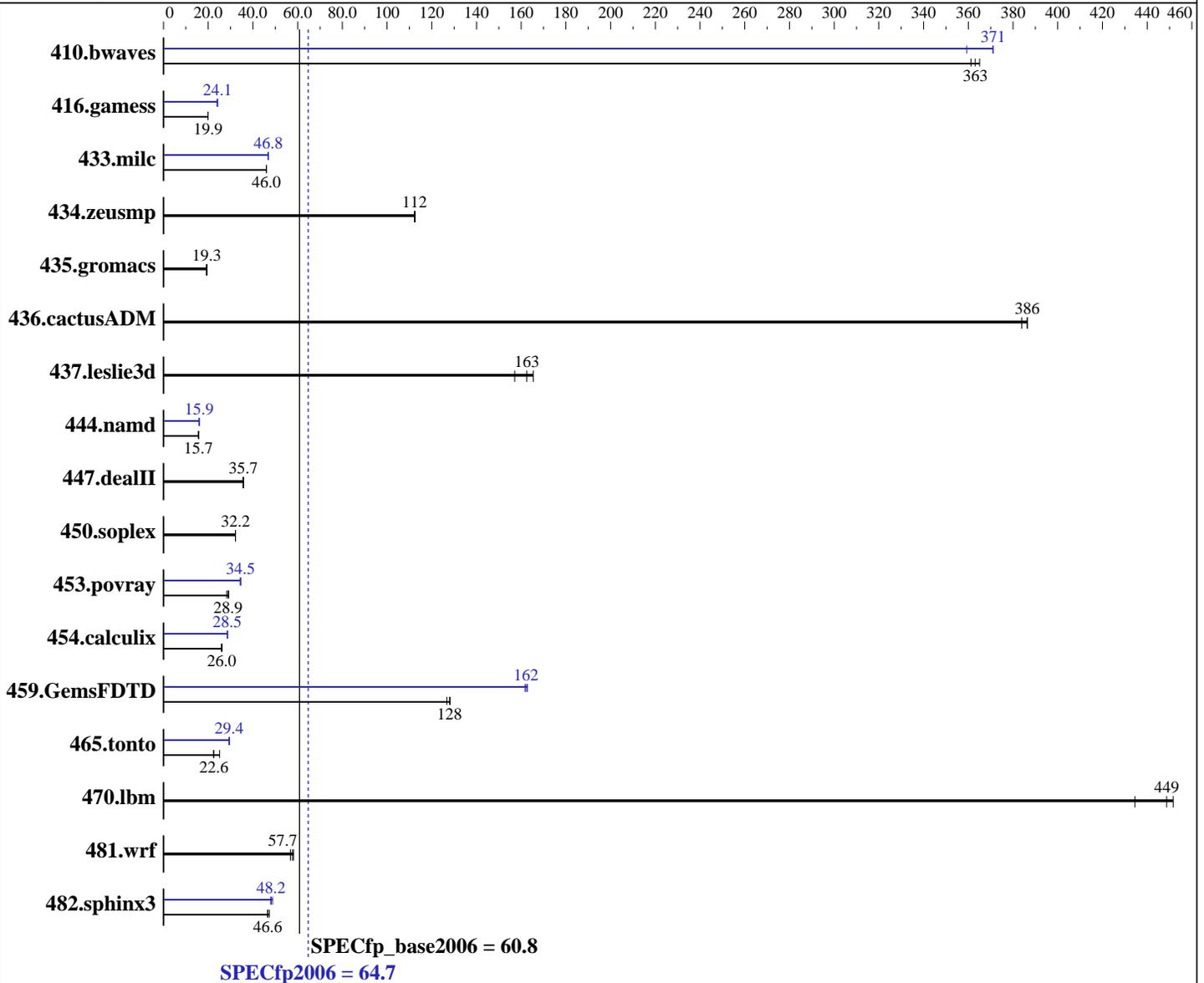
Test sponsor: Huawei

Tested by: Huawei

Test date: Apr-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-2650L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.30 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 X 300 GB 10000 RPM SAS  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b><u>37.4</u></b>	<b><u>363</u></b>	37.6	361	37.2	365	37.8	359	36.6	371	<b><u>36.6</u></b>	<b><u>371</u></b>
416.gamess	982	19.9	984	19.9	<b><u>983</u></b>	<b><u>19.9</u></b>	808	24.2	819	23.9	<b><u>811</u></b>	<b><u>24.1</u></b>
433.milc	<b><u>200</u></b>	<b><u>46.0</u></b>	199	46.0	200	46.0	196	46.8	196	46.8	<b><u>196</u></b>	<b><u>46.8</u></b>
434.zeusmp	<b><u>81.0</u></b>	<b><u>112</u></b>	81.0	112	80.8	113	<b><u>81.0</u></b>	<b><u>112</u></b>	81.0	112	80.8	113
435.gromacs	<b><u>369</u></b>	<b><u>19.3</u></b>	373	19.2	368	19.4	<b><u>369</u></b>	<b><u>19.3</u></b>	373	19.2	368	19.4
436.cactusADM	30.9	386	31.1	384	<b><u>30.9</u></b>	<b><u>386</u></b>	30.9	386	31.1	384	<b><u>30.9</u></b>	<b><u>386</u></b>
437.leslie3d	<b><u>57.8</u></b>	<b><u>163</u></b>	56.8	165	59.8	157	<b><u>57.8</u></b>	<b><u>163</u></b>	56.8	165	59.8	157
444.namd	<b><u>511</u></b>	<b><u>15.7</u></b>	511	15.7	511	15.7	503	16.0	<b><u>503</u></b>	<b><u>15.9</u></b>	503	15.9
447.dealII	320	35.8	322	35.6	<b><u>321</u></b>	<b><u>35.7</u></b>	320	35.8	322	35.6	<b><u>321</u></b>	<b><u>35.7</u></b>
450.soplex	259	32.2	259	32.2	<b><u>259</u></b>	<b><u>32.2</u></b>	259	32.2	259	32.2	<b><u>259</u></b>	<b><u>32.2</u></b>
453.povray	<b><u>184</u></b>	<b><u>28.9</u></b>	182	29.2	188	28.3	<b><u>154</u></b>	<b><u>34.5</u></b>	155	34.3	154	34.6
454.calculix	314	26.2	319	25.9	<b><u>317</u></b>	<b><u>26.0</u></b>	287	28.7	289	28.5	<b><u>289</u></b>	<b><u>28.5</u></b>
459.GemsFDTD	83.7	127	82.7	128	<b><u>82.9</u></b>	<b><u>128</u></b>	<b><u>65.4</u></b>	<b><u>162</u></b>	65.6	162	65.2	163
465.tonto	393	25.1	<b><u>436</u></b>	<b><u>22.6</u></b>	440	22.4	334	29.5	<b><u>335</u></b>	<b><u>29.4</u></b>	336	29.3
470.lbm	30.4	452	31.6	435	<b><u>30.6</u></b>	<b><u>449</u></b>	30.4	452	31.6	435	<b><u>30.6</u></b>	<b><u>449</u></b>
481.wrf	192	58.1	<b><u>194</u></b>	<b><u>57.7</u></b>	197	56.8	192	58.1	<b><u>194</u></b>	<b><u>57.7</u></b>	197	56.8
482.sphinx3	412	47.3	418	46.6	<b><u>418</u></b>	<b><u>46.6</u></b>	<b><u>405</u></b>	<b><u>48.2</u></b>	406	48.0	399	48.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Intel Hyper-Threading set to Disabled  
 Sysinfo program /spec/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on BH622-RH6.2 Wed Jan 26 11:20:29 2011

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Apr-2012

Hardware Availability: Mar-2012

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## Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650L 0 @ 1.80GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings  : 8
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB

From /proc/meminfo
MemTotal:      132124016 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux BH622-RH6.2 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 26 02:48

SPEC is set to: /spec
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sdal       ext4     197G  113G   75G  61% /

Additional information from dmidecode:
Memory:
 16x Samsung M393B1K70DH0-CK0 8 GB 1600 MHz 2 rank

```

## General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "16"

```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled



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## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:  
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias



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## Peak Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

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## Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revD.20120509.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revD.20120509.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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