SPEC® CINT2006 Result
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Supermicro

SuperServer 7047R-TXRF (X9DRX+-F, Intel Xeon E5-2667)

$SPECint®\_rate2006 = 517$

$SPECint\_rate\_base2006 = 493$
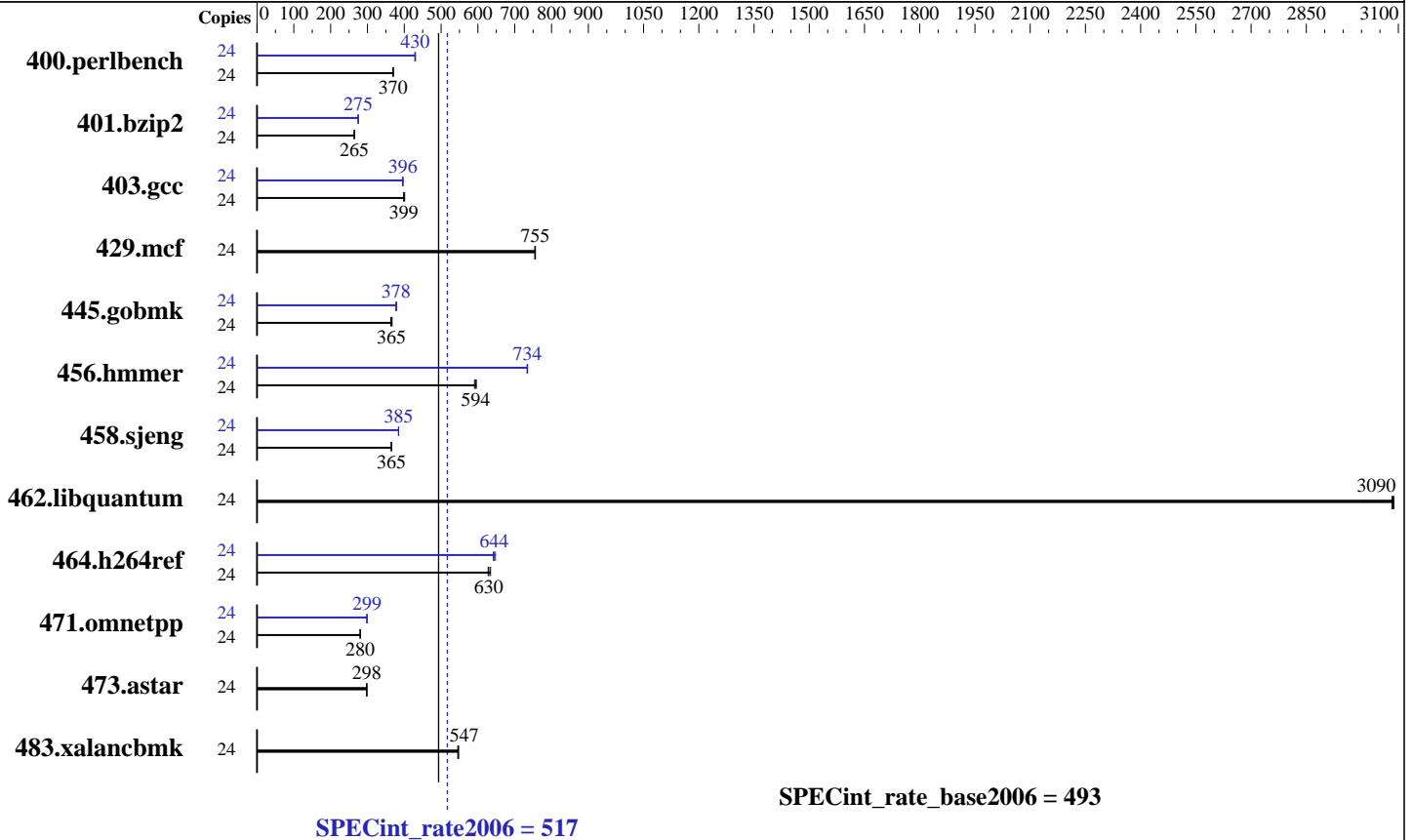
CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Mar-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011

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Hardware

CPU Name: Intel Xeon E5-2667

CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz

CPU MHz: 2900

FPU: Integrated

CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core

CPU(s) orderable: 1,2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

L3 Cache: 15 MB I+D on chip per chip

Other Cache: None

Memory: 256 GB (16 x 16 GB 2Rx4 PC3-12800R-11, ECC)

Disk Subsystem: 1 x 2 TB SATA II, 7200 RPM

Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64

Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux

Auto Parallel: No

File System: ext3

System State: Run level 3 (multi-user)

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	**634**	**370**	634	370	633	370	24	**545**	**430**	545	430	547	429
401.bzip2	24	878	264	875	265	**875**	**265**	24	842	275	843	275	**843**	**275**
403.gcc	24	482	401	**484**	**399**	485	399	24	487	397	**488**	**396**	489	395
429.mcf	24	**290**	**755**	290	755	289	756	24	**290**	**755**	290	755	289	756
445.gobmk	24	693	363	**689**	**365**	686	367	24	**667**	**378**	667	378	664	379
456.hmmer	24	**377**	**594**	376	596	379	591	24	305	734	305	735	**305**	**734**
458.sjeng	24	**796**	**365**	795	365	796	365	24	755	385	756	384	**755**	**385**
462.libquantum	24	161	3080	161	3090	**161**	**3090**	24	161	3080	161	3090	**161**	**3090**
464.h264ref	24	**843**	**630**	837	634	846	628	24	820	648	**825**	**644**	828	642
471.omnetpp	24	**535**	**280**	535	280	536	280	24	502	299	503	298	**502**	**299**
473.astar	24	564	298	**565**	**298**	566	298	24	564	298	**565**	**298**	566	298
483.xalancbmk	24	**303**	**547**	304	545	303	547	24	**303**	**547**	304	545	303	547

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>   /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

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Base Compiler Invocation

C benchmarks:
        icc -m32

C++ benchmarks:
        icpc -m32

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
        -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
        -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
        -Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
        icc -m32

    400.perlbench: icc -m64

        401.bzip2: icc -m64

      456.hmmer: icc -m64

        458.sjeng: icc -m64

C++ benchmarks:
        icpc -m32

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Peak Portability Flags

```
      400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
          401.bzip2: -DSPEC_CPU_LP64
         456.hmmer: -DSPEC_CPU_LP64
          458.sjeng: -DSPEC_CPU_LP64
     462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
      400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                     -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

          401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                     -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
                     -auto-ilp32 -ansi-alias

            403.gcc: -xAVX -ipo -O3 -no-prec-div

            429.mcf: basepeak = yes

         445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                     -ansi-alias -opt-mem-layout-trans=3

         456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

          458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                     -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
                     -auto-ilp32

     462.libquantum: basepeak = yes

        464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                     -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
                     -ansi-alias
```

C++ benchmarks:

```
        471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                     -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
                     -opt-ra-region-strategy=block -Wl,-z,muldefs
                     -L/smartheap -lsmartheap

           473.astar: basepeak = yes

      483.xalancbmk: basepeak = yes
```

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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