



# SPEC® CINT2006 Result

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## Supermicro

### SPECint®\_rate2006 = 55.0

### SuperServer 5017C-MF (X9SCL-F, Intel G530)

### SPECint\_rate\_base2006 = 52.8

CPU2006 license: 001176

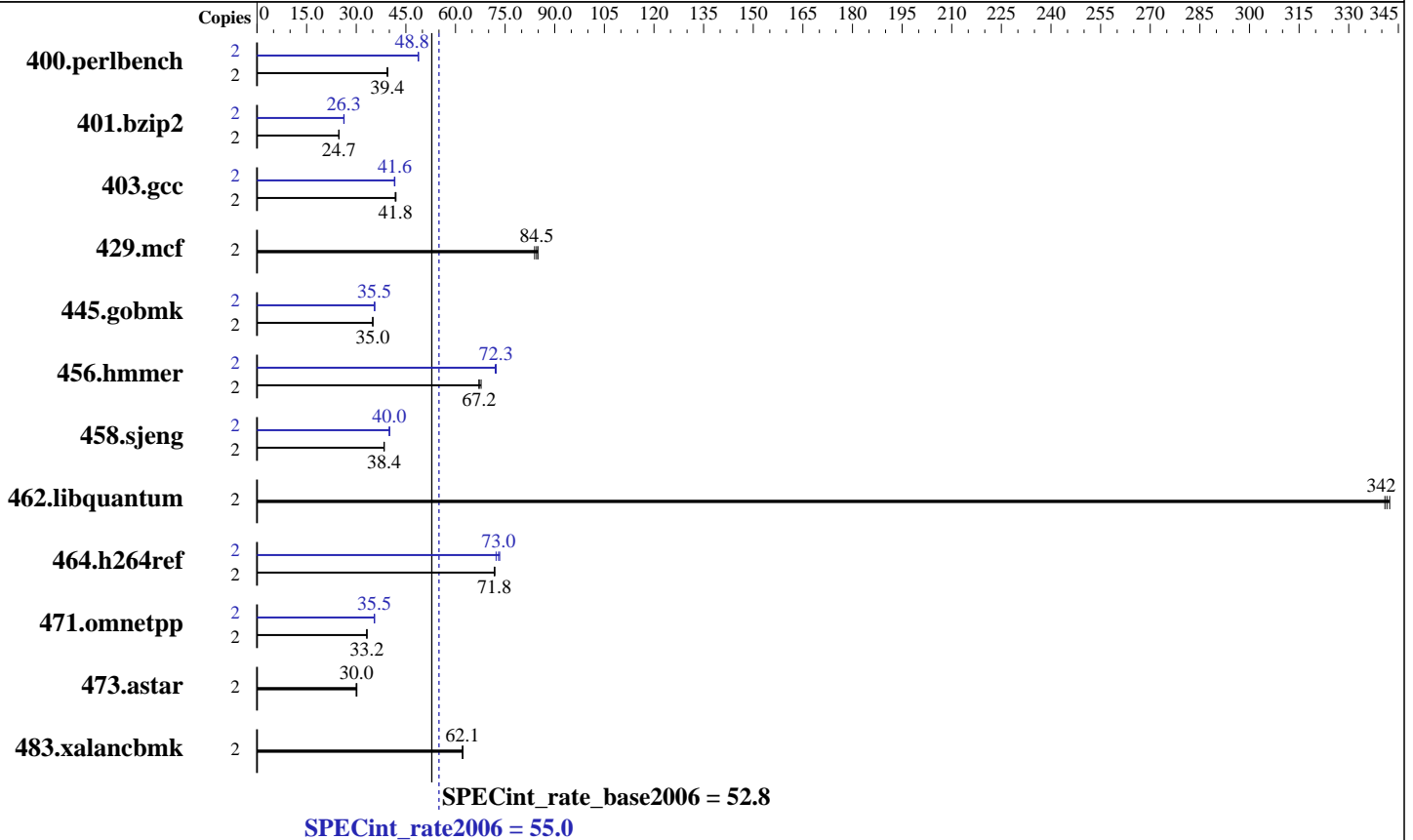
Test date: Mar-2012

Test sponsor: Supermicro

Hardware Availability: Apr-2011

Tested by: Supermicro

Software Availability: Oct-2011



### Hardware

CPU Name: Intel Celeron G530  
 CPU Characteristics:  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 2 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC, running at 1066 MHz and CL7)  
 Disk Subsystem: 1 x 1 TB SATA II, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)  
 2.6.32-131.0.15.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	<b>496</b>	<b>39.4</b>	495	39.5	496	39.4	2	<b>401</b>	<b>48.8</b>	401	48.8	400	48.8
401.bzip2	2	778	24.8	780	24.7	<b>780</b>	<b>24.7</b>	2	<b>734</b>	<b>26.3</b>	736	26.2	733	26.3
403.gcc	2	383	42.0	<b>385</b>	<b>41.8</b>	385	41.8	2	388	41.5	<b>387</b>	<b>41.6</b>	386	41.7
429.mcf	2	215	84.9	<b>216</b>	<b>84.5</b>	217	83.9	2	215	84.9	<b>216</b>	<b>84.5</b>	217	83.9
445.gobmk	2	599	35.0	<b>599</b>	<b>35.0</b>	600	35.0	2	590	35.6	<b>591</b>	<b>35.5</b>	591	35.5
456.hammer	2	275	67.7	278	67.1	<b>278</b>	<b>67.2</b>	2	259	72.0	258	72.3	<b>258</b>	<b>72.3</b>
458.sjeng	2	<b>630</b>	<b>38.4</b>	629	38.4	630	38.4	2	<b>605</b>	<b>40.0</b>	605	40.0	604	40.1
462.libquantum	2	122	341	121	342	<b>121</b>	<b>342</b>	2	122	341	121	342	<b>121</b>	<b>342</b>
464.h264ref	2	615	71.9	<b>617</b>	<b>71.8</b>	617	71.7	2	<b>606</b>	<b>73.0</b>	603	73.4	612	72.3
471.omnetpp	2	376	33.2	<b>376</b>	<b>33.2</b>	377	33.2	2	353	35.4	<b>352</b>	<b>35.5</b>	352	35.5
473.astar	2	<b>467</b>	<b>30.0</b>	468	30.0	465	30.2	2	<b>467</b>	<b>30.0</b>	468	30.0	465	30.2
483.xalancbmk	2	222	62.2	<b>222</b>	<b>62.1</b>	222	62.1	2	222	62.2	<b>222</b>	<b>62.1</b>	222	62.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on localhost.localdomain Fri Mar 30 20:21:00 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Celeron(R) CPU G530 @ 2.40GHz  
1 "physical id"s (chips)  
2 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 2  
siblings : 2

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### Platform Notes (Continued)

physical 0: cores 0 1  
cache size : 2048 KB

From /proc/meminfo  
MemTotal: 8040724 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
Red Hat Enterprise Linux Server release 6.1 (Santiago)

From /etc/\*release\* /etc/\*version\*  
redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)  
system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:  
Linux localhost.localdomain 2.6.32-131.0.15.el6.x86\_64 #1 SMP Tue May 10  
15:42:40 EDT 2011 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Mar 30 20:12

SPEC is set to: /usr/cpu2006  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/VolGroup-lv\_root  
ext4 50G 35G 13G 75% /

Additional information from dmidecode:

Memory:  
2x Micron 18JSF51272AZ-1G4D1 4 GB 1067 MHz 2 rank

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

### Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
400.perlbench: icc -m64  
401.bzip2: icc -m64  
456.hmmer: icc -m64  
458.sjeng: icc -m64  
C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/smartheap -lsmartheap

473.astar: basepeak = yes

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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