SPEC® CFP2006 Result

Fujitsu

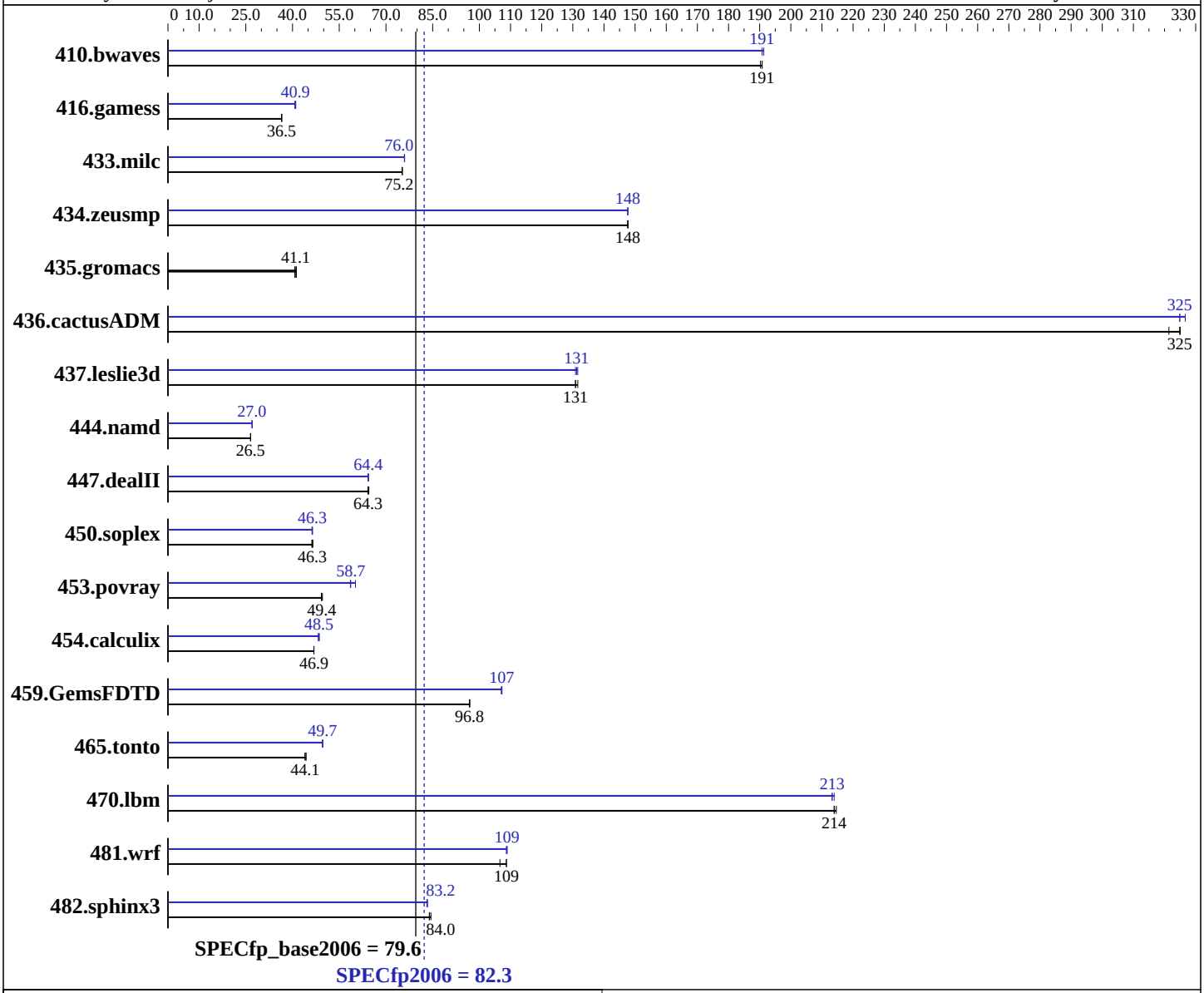
CELSIUS M720 (Intel Xeon E5-1660)

SPECfp®2006 = 82.3

SPECfp_base2006 = 79.6

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2012
Hardware Availability: Mar-2012
Software Availability: Feb-2012



Hardware

CPU Name:	Intel Xeon E5-1660
CPU Characteristics:	Intel Turbo Boost Technology up to 3.90 GHz
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	6 cores, 1 chip, 6 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.2, 2.6.32-220.0.el6.x86_64
Compiler:	C/C++: Version 12.1.3.293 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.3.293 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi - user)

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (8 x 4 GB 2Rx8 PC3-12800E-11, ECC)
Disk Subsystem: 1 x SATA III, 500 GB, 7200 rpm
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	71.2	191	**71.2**	**191**	71.4	190	71.0	191	71.2	191	**71.2**	**191**
416.gamess	536	36.5	537	36.5	**536**	**36.5**	**478**	**40.9**	477	41.0	480	40.7
433.milc	122	75.3	122	75.2	**122**	**75.2**	121	76.0	**121**	**76.0**	121	75.9
434.zeusmp	**61.6**	**148**	61.6	148	61.6	148	**61.6**	**148**	61.6	148	61.6	148
435.gromacs	**174**	**41.1**	173	41.2	175	40.7	**174**	**41.1**	173	41.2	175	40.7
436.cactusADM	37.2	321	36.8	325	**36.8**	**325**	**36.8**	**325**	36.6	327	36.8	325
437.leslie3d	**71.8**	**131**	71.4	132	71.8	131	71.8	131	**71.6**	**131**	71.4	132
444.namd	302	26.5	302	26.5	**302**	**26.5**	297	27.0	**297**	**27.0**	297	27.0
447.dealII	178	64.2	177	64.5	**178**	**64.3**	178	64.4	**178**	**64.4**	178	64.3
450.soplex	179	46.5	**180**	**46.3**	181	46.2	180	46.4	180	46.3	**180**	**46.3**
453.povray	**108**	**49.4**	107	49.6	108	49.3	90.8	58.6	**90.6**	**58.7**	88.4	60.2
454.calculix	176	46.9	176	46.9	**176**	**46.9**	170	48.6	171	48.2	**170**	**48.5**
459.GemsFDTD	109	96.9	**110**	**96.8**	110	96.8	**98.9**	**107**	99.1	107	98.9	107
465.tonto	**223**	**44.1**	222	44.4	224	44.0	**198**	**49.7**	198	49.6	198	49.7
470.lbm	**64.2**	**214**	64.0	215	64.2	214	64.4	213	64.2	214	**64.4**	**213**
481.wrf	**103**	**109**	105	107	103	109	103	109	103	109	**103**	**109**
482.sphinx3	232	83.9	**232**	**84.0**	231	84.4	234	83.3	234	83.1	**234**	**83.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS settings:
Hyper-Threading Technology = Disabled
Frequency Floor Override = Enabled
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/work/cpu2006/libs/32:/work/cpu2006/libs/64"
```

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General Notes (Continued)

```
OMP_NUM_THREADS = "6"

 Binaries compiled on a system with
 2x Xeon E5-2650 CPU + 64 GB memory using
 Red Hat Enterprise Linux Server release 6.2 (Santiago)
 The RPMs glibc-static-2.12-1.47.el6.x86_64.rpm
 and glibc-static-2.12-1.47.el6.i686.rpm
 were added to enable static linking.

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
     410.bwaves: -DSPEC_CPU_LP64
     416.gamess: -DSPEC_CPU_LP64
       433.milc: -DSPEC_CPU_LP64
     434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
     450.soplex: -DSPEC_CPU_LP64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
       465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -ansi-alias`

470.lbm: `-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

Continued on next page

Peak Optimization Flags (Continued)

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -static

450.soplex: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: -xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

437.leslie3d: Same as 434.zeusmp

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: -xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

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Peak Optimization Flags (Continued)

481.wrf: Same as 436.cactusADM

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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