SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

Fujitsu CELSIUS R920 (2x Intel Xeon E5-2650)

$SPECint^{®}\_rate2006 =$ 540

$SPECint\_rate\_base2006 =$ 517

CPU2006 license: 19	**Test date:**	Mar-2012
Test sponsor: Fujitsu	**Hardware Availability:**	Mar-2012
Tested by: Fujitsu	**Software Availability:**	Dec-2011

Copies 0 100 200 300 400 500 600 700 800 900 1050 1200 1350 1500 1650 1800 1950 2100 2250 2400 2550 2700 2850 3150

400.perlbench 32 438 / 32 372

401.bzip2 32 293 / 32 284

403.gcc 32 419 / 32 421

429.mcf 32 835

445.gobmk 32 390 / 32 378

456.hmmer 32 768 / 32 639

458.sjeng 32 390 / 32 376

462.libquantum 32 3110 / 32 3050

464.h264ref 32 645 / 32 638

471.omnetpp 32 340 / 32 319

473.astar 32 308 / 32 306

483.xalancbmk 32 559 / 32 565

SPECint_rate_base2006 = 517

SPECint_rate2006 = 540

Hardware

CPU Name:	Intel Xeon E5-2650
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem:	1 x SATA III, 500 GB, 7200 rpm
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.1, 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi - user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap 10 (Multi-Core)

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

Fujitsu CELSIUS R920 (2x Intel Xeon E5-2650)

SPECint_rate2006 =   540
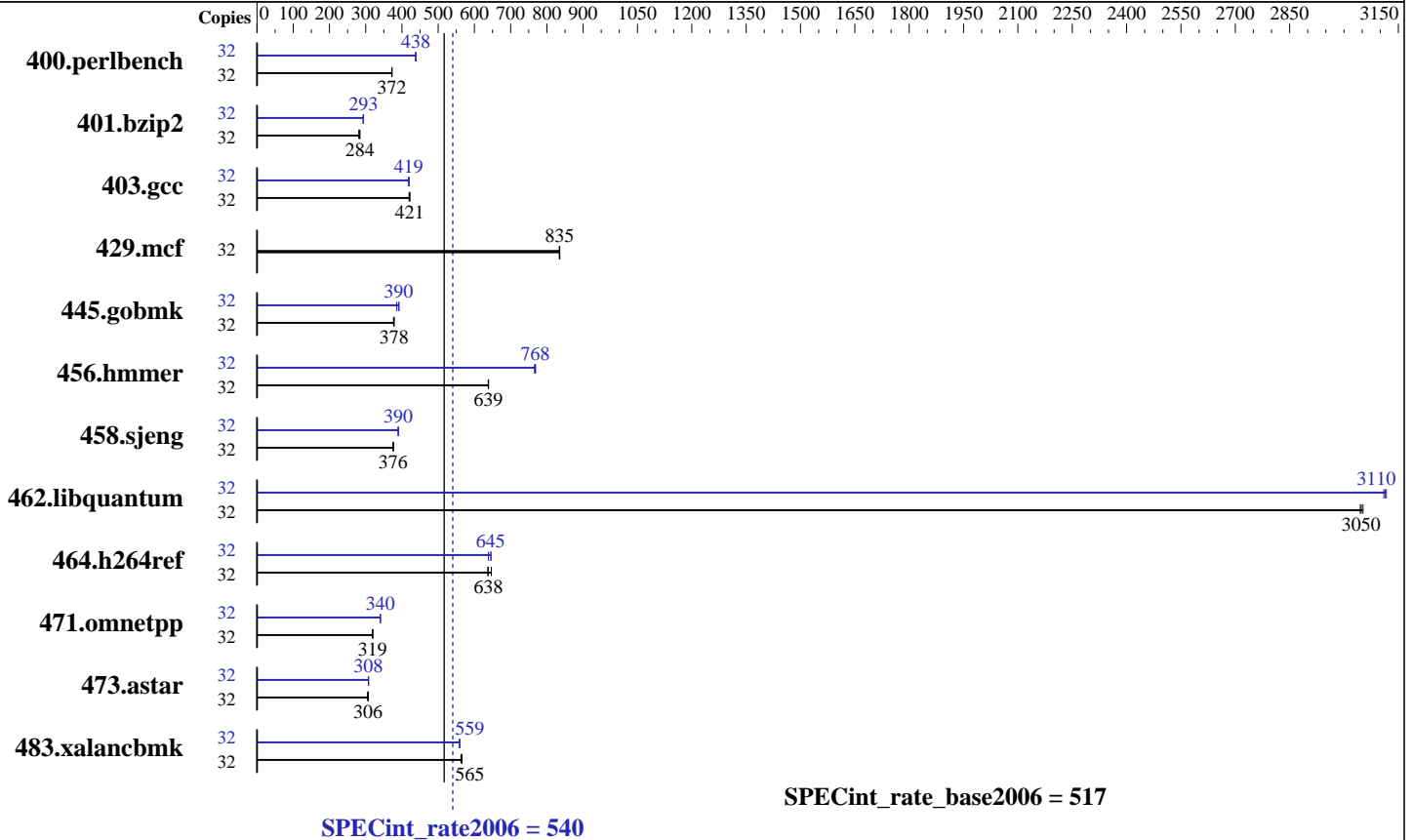
SPECint_rate_base2006 = 517

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	840	372	**840**	**372**	839	373	32	714	438	**713**	**438**	713	439
401.bzip2	32	1088	284	1099	281	**1089**	**284**	32	1052	293	1054	293	**1052**	**293**
403.gcc	32	610	422	**612**	**421**	614	420	32	613	420	615	419	**615**	**419**
429.mcf	32	**349**	**835**	349	836	350	834	32	**349**	**835**	349	836	350	834
445.gobmk	32	886	379	**888**	**378**	891	377	32	871	385	856	392	**860**	**390**
456.hmmer	32	467	639	468	638	**467**	**639**	32	390	766	**389**	**768**	388	769
458.sjeng	32	1031	376	**1030**	**376**	1028	377	32	995	389	991	391	**993**	**390**
462.libquantum	32	217	3050	**218**	**3050**	218	3050	32	213	3110	**213**	**3110**	213	3120
464.h264ref	32	1095	647	1112	637	**1109**	**638**	32	1108	639	1097	646	**1098**	**645**
471.omnetpp	32	**627**	**319**	626	319	627	319	32	587	340	**588**	**340**	588	340
473.astar	32	**734**	**306**	736	305	731	307	32	**730**	**308**	731	307	729	308
483.xalancbmk	32	390	566	**391**	**565**	392	563	32	**395**	**559**	395	559	395	559

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit'
    was used to generate numactl commands to bind each copy to a specific processor.
    For details, please see the config file.
    For full details on using numactl, please refer to your Linux documentation, 'man numactl'
```

Operating System Notes

```
    Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
    BIOS settings:
    Frequency Floor Overright = Enabled
```

General Notes

```
    Environment variables set by runspec before the start of the run:
    LD_LIBRARY_PATH = "/work/cpu2006/libs/32:/work/cpu2006/libs/64"

    Binaries compiled on a system with
    2x Xeon E5-2690 CPU + 64 GB memory using
    Red Hat Enterprise Linux Server release 6.1 (Santiago)

    Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
    Filesystem page cache cleared with:
```

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

Fujitsu CELSIUS R920 (2x Intel Xeon E5-2650)

SPECint_rate2006 =   540

SPECint_rate_base2006 = 517

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011

General Notes (Continued)

```
echo 1>        /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:
```
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc -m64
```

```
        403.gcc: icc  -m32
```

```
        429.mcf: icc  -m32
```

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

Fujitsu CELSIUS R920 (2x Intel Xeon E5-2650)

SPECint_rate2006 =   540

SPECint_rate_base2006 = 517

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011

Peak Compiler Invocation (Continued)

    445.gobmk: `icc  -m32`

 462.libquantum: `icc -m32`

   464.h264ref: `icc  -m32`

C++ benchmarks:
   `icpc -m32`

Peak Portability Flags

    400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
        401.bzip2: `-DSPEC_CPU_LP64`
       456.hmmer: `-DSPEC_CPU_LP64`
        458.sjeng: `-DSPEC_CPU_LP64`
   462.libquantum: `-DSPEC_CPU_LINUX`
   483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                 `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                 `-auto-ilp32`

       401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                 `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                 `-opt-prefetch -auto-ilp32 -ansi-alias`

         403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div`

         429.mcf: `basepeak = yes`

      445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
                 `-ansi-alias -opt-mem-layout-trans=3`

       456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

       458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                 `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                 `-unroll4 -auto-ilp32`

   462.libquantum: `-xAVX -ipo -O3 -no-prec-div -opt-prefetch`
                 `-opt-mem-layout-trans=3`

Fujitsu

Fujitsu CELSIUS R920 (2x Intel Xeon E5-2650)

SPECint_rate2006 =   540

SPECint_rate_base2006 = 517

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011

Peak Optimization Flags (Continued)

464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
`-L/opt/SmartHeap/lib -lsmartheap`

473.astar: `-xAVX -ipo -O3 -no-prec-div -opt-prefetch`
`-opt-mem-layout-trans=3 -Wl,-z,muldefs`
`-L/opt/SmartHeap/lib -lsmartheap`

483.xalancbmk: `Same as 473.astar`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 04:16:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 April 2012.