SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 6027R-WRF (X9DRW-iF, Intel E5-2670)

SPECint®_rate2006 = 644

SPECint_rate_base2006 = 615

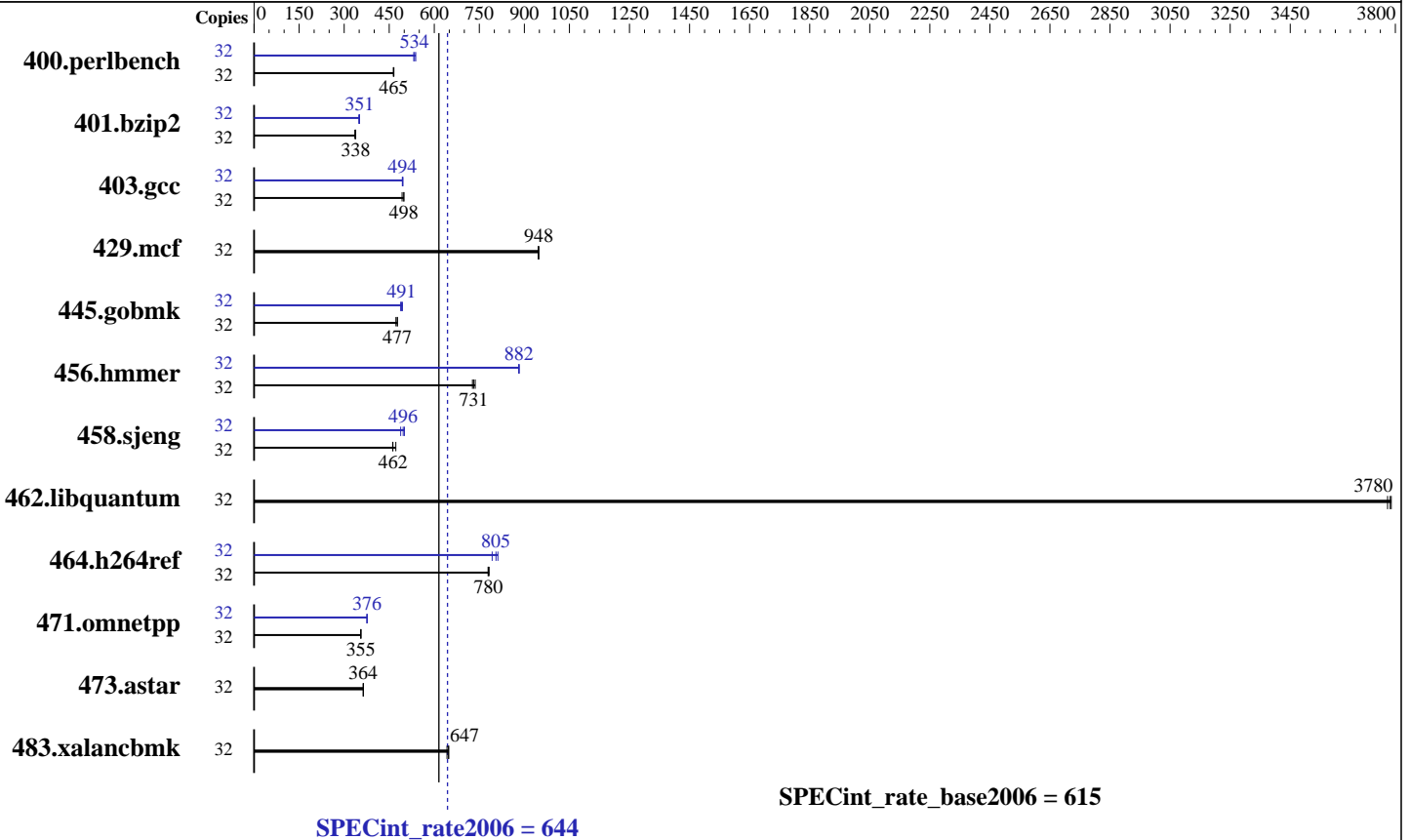**CPU2006 license:** 001176　　　　　　　　　　　　　　　　　　　　**Test date:** Mar-2012
Test sponsor: Supermicro　　　　　　　　　　　　　　　　**Hardware Availability:** Mar-2011
Tested by: Supermicro　　　　　　　　　　　　　　　　**Software Availability:** Dec-2011

SPECint_rate_base2006 = 615

SPECint_rate2006 = 644

Hardware

CPU Name:	Intel Xeon E5-2670
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem:	1 x 1 TB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 6027R-WRF (X9DRW-iF, Intel E5-2670)

SPECint_rate2006 =　　644

SPECint_rate_base2006 = 615

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date:　Mar-2012
Hardware Availability: Mar-2011
Software Availability: Dec-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	674	464	672	466	**673**	**465**	32	581	538	588	532	**585**	**534**
401.bzip2	32	**914**	**338**	913	338	920	335	32	**880**	**351**	886	349	879	351
403.gcc	32	**517**	**498**	523	493	516	499	32	**521**	**494**	522	494	520	496
429.mcf	32	309	946	**308**	**948**	308	948	32	309	946	**308**	**948**	308	948
445.gobmk	32	**704**	**477**	711	472	704	477	32	**684**	**491**	688	488	679	494
456.hmmer	32	405	736	411	727	**408**	**731**	32	338	883	**339**	**882**	339	881
458.sjeng	32	839	461	**837**	**462**	820	472	32	773	501	**780**	**496**	793	488
462.libquantum	32	176	3770	175	3790	**175**	**3780**	32	176	3770	175	3790	**175**	**3780**
464.h264ref	32	909	779	**908**	**780**	905	783	32	893	793	**879**	**805**	872	812
471.omnetpp	32	562	356	563	355	**563**	**355**	32	**532**	**376**	532	376	532	376
473.astar	32	617	364	**618**	**364**	618	364	32	617	364	**618**	**364**	618	364
483.xalancbmk	32	**341**	**647**	344	642	341	648	32	**341**	**647**	344	642	341	648

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/test/cpu2006/libs/32:/home/test/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>   /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 6027R-WRF (X9DRW-iF, Intel E5-2670)

SPECint_rate2006 =   644

SPECint_rate_base2006 = 615

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2012
Hardware Availability: Mar-2011
Software Availability: Dec-2011

Base Compiler Invocation

C benchmarks:
        icc -m32

C++ benchmarks:
        icpc -m32

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
        -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
        -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
        -Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
        icc -m32

    400.perlbench: icc -m64

        401.bzip2: icc -m64

     456.hmmer: icc -m64

       458.sjeng: icc -m64

C++ benchmarks:
        icpc -m32

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 6027R-WRF (X9DRW-iF, Intel E5-2670)

SPECint_rate2006 = 644

SPECint_rate_base2006 = 615

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2012
Hardware Availability: Mar-2011
Software Availability: Dec-2011

Peak Portability Flags

```
   400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
       401.bzip2: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
       458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LINUX
   483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
   400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                  -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

       401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                  -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
                  -auto-ilp32 -ansi-alias

         403.gcc: -xAVX -ipo -O3 -no-prec-div

         429.mcf: basepeak = yes

       445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                  -ansi-alias -opt-mem-layout-trans=3

      456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

       458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                  -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
                  -auto-ilp32

   462.libquantum: basepeak = yes

      464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
                   -ansi-alias
```

C++ benchmarks:

```
      471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
                   -opt-ra-region-strategy=block -Wl,-z,muldefs
                   -L/smartheap -lsmartheap

        473.astar: basepeak = yes

   483.xalancbmk: basepeak = yes
```

Supermicro

SuperServer 6027R-WRF (X9DRW-iF, Intel E5-2670)

SPECint_rate2006 =   644

SPECint_rate_base2006 = 615

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2012
Hardware Availability: Mar-2011
Software Availability: Dec-2011

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml

SPEC and SPECint are registered trademarks of the Standard Performance
Evaluation Corporation. All other brand and product names appearing in
this result are trademarks or registered trademarks of their respective
holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 07:34:46 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 April 2012.