



# SPEC® CINT2006 Result

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## Supermicro

SuperServer 5037MC-H8TRF (X9SCD-F single node,  
Intel i3-2120)

**SPECint\_rate2006 = 90.7**

**SPECint\_rate\_base2006 = 86.6**

CPU2006 license: 001176

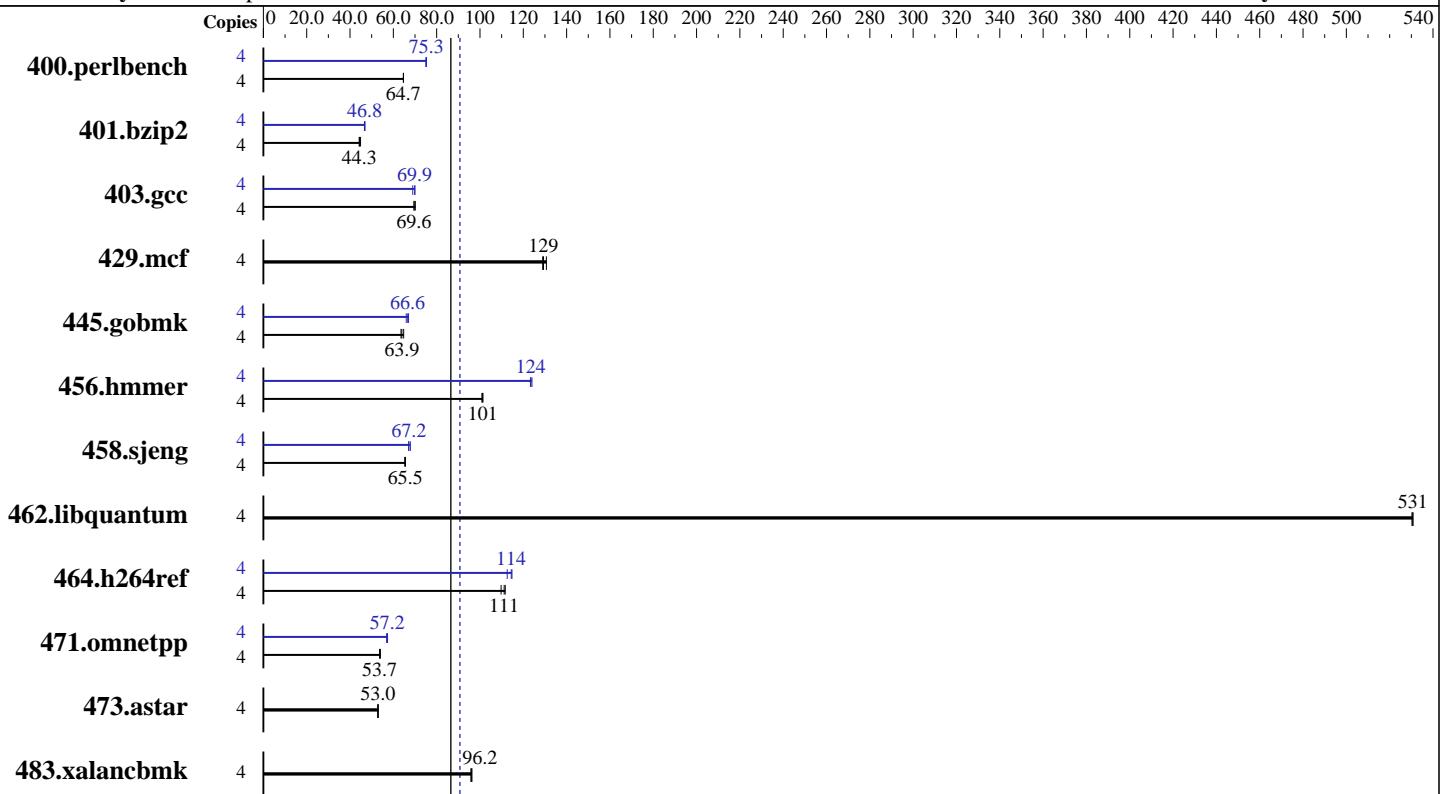
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Mar-2012

Hardware Availability: Aug-2011

Software Availability: Oct-2011



**SPECint\_rate\_base2006 = 86.6**

**SPECint\_rate2006 = 90.7**

### Hardware

CPU Name:	Intel Core i3-2120
CPU Characteristics:	
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	3 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
Disk Subsystem:	1 x 500 GB SATA III, 7200 RPM
Other Hardware:	None

### Software

Operating System:	Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>604</b>	<b>64.7</b>	605	64.6	604	64.7	4	521	75.0	519	75.3	<b>519</b>	<b>75.3</b>
401.bzip2	4	860	44.9	<b>870</b>	<b>44.3</b>	872	44.2	4	827	46.7	823	46.9	<b>825</b>	<b>46.8</b>
403.gcc	4	459	70.2	463	69.5	<b>463</b>	<b>69.6</b>	4	467	69.0	460	69.9	<b>461</b>	<b>69.9</b>
429.mcf	4	<b>282</b>	<b>129</b>	283	129	279	131	4	<b>282</b>	<b>129</b>	283	129	279	131
445.gobmk	4	<b>656</b>	<b>63.9</b>	660	63.5	648	64.8	4	626	67.0	<b>630</b>	<b>66.6</b>	636	66.0
456.hammer	4	368	101	<b>369</b>	<b>101</b>	370	101	4	<b>302</b>	<b>124</b>	303	123	301	124
458.sjeng	4	<b>739</b>	<b>65.5</b>	741	65.3	738	65.6	4	722	67.1	713	67.9	<b>721</b>	<b>67.2</b>
462.libquantum	4	156	530	<b>156</b>	<b>531</b>	156	531	4	156	530	<b>156</b>	<b>531</b>	156	531
464.h264ref	4	793	112	807	110	<b>796</b>	<b>111</b>	4	786	113	771	115	<b>774</b>	<b>114</b>
471.omnetpp	4	<b>465</b>	<b>53.7</b>	466	53.7	463	54.0	4	<b>437</b>	<b>57.2</b>	436	57.3	439	56.9
473.astar	4	<b>530</b>	<b>53.0</b>	532	52.8	530	53.0	4	<b>530</b>	<b>53.0</b>	532	52.8	530	53.0
483.xalancbmk	4	288	95.8	287	96.2	<b>287</b>	<b>96.2</b>	4	288	95.8	287	96.2	<b>287</b>	<b>96.2</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14  
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>  
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>  
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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