



# SPEC<sup>®</sup> CFP2006 Result

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## IBM Corporation

**SPECfp<sup>®</sup>2006 = 58.1**

IBM BladeCenter HX5 (Intel Xeon E7-4870, 2.40 GHz)

**SPECfp\_base2006 = 54.3**

CPU2006 license: 11

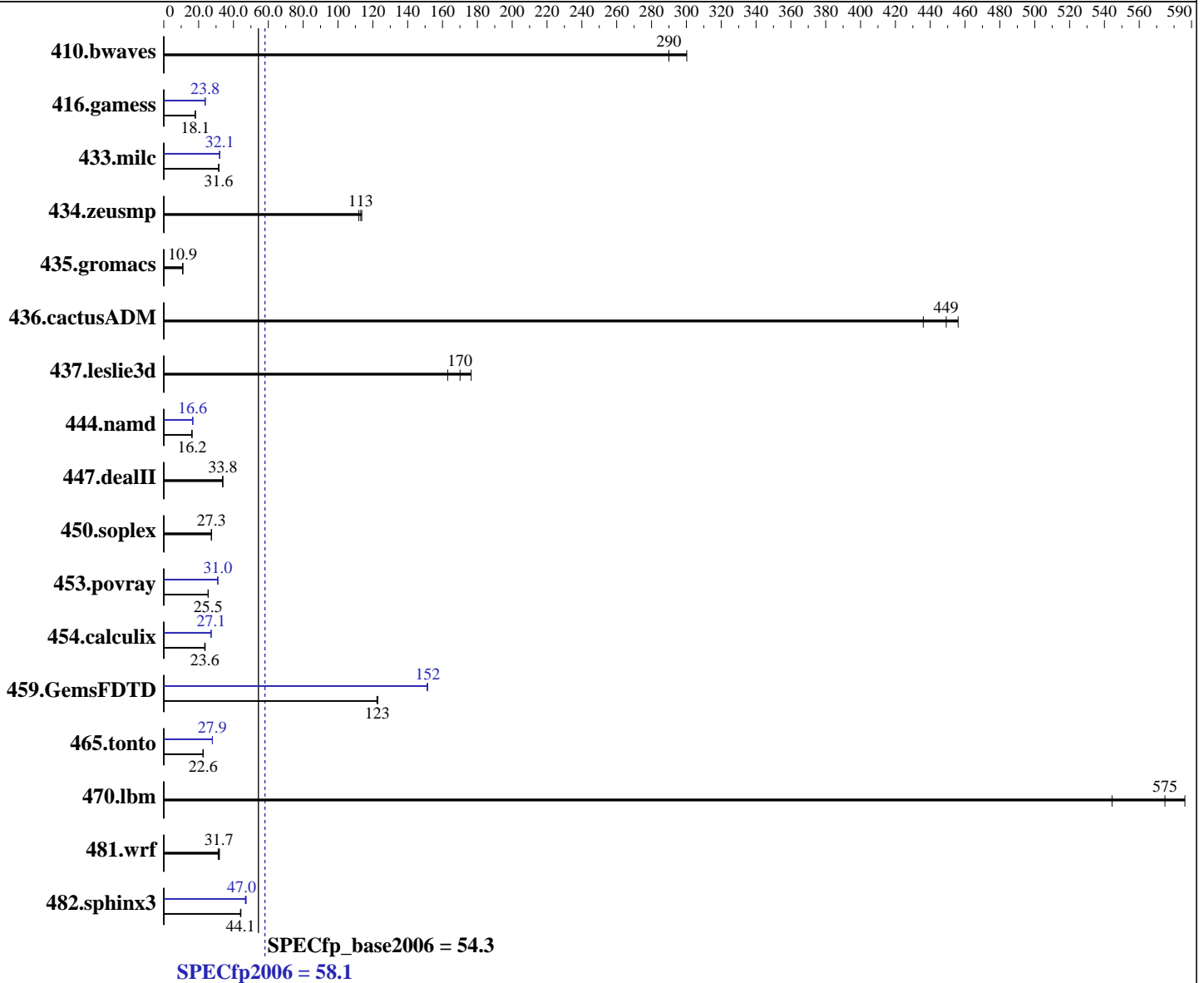
Test date: Feb-2012

Test sponsor: IBM Corporation

Hardware Availability: May-2011

Tested by: IBM Corporation

Software Availability: Dec-2011



**Hardware**

CPU Name: Intel Xeon E7-4870  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,3,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (32 x 8 GB 4Rx8 PC3-8500R-7, ECC)  
 Disk Subsystem: 4 x 50 GB SSD, RAID 0  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	46.9	290	45.3	300	<b>46.9</b>	<b>290</b>	46.9	290	45.3	300	<b>46.9</b>	<b>290</b>
416.gamess	1082	18.1	<b>1082</b>	<b>18.1</b>	1085	18.0	<b>824</b>	<b>23.8</b>	825	23.7	824	23.8
433.milc	<b>291</b>	<b>31.6</b>	291	31.5	291	31.6	286	32.1	<b>286</b>	<b>32.1</b>	287	32.0
434.zeusmp	<b>80.5</b>	<b>113</b>	81.3	112	80.1	114	<b>80.5</b>	<b>113</b>	81.3	112	80.1	114
435.gromacs	<b>656</b>	<b>10.9</b>	657	10.9	655	10.9	<b>656</b>	<b>10.9</b>	657	10.9	655	10.9
436.cactusADM	27.4	436	26.2	456	<b>26.6</b>	<b>449</b>	27.4	436	26.2	456	<b>26.6</b>	<b>449</b>
437.leslie3d	<b>55.3</b>	<b>170</b>	53.3	176	57.7	163	<b>55.3</b>	<b>170</b>	53.3	176	57.7	163
444.namd	497	16.1	<b>496</b>	<b>16.2</b>	496	16.2	483	16.6	483	16.6	<b>483</b>	<b>16.6</b>
447.dealII	<b>338</b>	<b>33.8</b>	338	33.8	338	33.8	<b>338</b>	<b>33.8</b>	338	33.8	338	33.8
450.soplex	304	27.4	305	27.3	<b>305</b>	<b>27.3</b>	304	27.4	305	27.3	<b>305</b>	<b>27.3</b>
453.povray	209	25.5	<b>209</b>	<b>25.5</b>	209	25.5	172	31.0	<b>172</b>	<b>31.0</b>	172	31.0
454.calculix	347	23.8	<b>350</b>	<b>23.6</b>	351	23.5	304	27.1	<b>304</b>	<b>27.1</b>	304	27.2
459.GemsFDTD	<b>86.5</b>	<b>123</b>	86.3	123	86.7	122	70.2	151	<b>70.0</b>	<b>152</b>	70.0	152
465.tonto	436	22.6	436	22.6	<b>436</b>	<b>22.6</b>	353	27.9	<b>353</b>	<b>27.9</b>	352	27.9
470.lbm	<b>23.9</b>	<b>575</b>	25.2	544	23.4	586	<b>23.9</b>	<b>575</b>	25.2	544	23.4	586
481.wrf	358	31.2	350	31.9	<b>352</b>	<b>31.7</b>	358	31.2	350	31.9	<b>352</b>	<b>31.7</b>
482.sphinx3	440	44.3	442	44.0	<b>442</b>	<b>44.1</b>	412	47.3	<b>415</b>	<b>47.0</b>	416	46.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Turbo Boost Power Optimization set to Traditional in BIOS  
 Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on hammerhead2n-pete Thu Feb 23 16:37:11 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

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## Platform Notes (Continued)

```

model name : Intel(R) Xeon(R) CPU E7- 4870 @ 2.40GHz
 4 "physical id"s (chips)
 80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 10
  siblings  : 20
  physical 0: cores 0 1 2 8 9 16 17 18 24 25
  physical 1: cores 0 1 2 8 9 16 17 18 24 25
  physical 2: cores 0 1 2 8 9 16 17 18 24 25
  physical 3: cores 0 1 2 8 9 16 17 18 24 25
cache size : 30720 KB

```

```

From /proc/meminfo
MemTotal:      264503032 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux hammerhead2n-pete 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST
2011 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Mar 21 15:41

```

SPEC is set to: /cpu2006.1.2
Filesystem      Type      Size Used Avail Use% Mounted on
/dev/mapper/vg_hammerhead2np-lv_root
                ext4      182G  5.8G 167G   4% /

```

```

Additional information from dmidecode:
Memory:
 32x Hynix HMT41GV7AMR8C-G7 8 GB 1067 MHz 4 rank

```

(End of data from sysinfo program)

## General Notes

```

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"
OMP_NUM_THREADS = "40"

```

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-WSM-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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