SPEC® CINT2006 Result
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Fujitsu

PRIMERGY RX350 S7, Intel Xeon E5-2630, 2.30 GHz

$SPECint^®_rate2006 = $ 436

$SPECint_rate_base2006 = $ 417
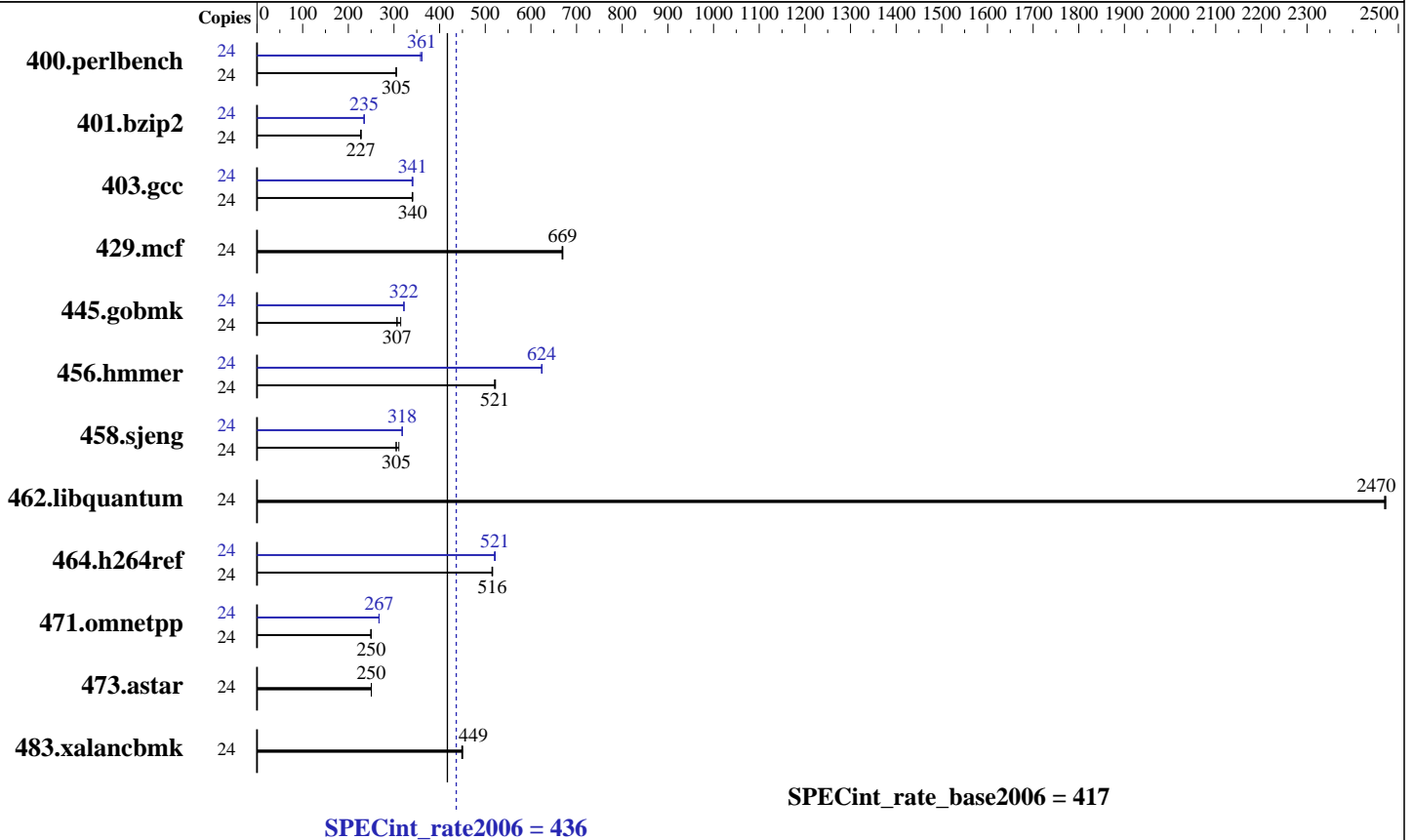
CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011

Copies	Benchmark	Peak	Base
24	400.perlbench	361	305
24	401.bzip2	235	227
24	403.gcc	341	340
24	429.mcf		669
24	445.gobmk	322	307
24	456.hmmer	624	521
24	458.sjeng	318	305
24	462.libquantum		2470
24	464.h264ref	521	516
24	471.omnetpp	267	250
24	473.astar		250
24	483.xalancbmk		449

SPECint_rate_base2006 = 417

SPECint_rate2006 = 436

Hardware

CPU Name: Intel Xeon E5-2630
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2300
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz and CL9)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
2.6.32-220.el6.x86_64
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01

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Results Table

Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
			Base								Peak			
400.perlbench	24	769	305	**_769_**	**_305_**	768	305	24	654	358	**_649_**	**_361_**	649	361
401.bzip2	24	1015	228	**_1018_**	**_227_**	1020	227	24	984	235	989	234	**_987_**	**_235_**
403.gcc	24	568	340	566	341	**_567_**	**_340_**	24	**_567_**	**_341_**	566	341	569	340
429.mcf	24	327	670	327	668	**_327_**	**_669_**	24	327	670	327	668	**_327_**	**_669_**
445.gobmk	24	**_821_**	**_307_**	821	307	800	315	24	782	322	**_782_**	**_322_**	783	322
456.hmmer	24	**_430_**	**_521_**	430	520	429	522	24	359	624	359	624	**_359_**	**_624_**
458.sjeng	24	953	305	**_952_**	**_305_**	935	311	24	**_913_**	**_318_**	914	318	912	318
462.libquantum	24	201	2470	201	2470	**_201_**	**_2470_**	24	201	2470	201	2470	**_201_**	**_2470_**
464.h264ref	24	1029	516	1032	515	**_1030_**	**_516_**	24	**_1019_**	**_521_**	1021	520	1018	522
471.omnetpp	24	**_600_**	**_250_**	599	250	602	249	24	**_561_**	**_267_**	561	267	561	267
473.astar	24	673	250	672	251	**_673_**	**_250_**	24	673	250	672	251	**_673_**	**_250_**
483.xalancbmk	24	**_369_**	**_449_**	369	449	368	450	24	**_369_**	**_449_**	369	449	368	450

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The numactl mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate numactl commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

General Notes

    Environment variables set by runspec before the start of the run:
    LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

    Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
    memory using RHEL5.5
    Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
    runspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>
    This result was measured on the PRIMERGY RX350 S7. The PRIMERGY RX350 S7
    and the PRIMERGY TX300 S7 are electronically equivalent.
    For information about Fujitsu please visit: http://www.fujitsu.com

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Base Compiler Invocation

C benchmarks:
```
icc -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap
```

Base Other Flags

C benchmarks:

```
    403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc -m32
```

```
    400.perlbench: icc -m64
```

```
    401.bzip2: icc -m64
```

```
    456.hmmer: icc -m64
```

```
    458.sjeng: icc -m64
```

C++ benchmarks:
```
icpc -m32
```

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -auto-ilp32

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32 -ansi-alias

      403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

      429.mcf: basepeak = yes

    445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -opt-mem-layout-trans=3

    456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

    458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

   464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/smartheap -lsmartheap

     473.astar: basepeak = yes
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

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