



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp[®]2006 = 73.2

PowerEdge R720 (Intel Xeon E5-2650, 2.00 GHz)

SPECfp_base2006 = 69.0

CPU2006 license: 55

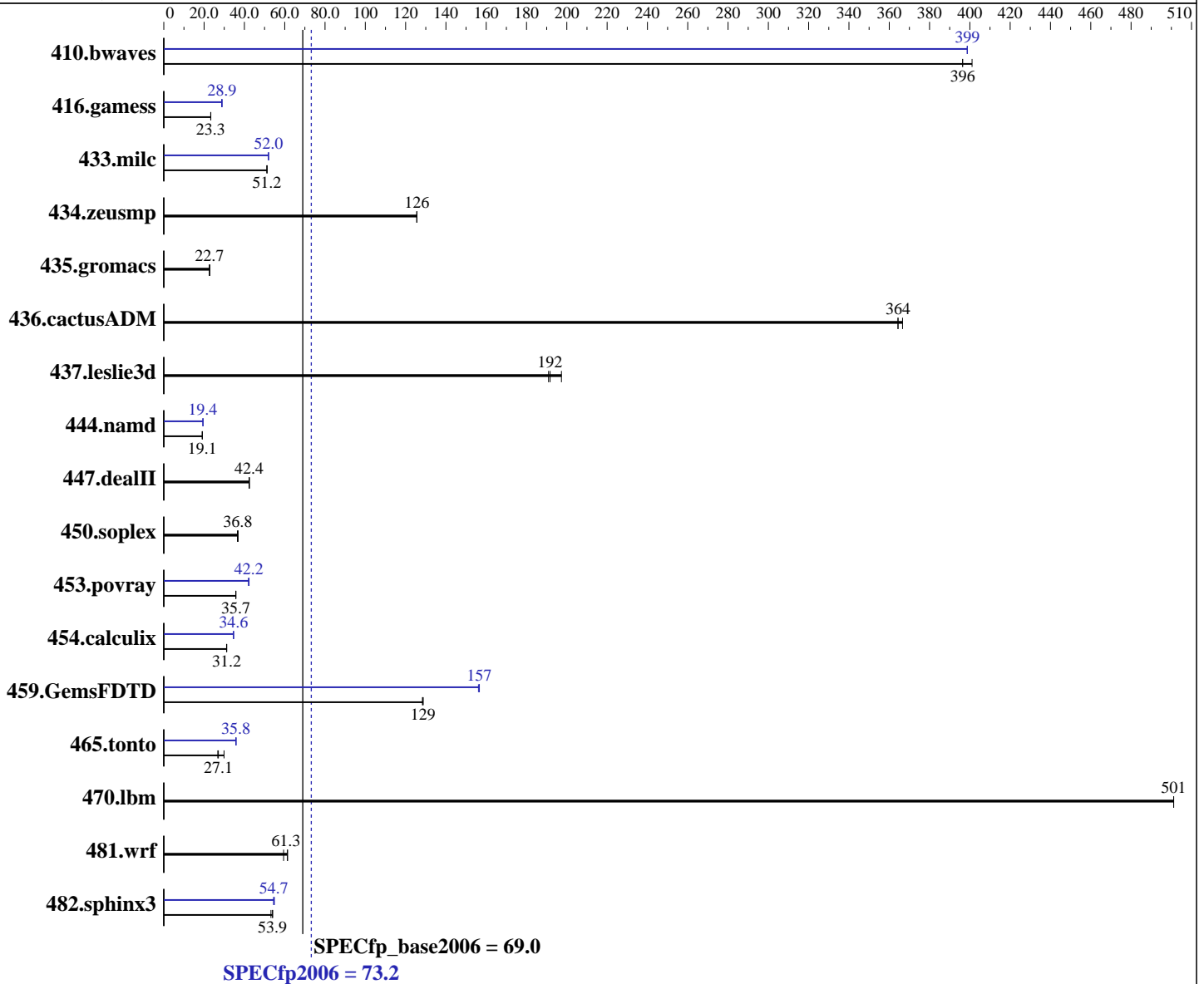
Test date: Feb-2012

Test sponsor: Dell Inc.

Hardware Availability: Mar-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



Hardware

CPU Name: Intel Xeon E5-2650
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.9-default
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (add definition here)

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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 146 GB 10000 RPM SAS
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>34.3</u>	<u>396</u>	33.9	401	34.3	396	<u>34.1</u>	<u>399</u>	34.1	399	34.1	399
416.gamess	<u>841</u>	<u>23.3</u>	841	23.3	839	23.3	<u>678</u>	<u>28.9</u>	683	28.7	677	28.9
433.milc	179	51.2	179	51.2	<u>179</u>	<u>51.2</u>	177	52.0	<u>177</u>	<u>52.0</u>	177	52.0
434.zeusmp	72.5	126	<u>72.5</u>	<u>126</u>	72.5	126	72.5	126	<u>72.5</u>	<u>126</u>	72.5	126
435.gromacs	315	22.6	<u>315</u>	<u>22.7</u>	313	22.8	315	22.6	<u>315</u>	<u>22.7</u>	313	22.8
436.cactusADM	<u>32.8</u>	<u>364</u>	32.6	367	32.8	364	<u>32.8</u>	<u>364</u>	32.6	367	32.8	364
437.leslie3d	47.6	197	49.2	191	<u>49.0</u>	<u>192</u>	47.6	197	49.2	191	<u>49.0</u>	<u>192</u>
444.namd	419	19.1	<u>420</u>	<u>19.1</u>	420	19.1	413	19.4	413	19.4	<u>413</u>	<u>19.4</u>
447.dealII	269	42.5	<u>270</u>	<u>42.4</u>	270	42.3	269	42.5	<u>270</u>	<u>42.4</u>	270	42.3
450.soplex	227	36.8	228	36.5	<u>227</u>	<u>36.8</u>	227	36.8	228	36.5	<u>227</u>	<u>36.8</u>
453.povray	149	35.7	149	35.8	<u>149</u>	<u>35.7</u>	<u>126</u>	<u>42.2</u>	126	42.3	127	42.0
454.calculix	<u>264</u>	<u>31.2</u>	264	31.2	265	31.1	239	34.6	<u>238</u>	<u>34.6</u>	238	34.6
459.GemsFDTD	82.7	128	82.5	129	<u>82.5</u>	<u>129</u>	67.8	157	68.0	156	<u>67.8</u>	<u>157</u>
465.tonto	367	26.8	<u>363</u>	<u>27.1</u>	329	29.9	274	35.9	275	35.8	<u>275</u>	<u>35.8</u>
470.lbm	27.4	501	<u>27.4</u>	<u>501</u>	27.4	501	27.4	501	<u>27.4</u>	<u>501</u>	27.4	501
481.wrf	188	59.4	182	61.4	<u>182</u>	<u>61.3</u>	188	59.4	182	61.4	<u>182</u>	<u>61.3</u>
482.sphinx3	361	54.0	367	53.2	<u>362</u>	<u>53.9</u>	<u>357</u>	<u>54.7</u>	355	54.9	358	54.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

System Profile set to Custom
 CPU Power Management set to Maximum Performance
 Memory Frequency set to Maximum Performance
 Turbo Boost set to Enabled
 C States/C1E set to Enabled
 Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6800
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
 running on linux-r2xm Wed Feb 15 17:53:48 2012

This section contains SUT (System Under Test) info as seen by
Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2650 0 @ 2.00GHz
  2 "physical id"s (chips)
  32 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 8
    siblings  : 16
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7
  cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      132122696 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2

```

```

uname -a:
Linux linux-r2xm 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012
(54ddfaf) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Feb 15 10:42 last=S

```

SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext3  133G  7.8G  124G   6% /

```

Additional information from dmidecode:

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64"
OMP_NUM_THREADS = "16"

```

The Dell PowerEdge R720 and

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General Notes (Continued)

the Bull NovaScale R460 F3 models are electronically equivalent.
 The results have been measured on a Dell PowerEdge R720 model
 Transparent Huge Pages disabled with:
 echo never > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1> /proc/sys/vm/drop_caches
 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.lelie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.xml>

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