



SPEC[®] CFP2006 Result

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Bull SAS

SPECfp[®]2006 = **84.0**

NovaScale T840 F3 (Intel Xeon E5-2670, 2.60 GHz)

SPECfp_base2006 = **79.6**

CPU2006 license: 20

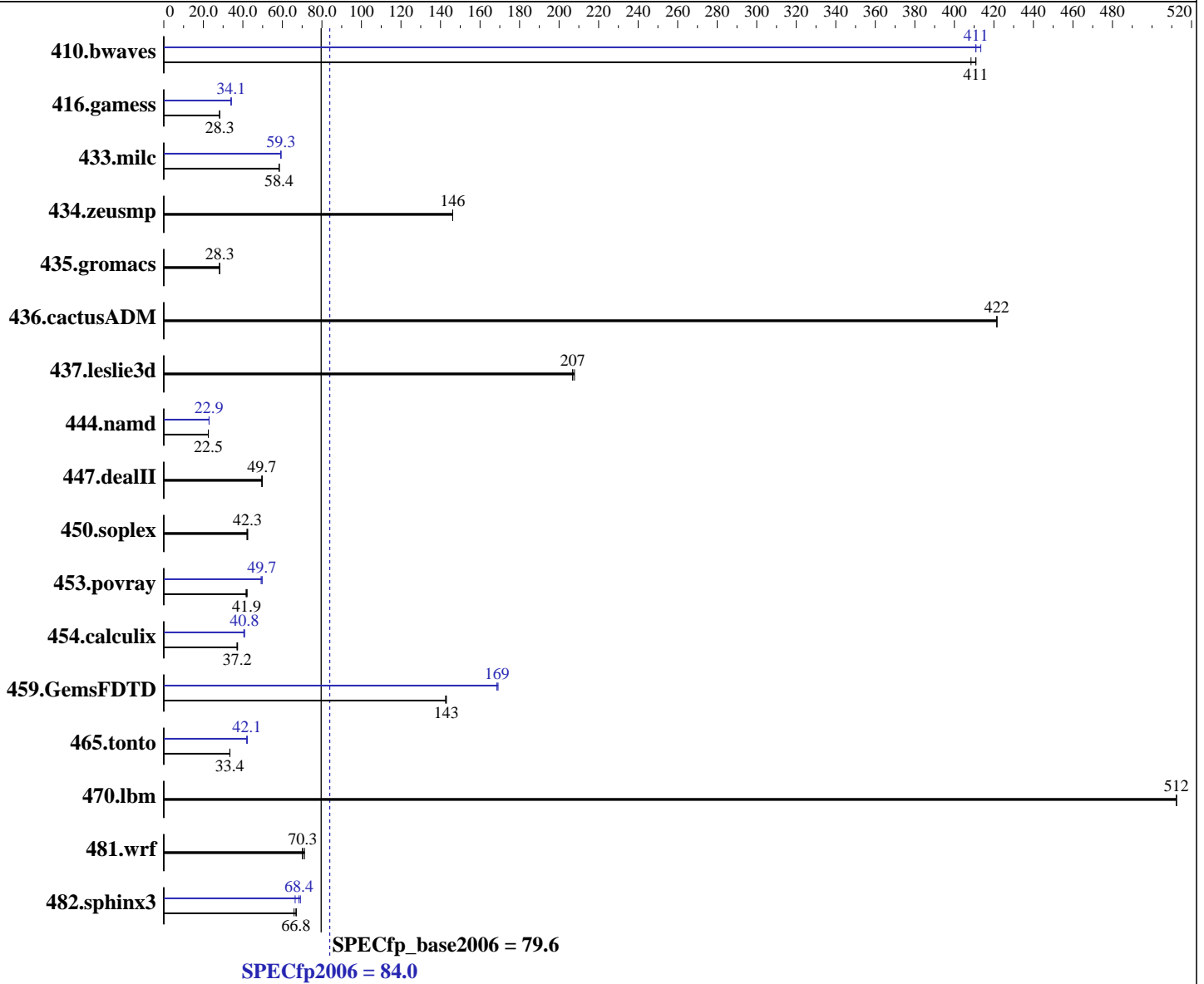
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Feb-2012

Hardware Availability: Mar-2012

Software Availability: Feb-2012



Hardware

CPU Name: Intel Xeon E5-2670
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.9-default
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 500 GB 7200 RPM SATA
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	33.3	408	33.1	411	<u>33.1</u>	<u>411</u>	<u>33.1</u>	<u>411</u>	33.1	411	32.9	413
416.gamess	692	28.3	<u>693</u>	<u>28.3</u>	694	28.2	<u>574</u>	<u>34.1</u>	576	34.0	574	34.1
433.milc	<u>157</u>	<u>58.4</u>	157	58.4	157	58.4	<u>155</u>	<u>59.3</u>	155	59.2	155	59.3
434.zeusmp	62.3	146	62.3	146	<u>62.3</u>	<u>146</u>	62.3	146	62.3	146	<u>62.3</u>	<u>146</u>
435.gromacs	253	28.2	253	28.3	<u>253</u>	<u>28.3</u>	253	28.2	253	28.3	<u>253</u>	<u>28.3</u>
436.cactusADM	28.3	422	28.4	422	<u>28.3</u>	<u>422</u>	28.3	422	28.4	422	<u>28.3</u>	<u>422</u>
437.leslie3d	45.4	207	45.2	208	<u>45.4</u>	<u>207</u>	45.4	207	45.2	208	<u>45.4</u>	<u>207</u>
444.namd	<u>356</u>	<u>22.5</u>	356	22.5	356	22.5	350	22.9	<u>350</u>	<u>22.9</u>	350	22.9
447.dealII	<u>230</u>	<u>49.7</u>	229	49.9	231	49.6	<u>230</u>	<u>49.7</u>	229	49.9	231	49.6
450.soplex	196	42.5	<u>197</u>	<u>42.3</u>	198	42.0	196	42.5	<u>197</u>	<u>42.3</u>	198	42.0
453.povray	126	42.2	<u>127</u>	<u>41.9</u>	128	41.6	108	49.2	<u>107</u>	<u>49.7</u>	107	49.7
454.calculix	221	37.4	<u>222</u>	<u>37.2</u>	223	37.0	<u>202</u>	<u>40.8</u>	202	40.8	204	40.5
459.GemsFDTD	<u>74.2</u>	<u>143</u>	74.2	143	74.4	143	62.7	169	62.9	169	<u>62.9</u>	<u>169</u>
465.tonto	295	33.4	294	33.5	<u>294</u>	<u>33.4</u>	<u>234</u>	<u>42.1</u>	233	42.2	235	41.9
470.lbm	26.8	512	<u>26.8</u>	<u>512</u>	26.8	512	26.8	512	<u>26.8</u>	<u>512</u>	26.8	512
481.wrf	<u>159</u>	<u>70.3</u>	159	70.1	157	71.2	<u>159</u>	<u>70.3</u>	159	70.1	157	71.2
482.sphinx3	290	67.1	<u>292</u>	<u>66.8</u>	296	65.9	282	69.1	293	66.5	<u>285</u>	<u>68.4</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

System Profile set to Custom
 CPU Power Management set to Maximum Performance
 Memory Frequency set to Maximum Performance
 Turbo Boost set to Enabled
 C States/C1E set to Enabled
 Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
 running on Miller Mon Feb 13 20:20:06 2012

This section contains SUT (System Under Test) info as seen by
Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2670 0 @ 2.60GHz
    2 "physical id"s (chips)
    32 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 8
    siblings  : 16
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7
  cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      132122692 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2

```

```

uname -a:
Linux Miller 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012 (54ddfaf)
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Feb 13 14:10 last=S

```

SPEC is set to: /root/CPU2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       ext3  99G   55G   39G  59% /

```

Additional information from dmidecode:

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"
OMP_NUM_THREADS = "16"
The Dell PowerEdge T620 and

```

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General Notes (Continued)

the Bull NovaScale T840 F3 models are electronically equivalent.
The results have been measured on a Dell PowerEdge T620 model
Transparent Huge Pages disabled with:
echo never > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel`

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.html>



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You can also download the XML flags sources by saving the following links:

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<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.xml>

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