



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp®2006 = 91.3

PowerEdge T620 (Intel Xeon E5-2690, 2.90 GHz)

SPECfp_base2006 = 86.2

CPU2006 license: 55

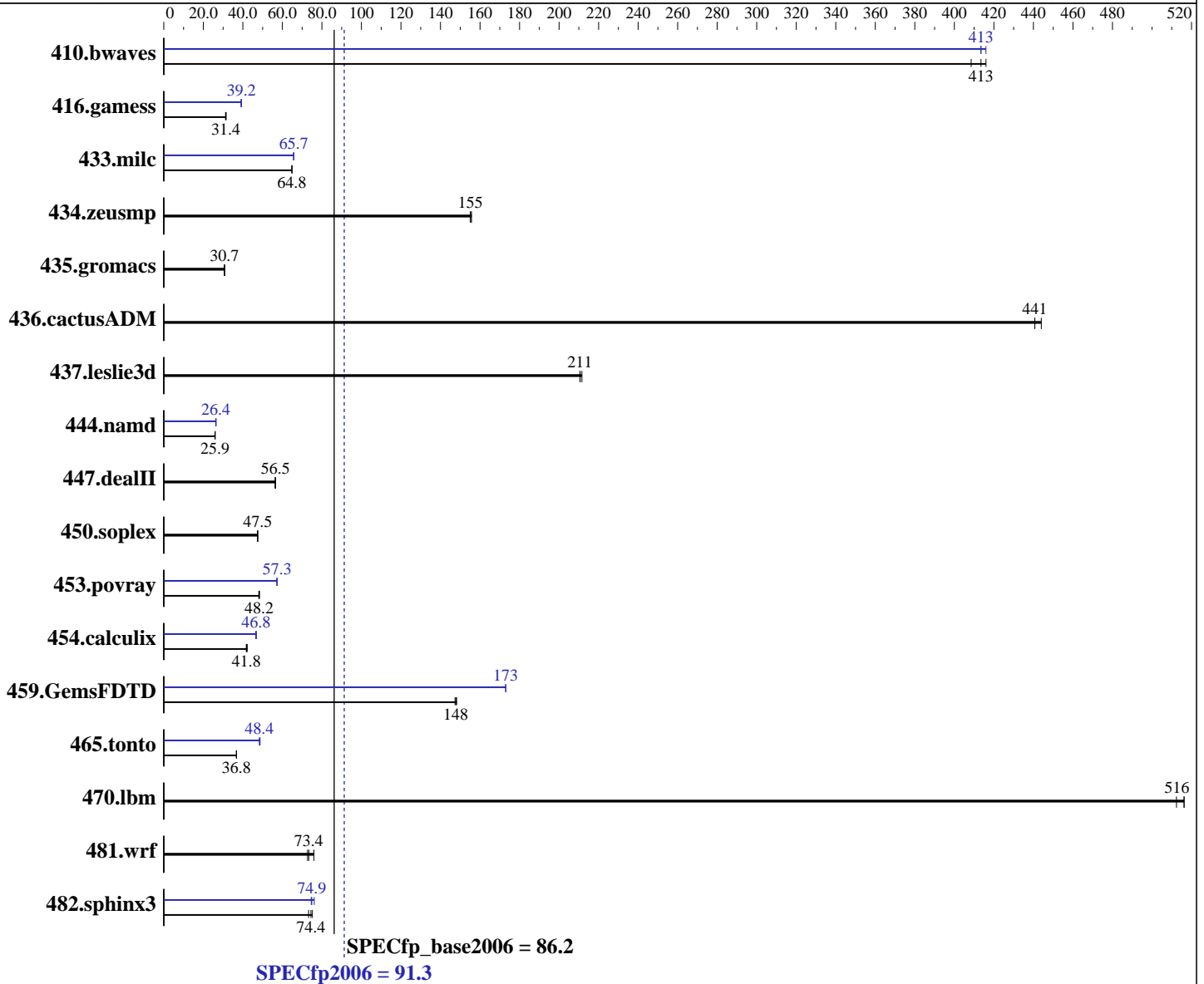
Test date: Feb-2012

Test sponsor: Dell Inc.

Hardware Availability: Mar-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



Hardware

CPU Name: Intel Xeon E5-2690
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
 CPU MHz: 2900
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 3.0.13-0.9-default
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 1 TB 7200 RPM SATA
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	33.3	409	<u>32.9</u>	<u>413</u>	32.7	416	32.9	413	<u>32.9</u>	<u>413</u>	32.7	416
416.gamess	620	31.6	<u>624</u>	<u>31.4</u>	627	31.2	<u>499</u>	<u>39.2</u>	501	39.1	499	39.3
433.milc	141	64.9	<u>142</u>	<u>64.8</u>	142	64.8	140	65.7	140	65.7	<u>140</u>	<u>65.7</u>
434.zeusmp	58.4	156	<u>58.6</u>	<u>155</u>	58.6	155	58.4	156	<u>58.6</u>	<u>155</u>	58.6	155
435.gromacs	231	30.9	233	30.6	<u>233</u>	<u>30.7</u>	231	30.9	233	30.6	<u>233</u>	<u>30.7</u>
436.cactusADM	26.9	444	27.1	441	<u>27.1</u>	<u>441</u>	26.9	444	27.1	441	<u>27.1</u>	<u>441</u>
437.leslie3d	44.4	212	<u>44.6</u>	<u>211</u>	44.6	211	44.4	212	<u>44.6</u>	<u>211</u>	44.6	211
444.namd	309	26.0	<u>309</u>	<u>25.9</u>	309	25.9	<u>304</u>	<u>26.4</u>	304	26.4	304	26.4
447.dealII	<u>203</u>	<u>56.5</u>	203	56.3	202	56.5	<u>203</u>	<u>56.5</u>	203	56.3	202	56.5
450.soplex	<u>176</u>	<u>47.5</u>	176	47.4	175	47.5	<u>176</u>	<u>47.5</u>	176	47.4	175	47.5
453.povray	<u>110</u>	<u>48.2</u>	110	48.4	111	48.1	<u>92.8</u>	<u>57.3</u>	92.8	57.3	93.0	57.2
454.calculix	195	42.2	<u>197</u>	<u>41.8</u>	198	41.7	176	46.8	<u>176</u>	<u>46.8</u>	177	46.5
459.GemsFDTD	71.6	148	<u>71.8</u>	<u>148</u>	72.0	147	<u>61.3</u>	<u>173</u>	61.3	173	61.3	173
465.tonto	267	36.8	<u>268</u>	<u>36.8</u>	268	36.7	203	48.4	<u>203</u>	<u>48.4</u>	202	48.7
470.lbm	<u>26.6</u>	<u>516</u>	26.8	512	26.6	516	<u>26.6</u>	<u>516</u>	26.8	512	26.6	516
481.wrf	147	76.0	154	72.7	<u>152</u>	<u>73.4</u>	147	76.0	154	72.7	<u>152</u>	<u>73.4</u>
482.sphinx3	259	75.2	<u>262</u>	<u>74.4</u>	266	73.3	261	74.6	<u>260</u>	<u>74.9</u>	256	76.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

System Profile set to Performance in BIOS
Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
running on linux-Sandy Fri Feb 3 00:48:13 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

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Platform Notes (Continued)

```

model name : Intel(R) Xeon(R) CPU E5-2690 0 @ 2.90GHz
 2 "physical id"s (chips)
 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings  : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      132122692 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2

```

```

uname -a:
Linux linux-Sandy 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012
(54ddfaf) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Feb 2 19:20 last=5

```

SPEC is set to: /root/CPU2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext3  197G  9.0G  178G   5% /

```

Additional information from dmidecode:

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"
OMP_NUM_THREADS = "16"

```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages disabled with:

```

echo never > /sys/kernel/mm/transparent_hugepage/enabled

```

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General Notes (Continued)

Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
The Dell PowerEdge T620 nd
the Bull NovaScale T840 F3 models are electronically equivalent.
The results have been measured on a Dell PowerEdge T620 model.

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deall: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120313.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120313.xml>



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