



SPEC® CINT2006 Result

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Acer Incorporated

(Test Sponsor: Acer Incorporation)

SPECint®_rate2006 = 141

Acer AT310 F2 (Xeon E3-1220)

SPECint_rate_base2006 = 136

CPU2006 license: 97

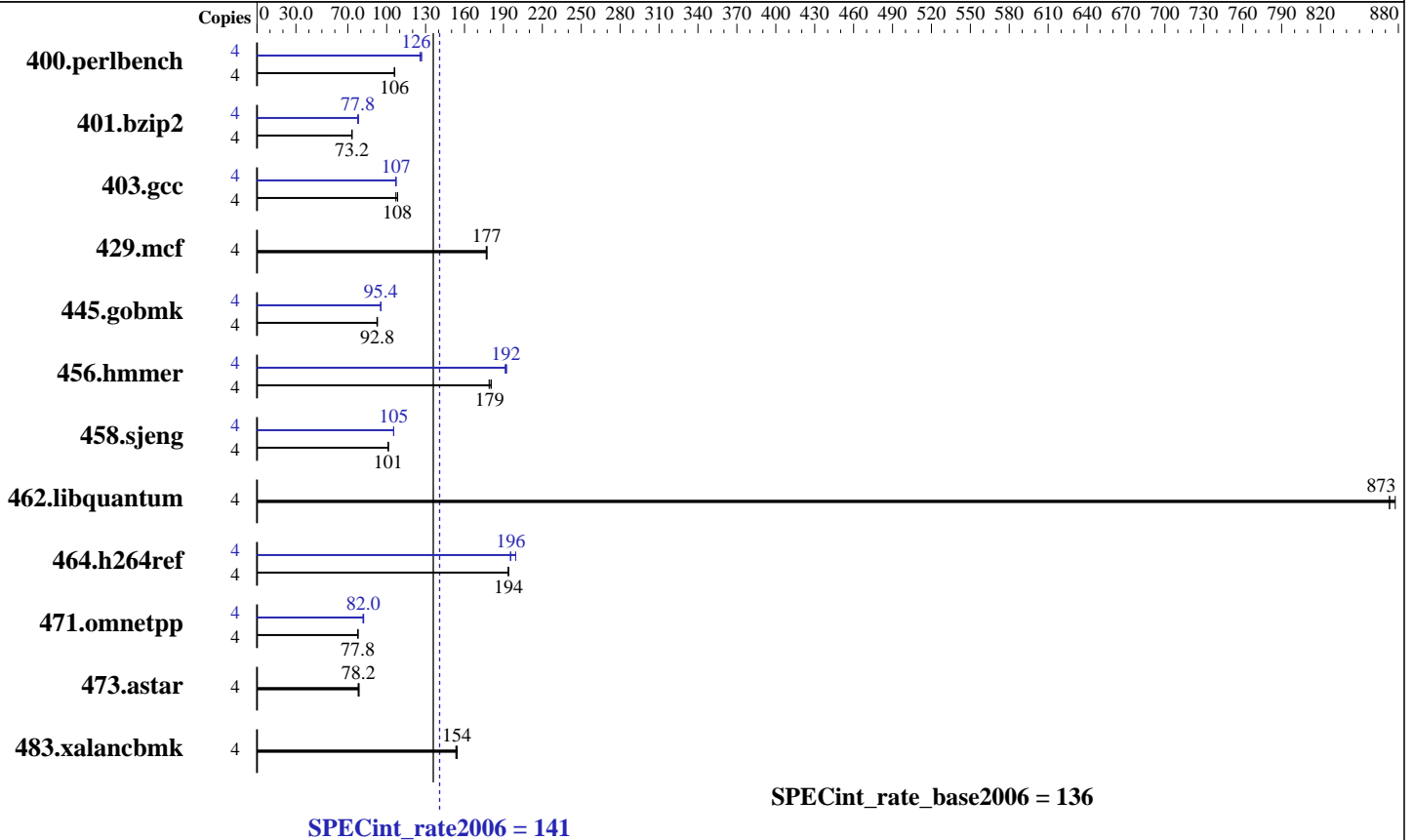
Test sponsor: Acer Incorporation

Tested by: Acer Incorporation

Test date: Dec-2011

Hardware Availability: Jan-2012

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E3-1220
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9,ECC)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.1
 Kernel 2.6.32-131.0.15.el6.x86_64-default
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	368	106	<u>369</u>	<u>106</u>	369	106	4	311	126	<u>309</u>	<u>126</u>	308	127
401.bzip2	4	<u>528</u>	<u>73.2</u>	527	73.3	528	73.2	4	496	77.8	<u>496</u>	<u>77.8</u>	493	78.3
403.gcc	4	297	108	<u>297</u>	<u>108</u>	301	107	4	300	107	301	107	<u>300</u>	<u>107</u>
429.mcf	4	206	177	206	177	<u>206</u>	<u>177</u>	4	206	177	206	177	<u>206</u>	<u>177</u>
445.gobmk	4	<u>452</u>	<u>92.8</u>	452	92.8	454	92.5	4	440	95.4	439	95.6	<u>440</u>	<u>95.4</u>
456.hammer	4	207	181	<u>208</u>	<u>179</u>	209	179	4	194	192	195	191	<u>194</u>	<u>192</u>
458.sjeng	4	478	101	478	101	<u>478</u>	<u>101</u>	4	459	105	<u>459</u>	<u>105</u>	459	105
462.libquantum	4	94.9	873	94.4	878	<u>94.9</u>	<u>873</u>	4	94.9	873	94.4	878	<u>94.9</u>	<u>873</u>
464.h264ref	4	<u>457</u>	<u>194</u>	457	194	457	194	4	444	199	<u>452</u>	<u>196</u>	453	195
471.omnetpp	4	322	77.7	321	77.9	<u>321</u>	<u>77.8</u>	4	<u>305</u>	<u>82.0</u>	306	81.8	304	82.3
473.astar	4	357	78.7	360	78.1	<u>359</u>	<u>78.2</u>	4	357	78.7	360	78.1	<u>359</u>	<u>78.2</u>
483.xalancbmk	4	<u>180</u>	<u>154</u>	180	153	179	154	4	<u>180</u>	<u>154</u>	180	153	179	154

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

numactl was used to bind copies to the cores
The config file option 'submit' was used.

Operating System Notes

Large pages were disabled for this run.

General Notes

Binaries compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmcr: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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