



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECfp[®]_rate2006 = 182

Servidor Itautec MX224 (Intel Xeon E5630)

SPECfp_rate_base2006 = 178

CPU2006 license: 9001

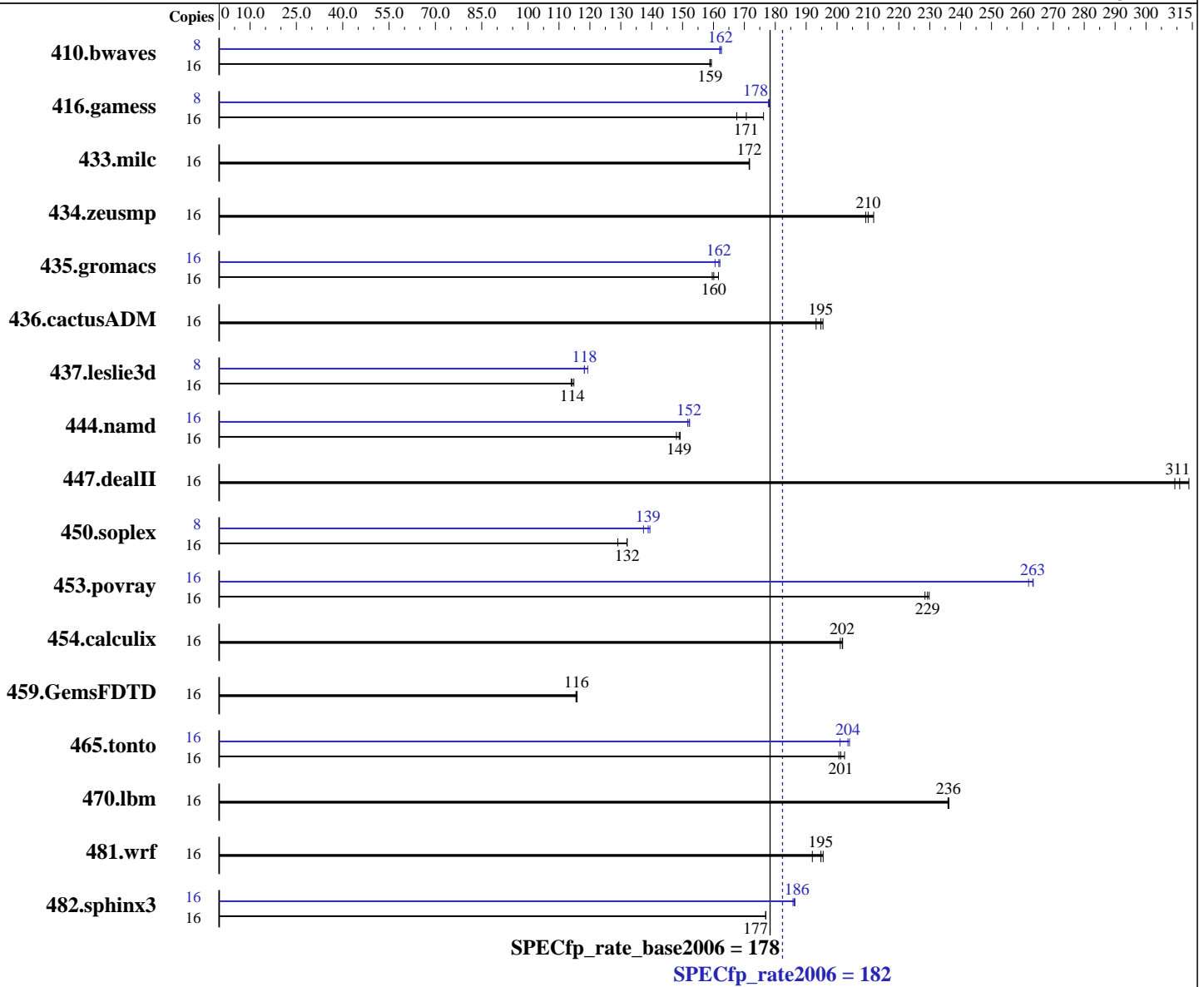
Test date: Nov-2011

Test sponsor: Itautec

Hardware Availability: Jul-2011

Tested by: Itautec

Software Availability: Aug-2011



Hardware

CPU Name: Intel Xeon E5630
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2533
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: C/C++/Fortran: Version 12.1.0 of Intel Compiler XE Build 20110811
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECfp_rate2006 = 182

Servidor Itaotec MX224 (Intel Xeon E5630)

SPECfp_rate_base2006 = 178

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Nov-2011
Hardware Availability: Jul-2011
Software Availability: Aug-2011

L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 500 GB SAS, 15000 RPM
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1365	159	<u>1368</u>	<u>159</u>	1369	159	8	<u>670</u>	<u>162</u>	669	163	671	162
416.gamess	16	<u>1836</u>	<u>171</u>	1870	168	1778	176	8	881	178	880	178	<u>880</u>	<u>178</u>
433.milc	16	855	172	856	172	<u>856</u>	<u>172</u>	16	855	172	856	172	<u>856</u>	<u>172</u>
434.zeusmp	16	687	212	<u>693</u>	<u>210</u>	696	209	16	687	212	<u>693</u>	<u>210</u>	696	209
435.gromacs	16	707	162	716	160	<u>713</u>	<u>160</u>	16	<u>706</u>	<u>162</u>	705	162	712	161
436.cactusADM	16	978	195	990	193	<u>982</u>	<u>195</u>	16	978	195	990	193	<u>982</u>	<u>195</u>
437.leslie3d	16	1319	114	<u>1316</u>	<u>114</u>	1310	115	8	636	118	<u>636</u>	<u>118</u>	630	119
444.namd	16	<u>862</u>	<u>149</u>	867	148	860	149	16	<u>844</u>	<u>152</u>	843	152	846	152
447.dealII	16	<u>589</u>	<u>311</u>	583	314	592	309	16	<u>589</u>	<u>311</u>	583	314	592	309
450.soplex	16	1034	129	1010	132	<u>1011</u>	<u>132</u>	8	486	137	<u>480</u>	<u>139</u>	479	139
453.povray	16	370	230	373	228	<u>371</u>	<u>229</u>	16	325	262	323	264	<u>323</u>	<u>263</u>
454.calculix	16	<u>654</u>	<u>202</u>	657	201	654	202	16	<u>654</u>	<u>202</u>	657	201	654	202
459.GemsFDTD	16	<u>1467</u>	<u>116</u>	1465	116	1470	115	16	<u>1467</u>	<u>116</u>	1465	116	1470	115
465.tonto	16	785	201	<u>783</u>	<u>201</u>	778	202	16	772	204	<u>774</u>	<u>204</u>	783	201
470.lbm	16	931	236	<u>932</u>	<u>236</u>	932	236	16	931	236	<u>932</u>	<u>236</u>	932	236
481.wrf	16	914	195	<u>918</u>	<u>195</u>	931	192	16	914	195	<u>918</u>	<u>195</u>	931	192
482.sphinx3	16	<u>1763</u>	<u>177</u>	1762	177	1763	177	16	<u>1676</u>	<u>186</u>	1679	186	1673	186

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run

Platform Notes

Data Reuse disabled in BIOS.



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECfp_rate2006 = 182

Servidor Itaotec MX224 (Intel Xeon E5630)

SPECfp_rate_base2006 = 178

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Nov-2011
Hardware Availability: Jul-2011
Software Availability: Aug-2011

General Notes

This result was measured on the Servidor Itaotec MX224.
The Servidor Itaotec MX203+, Servidor Itaotec MX223+ and the Servidor Itaotec MX224 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECfp_rate2006 = 182

Servidor Itaotec MX224 (Intel Xeon E5630)

SPECfp_rate_base2006 = 178

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Nov-2011
Hardware Availability: Jul-2011
Software Availability: Aug-2011

Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECfp_rate2006 = 182

Servidor Itautec MX224 (Intel Xeon E5630)

SPECfp_rate_base2006 = 178

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Nov-2011
Hardware Availability: Jul-2011
Software Availability: Aug-2011

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECfp_rate2006 = 182

Servidor Itautec MX224 (Intel Xeon E5630)

SPECfp_rate_base2006 = 178

CPU2006 license: 9001

Test date: Nov-2011

Test sponsor: Itautec

Hardware Availability: Jul-2011

Tested by: Itautec

Software Availability: Aug-2011

Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Thu Jul 24 00:44:14 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 December 2011.