



# SPEC® CFP2006 Result

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## IBM Corporation

SPECfp®\_rate2006 = 120

### IBM System x3250 M4 (Intel Xeon E3-1270)

SPECfp\_rate\_base2006 = 117

CPU2006 license: 11

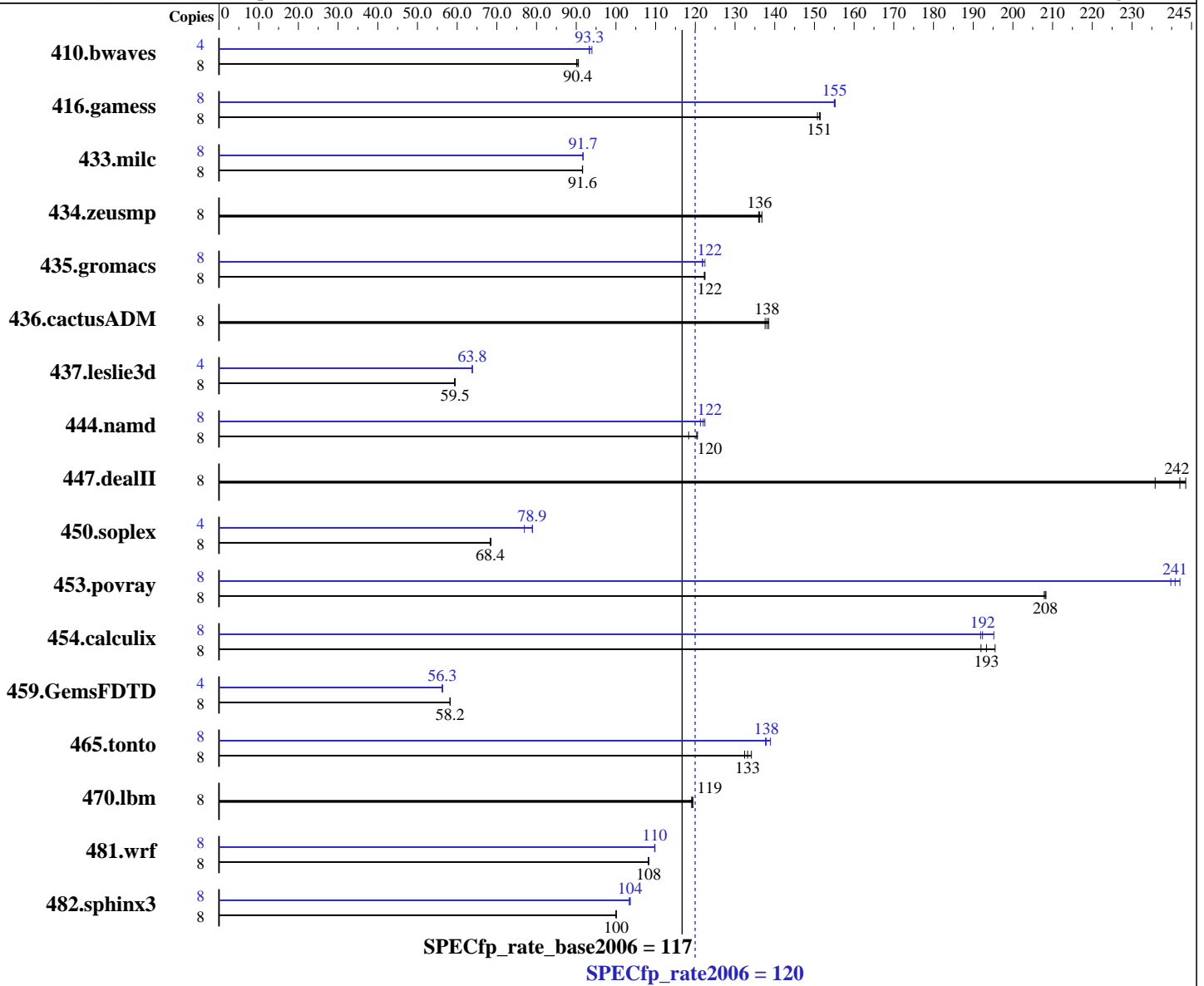
Test date: Oct-2011

Test sponsor: IBM Corporation

Hardware Availability: Oct-2011

Tested by: IBM Corporation

Software Availability: Aug-2011



### Hardware

CPU Name: Intel Xeon E3-1270  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86\_64  
 Compiler: C/C++/Fortran: Version 12.1.0.225 of Intel Compiler XE Build 20110803  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-10600E-9, ECC)  
Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1201	90.5	<u>1202</u>	<u>90.4</u>	1207	90.1	4	583	93.3	579	93.9	<u>582</u>	<u>93.3</u>
416.gamess	8	1034	151	1039	151	<u>1035</u>	<u>151</u>	8	1011	155	<u>1010</u>	<u>155</u>	1009	155
433.milc	8	<u>802</u>	<u>91.6</u>	802	91.6	802	91.5	8	<u>801</u>	<u>91.7</u>	801	91.7	800	91.7
434.zeusmp	8	<u>534</u>	<u>136</u>	536	136	532	137	8	<u>534</u>	<u>136</u>	536	136	532	137
435.gromacs	8	467	122	467	122	<u>467</u>	<u>122</u>	8	<u>469</u>	<u>122</u>	467	122	469	122
436.cactusADM	8	695	138	690	139	<u>692</u>	<u>138</u>	8	695	138	690	139	<u>692</u>	<u>138</u>
437.leslie3d	8	1264	59.5	<u>1265</u>	<u>59.5</u>	1267	59.3	4	589	63.8	<u>589</u>	<u>63.8</u>	589	63.8
444.namd	8	542	118	<u>533</u>	<u>120</u>	532	121	8	<u>526</u>	<u>122</u>	524	122	529	121
447.dealII	8	<u>378</u>	<u>242</u>	376	244	388	236	8	<u>378</u>	<u>242</u>	376	244	388	236
450.soplex	8	974	68.5	<u>976</u>	<u>68.4</u>	976	68.4	4	434	76.9	422	79.0	<u>423</u>	<u>78.9</u>
453.povray	8	205	208	204	208	<u>204</u>	<u>208</u>	8	<u>177</u>	<u>241</u>	176	242	177	240
454.calculix	8	344	192	338	196	<u>341</u>	<u>193</u>	8	338	195	344	192	<u>343</u>	<u>192</u>
459.GemsFDTD	8	<u>1458</u>	<u>58.2</u>	1458	58.2	1458	58.2	4	753	56.4	755	56.2	<u>754</u>	<u>56.3</u>
465.tonto	8	587	134	594	132	<u>591</u>	<u>133</u>	8	567	139	<u>571</u>	<u>138</u>	572	138
470.lbm	8	921	119	<u>923</u>	<u>119</u>	923	119	8	921	119	<u>923</u>	<u>119</u>	923	119
481.wrf	8	826	108	826	108	<u>826</u>	<u>108</u>	8	814	110	<u>814</u>	<u>110</u>	813	110
482.sphinx3	8	1559	100	<u>1558</u>	<u>100</u>	1558	100	8	<u>1506</u>	<u>104</u>	1505	104	1509	103

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset was used to bind copies to the cores

## Platform Notes

BIOS Settings:  
Turbo Mode enabled in BIOS  
C-State enabled in BIOS



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/root/SPECcpu12.1/smartheap:/root/SPECcpu12.1/ic12.1-libs/ia32:/root/SPECcpu12.1/ic12.1-libs/intel64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

memory using RHEL5.5 with binutils-2.17.50.0.6-14.el5

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

447.deallI: -DSPEC\_CPU\_LP64

453.povray: -DSPEC\_CPU\_LP64

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info@spec.org

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## Peak Portability Flags (Continued)

454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

### C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -static -auto-ilp32  
 470.lbm: basepeak = yes  
 482.sphinx3: -xAVX -ipo -O3 -no-prec-div -opt-mem-layout-trans=3  
 -unroll2

### C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -fno-alias -auto-ilp32  
 447.dealII: basepeak = yes  
 450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -opt-malloc-options=3  
 453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -static  
 416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
 -inline-level=0 -scalar-rep- -static  
 434.zeusmp: basepeak = yes  
 437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

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## Peak Optimization Flags (Continued)

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revB.20111206.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revB.20111206.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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