



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3755 M3
(AMD Opteron 6282 SE)

SPECfp®_rate2006 = 756

SPECfp_rate_base2006 = 700

CPU2006 license: 11

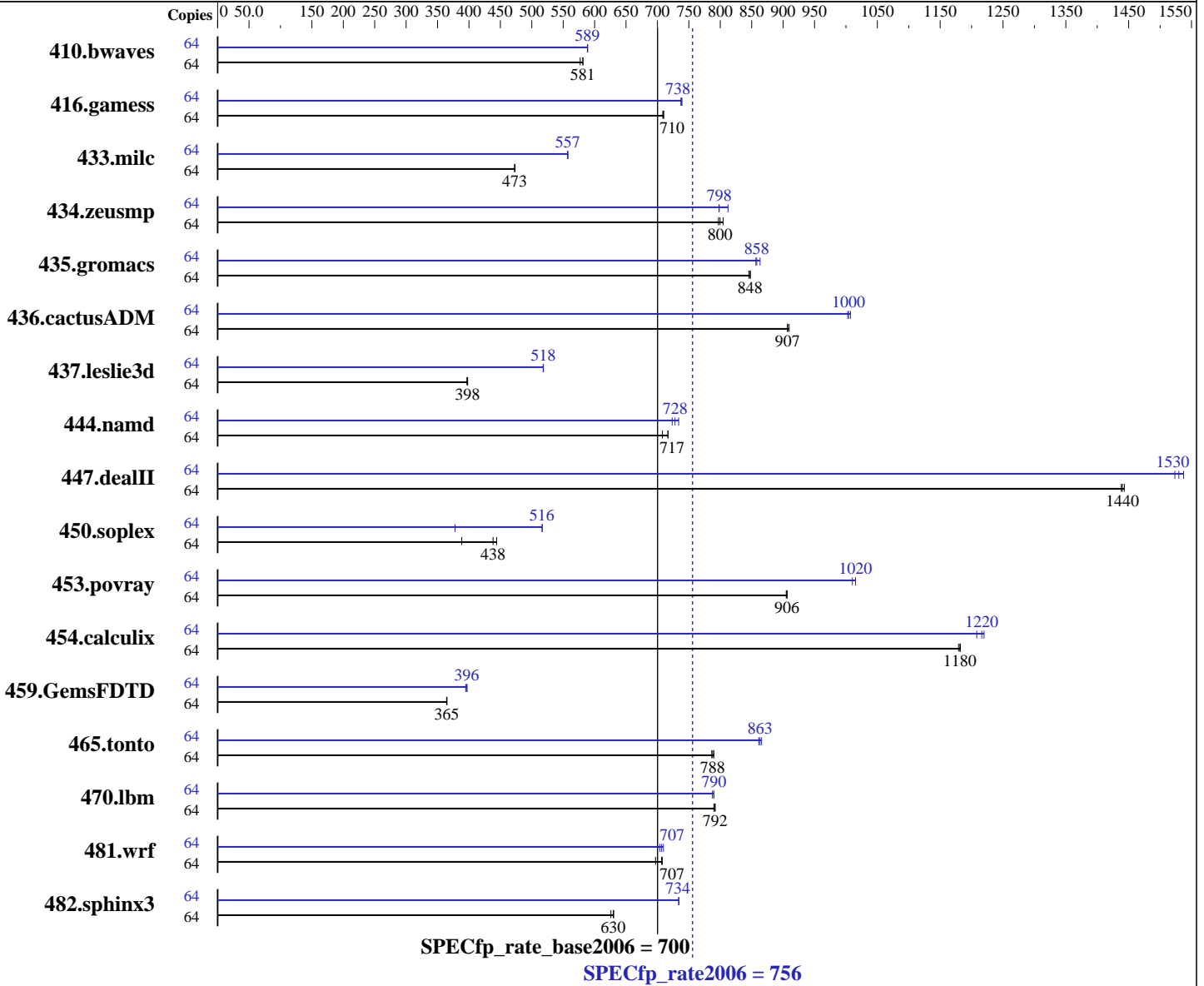
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Nov-2011

Hardware Availability: Dec-2011

Software Availability: Jul-2011



Hardware

CPU Name: AMD Opteron 6282 SE
 CPU Characteristics: AMD Turbo CORE technology up to 3.30 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 64 cores, 4 chips, 16 cores/chip
 CPU(s) orderable: 2,4 chips

Software

Operating System: Red Hat Enterprise Linux Server release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++/Fortran: Version 4.2.5.2 of x86 Open64 Compiler Suite (from AMD)
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit

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Primary Cache: 512 KB I on chip per chip,
64 KB I shared / 2 cores;
16 KB D on chip per core
Secondary Cache: 16 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache: 16 MB I+D on chip per chip, 8 MB shared / 8 cores
Other Cache: None
Memory: 256 GB (32 x 8 GB 2Rx4 PC3L-10600R-09, ECC)
Disk Subsystem: 1 x 600 GB SAS, 15000 RPM
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	1507	577	<u>1496</u>	<u>581</u>	1496	581	64	<u>1478</u>	<u>589</u>	1478	588	1477	589
416.gamess	64	1766	710	1765	710	1769	708	64	1696	739	<u>1697</u>	<u>738</u>	1700	737
433.milc	64	1241	473	1243	473	<u>1243</u>	<u>473</u>	64	<u>1055</u>	<u>557</u>	1055	557	1054	558
434.zeusmp	64	731	797	724	805	<u>728</u>	<u>800</u>	64	730	798	717	812	<u>730</u>	<u>798</u>
435.gromacs	64	<u>539</u>	<u>848</u>	541	845	539	848	64	<u>533</u>	<u>858</u>	533	857	529	863
436.cactusADM	64	843	907	841	910	843	907	64	759	1010	<u>762</u>	<u>1000</u>	763	1000
437.leslie3d	64	1512	398	<u>1513</u>	<u>398</u>	1516	397	64	1161	518	1160	518	<u>1160</u>	<u>518</u>
444.namd	64	716	717	725	708	<u>716</u>	<u>717</u>	64	709	723	<u>705</u>	<u>728</u>	699	734
447.dealII	64	509	1440	507	1440	<u>508</u>	<u>1440</u>	64	476	1540	481	1520	<u>479</u>	<u>1530</u>
450.soplex	64	1374	389	1202	444	<u>1218</u>	<u>438</u>	64	1413	378	<u>1034</u>	<u>516</u>	1033	517
453.povray	64	376	906	<u>376</u>	<u>906</u>	376	905	64	<u>335</u>	<u>1020</u>	337	1010	335	1020
454.calculix	64	448	1180	447	1180	<u>447</u>	<u>1180</u>	64	<u>434</u>	<u>1220</u>	433	1220	437	1210
459.GemsFDTD	64	1860	365	<u>1862</u>	<u>365</u>	1863	364	64	<u>1714</u>	<u>396</u>	1710	397	1720	395
465.tonto	64	801	786	797	790	<u>799</u>	<u>788</u>	64	731	861	727	866	<u>730</u>	<u>863</u>
470.lbm	64	1111	792	1114	790	<u>1111</u>	<u>792</u>	64	<u>1113</u>	<u>790</u>	1113	790	1117	787
481.wrf	64	1026	697	1010	708	<u>1012</u>	<u>707</u>	64	1016	704	1007	710	<u>1012</u>	<u>707</u>
482.sphinx3	64	<u>1981</u>	<u>630</u>	1993	626	1979	630	64	1699	734	1700	734	<u>1699</u>	<u>734</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set transparent_hugepage=never as a boot parameter in /boot/grub/menu.lst
Continued on next page



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Operating System Notes (Continued)

Set kernel/randomize_va_space=0 in /etc/sysctl.conf

Set vm/nr_hugepages=57344 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages

Platform Notes

BIOS settings:
Operating Mode set to Performance Mode

General Notes

Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "896"
LD_LIBRARY_PATH = "/root/speccpu-rate-rev1104A4/amd1104-rate-libs-revA/32:/root/speccpu-rate-rev1104A4/amd1104-rate-libs-revA/64"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
<http://developer.amd.com/cpu/open64>

Binaries were compiled on a system with 2x AMD Opteron 6276 chips + 128GB Memory using RHEL 6.1

Submitted_by: Alex CP Lee <cplee@tw.ibm.com>
Submitted: Fri Nov 11 04:42:07 EST 2011
Submission: cpu2006-20111111-18802.sub

Base Compiler Invocation

C benchmarks:
 opencc

C++ benchmarks:
 openCC

Fortran benchmarks:
 openf95

Benchmarks using both Fortran and C:
 opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64
-fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-march=bdver1 -Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m
-IPA:plimit=8000 -IPA:small_pu=100 -mso

C++ benchmarks:
-march=bdver1 -Ofast -static -CG:load_exe=0 -OPT:malloc_alg=1
-INLINE:aggressive=on -HP:bd=2m:heap=2m -D__OPEN64_FAST_SET

Fortran benchmarks:
-march=bdver1 -Ofast -LNO:blocking=off -OPT:rsqrt=2
-OPT:unroll_size=256 -HP:bd=2m:heap=2m -mso

Benchmarks using both Fortran and C:
-march=bdver1 -Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m
-IPA:plimit=8000 -IPA:small_pu=100 -mso -LNO:blocking=off
-OPT:rsqrt=2 -OPT:unroll_size=256

Peak Compiler Invocation

C benchmarks:
openc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
opencc openf95

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64
-fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -march=bdver1 -Ofast -CG:movnti=1 -CG:locs_best=on
-HP:bdt=2m:heap=2m -IPA:plimit=7000 -IPA:callee_limit=1200
-OPT:struct_array_copy=2 -OPT:alias=field_sensitive -mso

470.lbm: -march=bdver1 -Ofast -CG:cmp_peep=on
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -HP:bdt=2m:heap=2m
-IPA:plimit=8000 -IPA:small_pu=100 -mso

482.sphinx3: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:malloc_alg=2
-CG:cmp_peep=on -CG:local_sched_alg=2 -INLINE:aggressive=on
-LNO:prefetch=2 -LNO:prefetch_ahead=4 -mso
```

C++ benchmarks:

```
444.namd: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:ignore_feedback=off
-CG:local_sched_alg=2 -CG:load_exe=0 -OPT:unroll_size=256
-fno-exceptions -HP:bdt=2m:heap=2m
```

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Peak Optimization Flags (Continued)

447.dealIII: -march=bdver1 -Ofast -D__OPEN64_FAST_SET -static
-INLINE:aggressive=on -LNO:opt=0 -LNO:simd=0
-fno-emit-exceptions -m32 -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -HP:bd=2m:heap=2m
-GRA:unspill=on -CG:cmp_peep=on -CG:movext_icmp=off
-TENV:frame_pointer=off

450.soplex: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on -OPT:RO=1
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -fno-exceptions -m32
-HP:bd=2m:heap=2m -WOPT:sib=on

453.povray: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -CG:pre_local_sched=off
-INLINE:aggressive=on -HP:bd=2m:heap=2m -OPT:transform=2
-OPT:alias=disjoint -WOPT:aggcm=0

Fortran benchmarks:

410.bwaves: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:ignore_feedback=off -LNO:fu=4
-LNO:loop_model_simd=on -LNO:simd_rm_unity_remainder=on
-WOPT:aggstr=0 -HP:bd=2m:heap=2m -CG:cmp_peep=on

416.gamess: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:simd=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-OPT:unroll_times_max=2 -CG:local_sched_alg=1
-HP:bd=2m:heap=2m -WOPT:sib=on

434.zeusmp: -march=bdver1 -Ofast -LNO:blocking=off -LNO:interchange=off
-HP:bd=2m:heap=2m

437.leslie3d: -march=bdver1 -Ofast -CG:pre_minreg_level=2 -LNO:simd=0
-LNO:fusion=2 -HP:bd=2m:heap=2m -mso

459.GemsFDTD: -march=bdver1 -Ofast -OPT:unroll_size=0 -LNO:fission=2
-CG:load_exe=0 -CG:local_sched_alg=2 -HP

465.tonto: -march=bdver1 -Ofast -OPT:alias=no_f90_pointer_alias
-LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525
-HP:bd=2m:heap=2m

Benchmarks using both Fortran and C:

435.gromacs: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:rsqrt=2
-HP:bd=2m:heap=2m

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Peak Optimization Flags (Continued)

```

436.cactusADM: -march=bdver1 -fb_create fbdata(pass 1)
                -fb_opt fbdata(pass 2) -Ofast -LNO:blocking=off
                -LNO:prefetch=2 -HP -CG:locs_shallow_depth=1 -CG:load_exe=0
                -WOPT:sib=on

454.calculix: -march=bdver1 -Ofast -OPT:unroll_size=256
              -GRA:optimize_boundary=on -HP:bdt=2m:heap=2m

481.wrf: -march=bdver1 -Ofast -LNO:blocking=off -LANG:copyinout=off
         -IPA:callee_limit=5000 -GRA:prioritize_by_density=on
         -CG:load_exe=1 -HP -WOPT:sib=on

```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-425-flags-rate-revA.html>
<http://www.spec.org/cpu2006/flags/amd1104-platform-rate-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-425-flags-rate-revA.xml>
<http://www.spec.org/cpu2006/flags/amd1104-platform-rate-revA.xml>

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For other inquiries, please contact webmaster@spec.org.

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