



# SPEC® CFP2006 Result

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## Bull SAS

SPECfp®\_rate2006 = 214

NovaScale T840 F2 (Intel Xeon E5645, 2.40 GHz)

SPECfp\_rate\_base2006 = 210

CPU2006 license: 20

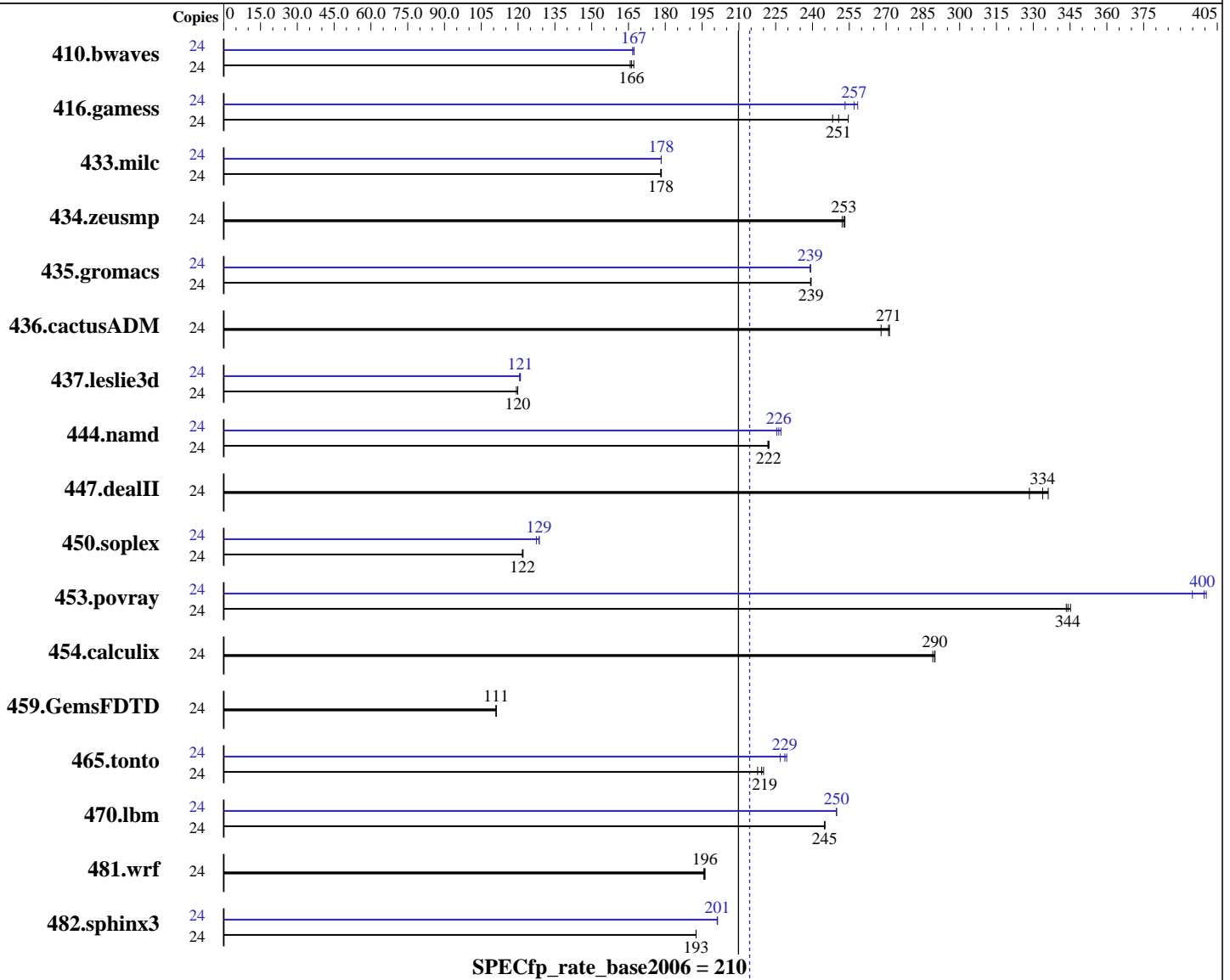
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: May-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon E5645  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
Disk Subsystem: 2 x 146 GB 15000 RPM SAS  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1951	167	<u>1962</u>	<u>166</u>	1968	166	24	1957	167	<u>1951</u>	<u>167</u>	1950	167
416.gamess	24	1893	248	1846	255	<u>1875</u>	<u>251</u>	24	1818	258	1855	253	<u>1828</u>	<u>257</u>
433.milc	24	1235	178	<u>1236</u>	<u>178</u>	1237	178	24	<u>1235</u>	<u>178</u>	1235	178	1236	178
434.zeusmp	24	866	252	<u>864</u>	<u>253</u>	862	253	24	866	252	<u>864</u>	<u>253</u>	862	253
435.gromacs	24	716	239	716	239	<u>716</u>	<u>239</u>	24	717	239	716	239	<u>717</u>	<u>239</u>
436.cactusADM	24	<u>1058</u>	<u>271</u>	1057	271	1070	268	24	<u>1058</u>	<u>271</u>	1057	271	1070	268
437.leslie3d	24	1891	119	1882	120	<u>1882</u>	<u>120</u>	24	1871	121	<u>1867</u>	<u>121</u>	1865	121
444.namd	24	866	222	868	222	<u>867</u>	<u>222</u>	24	<u>851</u>	<u>226</u>	847	227	854	225
447.dealII	24	<u>822</u>	<u>334</u>	817	336	836	328	24	<u>822</u>	<u>334</u>	817	336	836	328
450.soplex	24	<u>1643</u>	<u>122</u>	1640	122	1644	122	24	1570	127	<u>1557</u>	<u>129</u>	1556	129
453.povray	24	372	343	370	345	<u>371</u>	<u>344</u>	24	<u>319</u>	<u>400</u>	323	395	319	401
454.calculix	24	<u>683</u>	<u>290</u>	683	290	685	289	24	<u>683</u>	<u>290</u>	683	290	685	289
459.GemsFDTD	24	2292	111	2294	111	<u>2293</u>	<u>111</u>	24	2292	111	2294	111	<u>2293</u>	<u>111</u>
465.tonto	24	1085	218	1073	220	<u>1077</u>	<u>219</u>	24	1029	229	1041	227	<u>1032</u>	<u>229</u>
470.lbm	24	1346	245	1346	245	<u>1346</u>	<u>245</u>	24	1320	250	<u>1320</u>	<u>250</u>	1321	250
481.wrf	24	<u>1367</u>	<u>196</u>	1370	196	1366	196	24	<u>1367</u>	<u>196</u>	1370	196	1366	196
482.sphinx3	24	<u>2429</u>	<u>193</u>	2429	193	2429	193	24	2326	201	<u>2324</u>	<u>201</u>	2324	201

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages  
echo 10800 > /proc/sys/vm/nr\_hugepages  
export HUGETLB\_MORECORE=yes  
export LD\_PRELOAD=/usr/lib64/libhugetlbfs.so



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### Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)  
Data Reuse = Disabled (Default = Enabled)

### General Notes

The Dell PowerEdge T610 and the Bull NovaScale T840 F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge T610 model. Binaries were compiled on RHEL5.5

### Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

### Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.lelie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`  
 416.gamess: `-DSPEC_CPU_LP64`  
 433.milc: `-DSPEC_CPU_LP64`  
 434.zeusmp: `-DSPEC_CPU_LP64`  
 435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`  
 436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`  
 437.leslie3d: `-DSPEC_CPU_LP64`  
 444.namd: `-DSPEC_CPU_LP64`  
 447.dealII: `-DSPEC_CPU_LP64`  
 453.povray: `-DSPEC_CPU_LP64`  
 454.calculix: `-DSPEC_CPU_LP64 -nofor_main`  
 459.GemsFDTD: `-DSPEC_CPU_LP64`  
 465.tonto: `-DSPEC_CPU_LP64`

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64

481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

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## Peak Optimization Flags (Continued)

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32
```

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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