SPEC® CFP2006 Result
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Intel Corporation

$SPECfp^{®}_rate2006 = $ 62.0

Intel DH67BLB3 Motherboard (Intel Core i3-2100T)

$SPECfp_rate_base2006 = $ 62.0
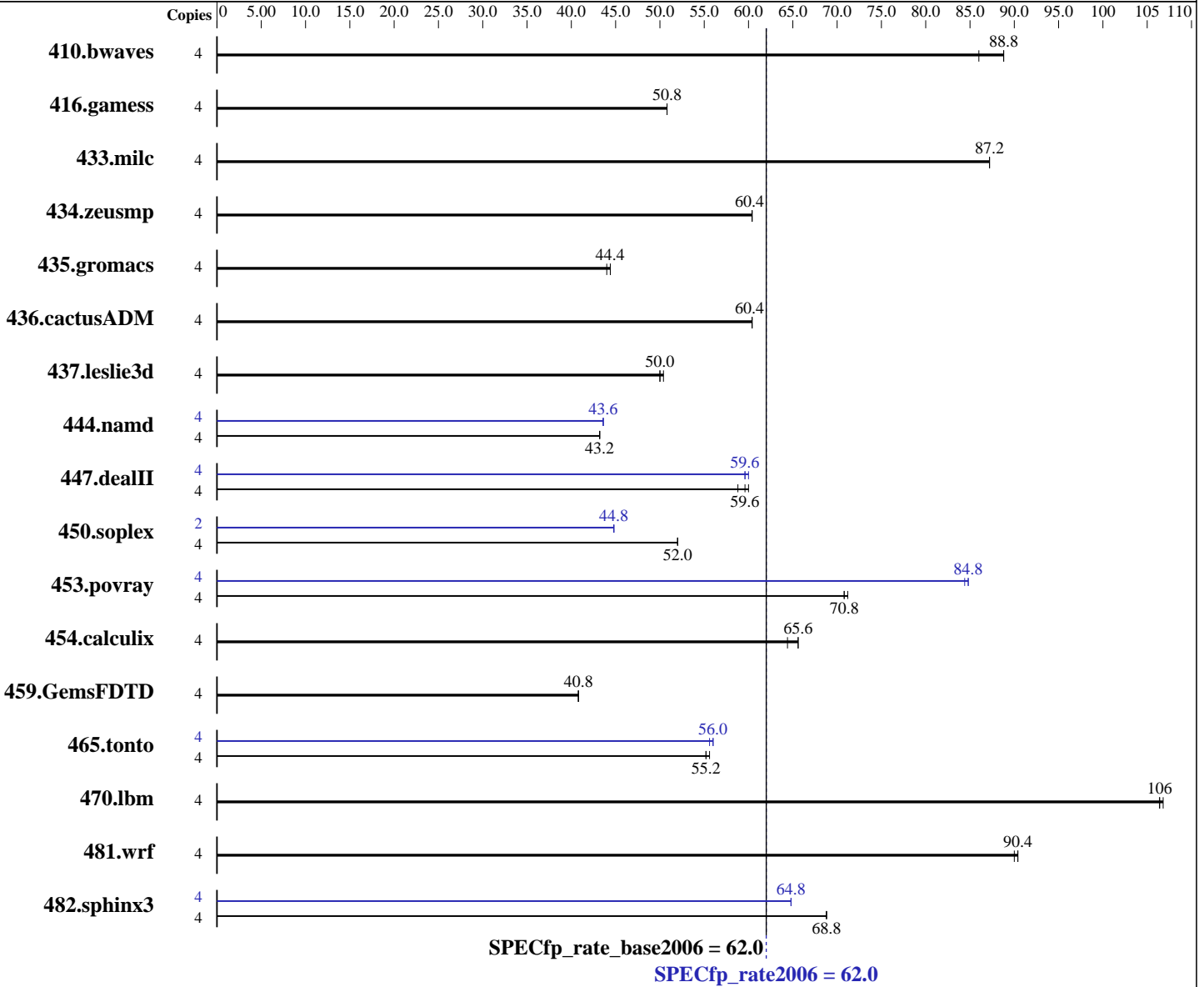
CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jul-2011
Hardware Availability: Mar-2011
Software Availability: Apr-2011

Benchmark	Copies	Result
410.bwaves	4	88.8
416.gamess	4	50.8
433.milc	4	87.2
434.zeusmp	4	60.4
435.gromacs	4	44.4
436.cactusADM	4	60.4
437.leslie3d	4	50.0
444.namd	4	43.6
444.namd	4	43.2
447.dealII	4	59.6
447.dealII	4	59.6
450.soplex	2	44.8
450.soplex	4	52.0
453.povray	4	84.8
453.povray	4	70.8
454.calculix	4	65.6
459.GemsFDTD	4	40.8
465.tonto	4	56.0
465.tonto	4	55.2
470.lbm	4	106
481.wrf	4	90.4
482.sphinx3	4	64.8
482.sphinx3	4	68.8

SPECfp_rate_base2006 = 62.0
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Hardware

CPU Name: Intel Core i3-2100T
CPU Characteristics:
CPU MHz: 2500
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Windows 7 Ultimate (64-bit)
Compiler: Intel C++ Compiler XE for Intel 64
Version 12.0.3.176 Build 20110309
Intel Visual Fortran Compiler XE for Intel 64
Version 12.0.3.176 Build 20110309
Microsoft Visual Studio 2008 Professional SP1
(for libraries)
Auto Parallel: No
File System: NTFS

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L3 Cache:	3 MB I+D on chip per chip	System State:	Default
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)	Peak Pointers:	32/64-bit
Disk Subsystem:	Seagate 1 TB SATA, 7200 RPM	Other Software:	SmartHeap Library Version 9.01 from
Other Hardware:	None		http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	632	86.0	612	88.8	**613**	**88.8**	4	632	86.0	612	88.8	**613**	**88.8**
416.gamess	4	**1547**	**50.8**	1547	50.8	1545	50.8	4	**1547**	**50.8**	1547	50.8	1545	50.8
433.milc	4	**421**	**87.2**	421	87.2	420	87.2	4	**421**	**87.2**	421	87.2	420	87.2
434.zeusmp	4	**603**	**60.4**	603	60.4	604	60.4	4	**603**	**60.4**	603	60.4	604	60.4
435.gromacs	4	645	44.4	**646**	**44.4**	646	44.0	4	645	44.4	**646**	**44.4**	646	44.0
436.cactusADM	4	**790**	**60.4**	789	60.4	790	60.4	4	**790**	**60.4**	789	60.4	790	60.4
437.leslie3d	4	752	50.0	748	50.4	**752**	**50.0**	4	752	50.0	748	50.4	**752**	**50.0**
444.namd	4	**740**	**43.2**	740	43.2	740	43.2	4	734	43.6	734	43.6	**734**	**43.6**
447.dealII	4	763	60.0	779	58.8	**769**	**59.6**	4	769	59.6	**767**	**59.6**	764	60.0
450.soplex	4	640	52.0	641	52.0	**640**	**52.0**	2	373	44.8	373	44.8	**373**	**44.8**
453.povray	4	301	70.8	**301**	**70.8**	300	71.2	4	252	84.4	251	84.8	**251**	**84.8**
454.calculix	4	502	65.6	**504**	**65.6**	512	64.4	4	502	65.6	**504**	**65.6**	512	64.4
459.GemsFDTD	4	1043	40.8	**1043**	**40.8**	1042	40.8	4	1043	40.8	**1043**	**40.8**	1042	40.8
465.tonto	4	712	55.2	**711**	**55.2**	710	55.6	4	**703**	**56.0**	703	56.0	707	55.6
470.lbm	4	**516**	**106**	516	106	516	107	4	**516**	**106**	516	106	516	107
481.wrf	4	**495**	**90.4**	493	90.4	496	90.0	4	**495**	**90.4**	493	90.4	496	90.0
482.sphinx3	4	1134	68.8	**1133**	**68.8**	1132	68.8	4	1202	64.8	1203	64.8	**1202**	**64.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
    The start command with the /affinity switch was used to bind processes to cores

General Notes

    Tested systems can be used with Shin-G ATX case,
    PC Power and Cooling 1200W power supply

Base Compiler Invocation

    C benchmarks:
        icl -Qvc9 -Qstd=c99

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Hardware Availability: Mar-2011

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Base Compiler Invocation (Continued)

C++ benchmarks:
```
icl -Qvc9
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc9 -Qstd=c99 ifort
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_P64 -names:lowercase
    416.gamess: -DSPEC_CPU_P64
      433.milc: -DSPEC_CPU_P64
    434.zeusmp: -DSPEC_CPU_P64
    435.gromacs: -DSPEC_CPU_P64
  436.cactusADM: -DSPEC_CPU_P64 /names:lowercase /assume:underscore
    437.leslie3d: -DSPEC_CPU_P64
      444.namd: -DSPEC_CPU_P64 /TP
     447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
     450.soplex: -DSPEC_CPU_P64
     453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
    454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER /names:lowercase
   459.GemsFDTD: -DSPEC_CPU_P64
      465.tonto: -DSPEC_CPU_P64
       470.lbm: -DSPEC_CPU_P64
       481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
    482.sphinx3: -DSPEC_CPU_P64
```

Base Optimization Flags

C benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000
        -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx-features
-Qauto-ilp32 /F1000000000 shlW64M.lib        -link /FORCE:MULTIPLE
```

Fortran benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias /F1000000000
        -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000
        -link /FORCE:MULTIPLE
```

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Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

    433.milc: basepeak = yes

    470.lbm: basepeak = yes

   482.sphinx3: `-QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias`
       `-Qauto-ilp32 /F1000000000          -link /FORCE:MULTIPLE`

C++ benchmarks:

   444.namd: `-QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo`
       `-O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 shlW64M.lib`
            `-link /FORCE:MULTIPLE`

   447.dealII: `-QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo`
       `-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qscalar-rep-`
       `-Qauto-ilp32 /F1000000000 shlW64M.lib`
            `-link /FORCE:MULTIPLE`

   450.soplex: `-QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo`
       `-O3 -Qauto-ilp32 /F1000000000 shlW64M.lib`
            `-link /FORCE:MULTIPLE`

   453.povray: `-QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo`
       `-O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000`
       `shlW64M.lib          -link /FORCE:MULTIPLE`

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Peak Optimization Flags (Continued)

Fortran benchmarks:

     410.bwaves: basepeak = yes

     416.gamess: basepeak = yes

     434.zeusmp: basepeak = yes

     437.leslie3d: basepeak = yes

   459.GemsFDTD: basepeak = yes

     465.tonto: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
             -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000
                  -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

     435.gromacs: basepeak = yes

   436.cactusADM: basepeak = yes

     454.calculix: basepeak = yes

        481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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