SPEC® CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GT350 F1 (Intel Xeon E5649)

$SPECfp®\_rate2006 = \quad 218$

$SPECfp\_rate\_base2006 = \quad 214$

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

SPECfp_rate_base2006 = 214

SPECfp_rate2006 = 218

Hardware

CPU Name:	Intel Xeon E5649
CPU Characteristics:	Intel Turbo Boost Technology up to 2.93 GHz
CPU MHz:	2533
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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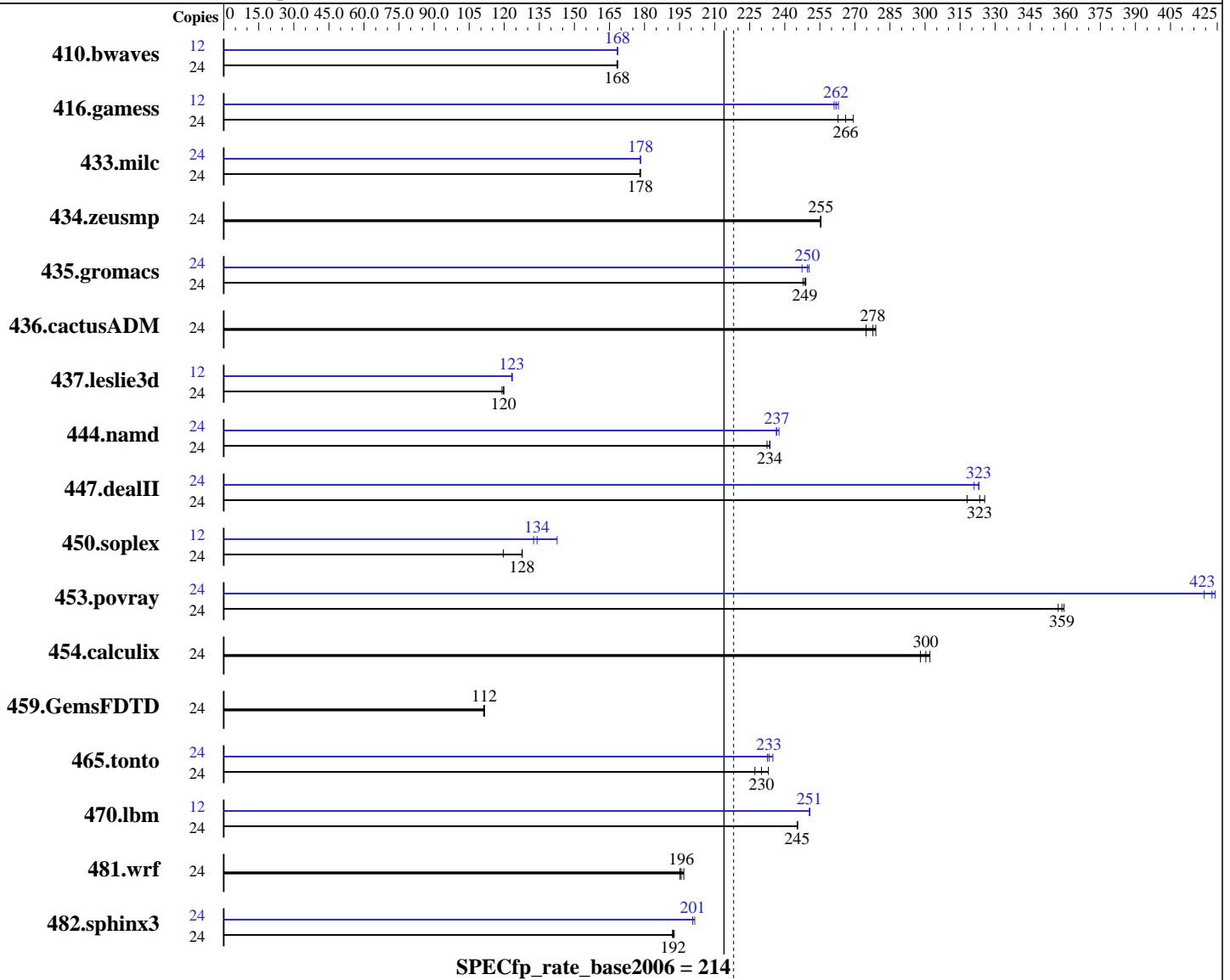SPECfp_rate_base2006 =   214

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L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx8 PC3-1066R-9 ECC)
Disk Subsystem:	1 x 500 GB SATA, 7200 RPM SATA HDD
Other Hardware:	None

Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1935	169	**1938**	**168**	1939	168	12	**968**	**168**	967	169	969	168
416.gamess	24	1788	263	1745	269	**1766**	**266**	12	**897**	**262**	900	261	894	263
433.milc	24	1236	178	1238	178	**1237**	**178**	24	1236	178	**1236**	**178**	1236	178
434.zeusmp	24	856	255	855	255	**855**	**255**	24	856	255	855	255	**855**	**255**
435.gromacs	24	688	249	**689**	**249**	691	248	24	**686**	**250**	684	250	693	247
436.cactusADM	24	1028	279	**1033**	**278**	1044	275	24	1028	279	**1033**	**278**	1044	275
437.leslie3d	24	1895	119	**1885**	**120**	1883	120	12	**915**	**123**	916	123	914	123
444.namd	24	828	232	**824**	**234**	824	234	24	810	238	**814**	**237**	814	236
447.dealII	24	**849**	**323**	843	326	863	318	24	**850**	**323**	855	321	850	323
450.soplex	24	1674	120	1567	128	**1569**	**128**	12	755	133	**747**	**134**	702	143
453.povray	24	358	357	**356**	**359**	355	359	24	301	424	304	419	**302**	**423**
454.calculix	24	655	302	**659**	**300**	664	298	24	655	302	**659**	**300**	664	298
459.GemsFDTD	24	2291	111	**2284**	**112**	2282	112	24	2291	111	**2284**	**112**	2282	112
465.tonto	24	**1027**	**230**	1039	227	1014	233	24	**1012**	**233**	1005	235	1015	233
470.lbm	24	1343	246	**1344**	**245**	1344	245	12	**658**	**251**	658	251	657	251
481.wrf	24	1362	197	**1370**	**196**	1374	195	24	1362	197	**1370**	**196**	1374	195
482.sphinx3	24	2436	192	**2433**	**192**	2427	193	24	2331	201	**2331**	**201**	2321	201

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
     'ulimit -s unlimited' was used to set environment stack size
     Large pages were disabled for this run
```

Platform Notes

```
    BIOS settings:
    Fan speed = full speed (Default = Balanced)
    Data Reuse = Disabled (Default = Enabled)
```

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General Notes

```
Binaries compiled on RHEL5.5
This result was measured on Gateway GT350 F1
Acer AT350 F1 is electronically equivalent
```

Base Compiler Invocation

C benchmarks:
     icc   -m64

C++ benchmarks:
     icpc   -m64

Fortran benchmarks:
     ifort -m64

Benchmarks using both Fortran and C:
     icc   -m64 ifort -m64

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
     -xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:
     -xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Continued on next page

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Base Optimization Flags (Continued)

Fortran benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Benchmarks using both Fortran and C:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc    -m64
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
     410.bwaves: -DSPEC_CPU_LP64
     416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
     434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
       444.namd: -DSPEC_CPU_LP64
       447.dealII: -DSPEC_CPU_LP64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
       465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
         481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

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Peak Optimization Flags

C benchmarks:

   433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32`

   470.lbm: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3`
`-ansi-alias -opt-prefetch -static -auto-ilp32`

   482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2`

C++ benchmarks:

   444.namd: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias`
`-auto-ilp32`

   447.dealII: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32`

   450.soplex: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3`
`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

   453.povray: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias`
`-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Fortran benchmarks:

   410.bwaves: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -static`

   416.gamess: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -unroll2`
`-inline-level=0 -scalar-rep- -static`

   434.zeusmp: `basepeak = yes`

   437.leslie3d: `-xSSE4.2 -ipo -O3 -no-prec-div`
`-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

   459.GemsFDTD: `basepeak = yes`

   465.tonto: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto`
`-inline-calloc -opt-malloc-options=3`
`-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
             -static -auto-ilp32
```

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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