



SPEC[®] CFP2006 Result

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NEC Corporation

SPECfp[®]_rate2006 = 109

Express5800/GT110d (Intel Xeon E3-1220)

SPECfp_rate_base2006 = 106

CPU2006 license: 9006

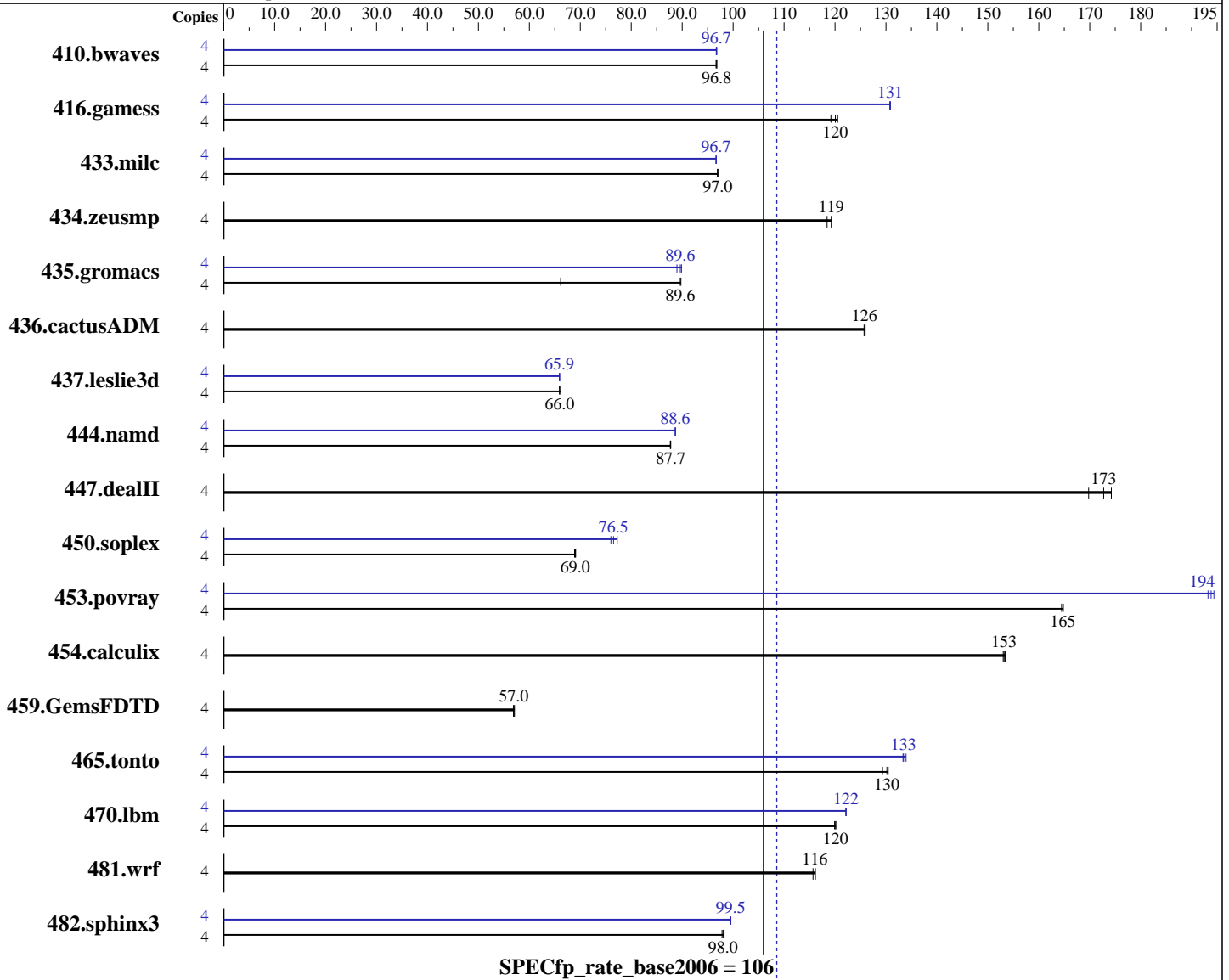
Test date: May-2011

Test sponsor: NEC Corporation

Hardware Availability: Jun-2011

Tested by: NEC Corporation

Software Availability: Mar-2011



Hardware

CPU Name: Intel Xeon E3-1220
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64, Version 12.0.3.174 Build 20110309
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

Continued on next page



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L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
 Disk Subsystem: 1 x 160 GB SATA, 7200 RPM
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	<u>562</u>	<u>96.8</u>	562	96.8	562	96.6	4	562	96.7	562	96.8	<u>562</u>	<u>96.7</u>
416.gamess	4	<u>652</u>	<u>120</u>	657	119	650	121	4	598	131	<u>599</u>	<u>131</u>	599	131
433.milc	4	379	97.0	<u>379</u>	<u>97.0</u>	379	96.9	4	380	96.7	380	96.7	<u>380</u>	<u>96.7</u>
434.zeusmp	4	<u>305</u>	<u>119</u>	307	118	305	119	4	<u>305</u>	<u>119</u>	307	118	305	119
435.gromacs	4	318	89.8	<u>319</u>	<u>89.6</u>	432	66.1	4	318	89.9	<u>319</u>	<u>89.6</u>	321	89.0
436.cactusADM	4	380	126	<u>380</u>	<u>126</u>	380	126	4	380	126	<u>380</u>	<u>126</u>	380	126
437.leslie3d	4	568	66.2	<u>570</u>	<u>66.0</u>	571	65.9	4	571	65.9	<u>570</u>	<u>65.9</u>	570	65.9
444.namd	4	366	87.7	<u>366</u>	<u>87.7</u>	366	87.7	4	362	88.6	<u>362</u>	<u>88.6</u>	362	88.7
447.dealII	4	<u>265</u>	<u>173</u>	263	174	270	170	4	<u>265</u>	<u>173</u>	263	174	270	170
450.soplex	4	484	68.9	483	69.1	<u>483</u>	<u>69.0</u>	4	439	76.0	<u>436</u>	<u>76.5</u>	432	77.3
453.povray	4	<u>129</u>	<u>165</u>	129	165	129	164	4	110	193	109	194	<u>110</u>	<u>194</u>
454.calculix	4	215	153	216	153	<u>215</u>	<u>153</u>	4	215	153	216	153	<u>215</u>	<u>153</u>
459.GemsFDTD	4	746	56.9	<u>745</u>	<u>57.0</u>	744	57.1	4	746	56.9	<u>745</u>	<u>57.0</u>	744	57.1
465.tonto	4	304	129	302	130	<u>302</u>	<u>130</u>	4	<u>295</u>	<u>133</u>	294	134	295	133
470.lbm	4	458	120	457	120	<u>458</u>	<u>120</u>	4	<u>450</u>	<u>122</u>	450	122	450	122
481.wrf	4	385	116	386	116	<u>385</u>	<u>116</u>	4	385	116	386	116	<u>385</u>	<u>116</u>
482.sphinx3	4	<u>795</u>	<u>98.0</u>	794	98.2	797	97.8	4	784	99.5	783	99.6	<u>783</u>	<u>99.5</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Huge pages were not configured for this run

Platform Notes

Default BIOS settings were used.



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General Notes

The Express5800/GT110d (Intel Xeon E3-1220) and the Express5800/GT110d-S (Intel Xeon E3-1220) models are electronically equivalent. The results have been measured on the Express5800/GT110d-S (Intel Xeon E3-1220) model.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

Continued on next page



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Base Optimization Flags (Continued)

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

Continued on next page



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Peak Optimization Flags (Continued)

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revF.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revF.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 18:26:45 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 7 June 2011.