



SPEC® CFP2006 Result

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Supermicro

SuperServer 5017C-LF (X9SCL-F, Intel Xeon E3-1260L)

SPECfp®_rate2006 = 99.5

SPECfp_rate_base2006 = 96.0

CPU2006 license: 001176

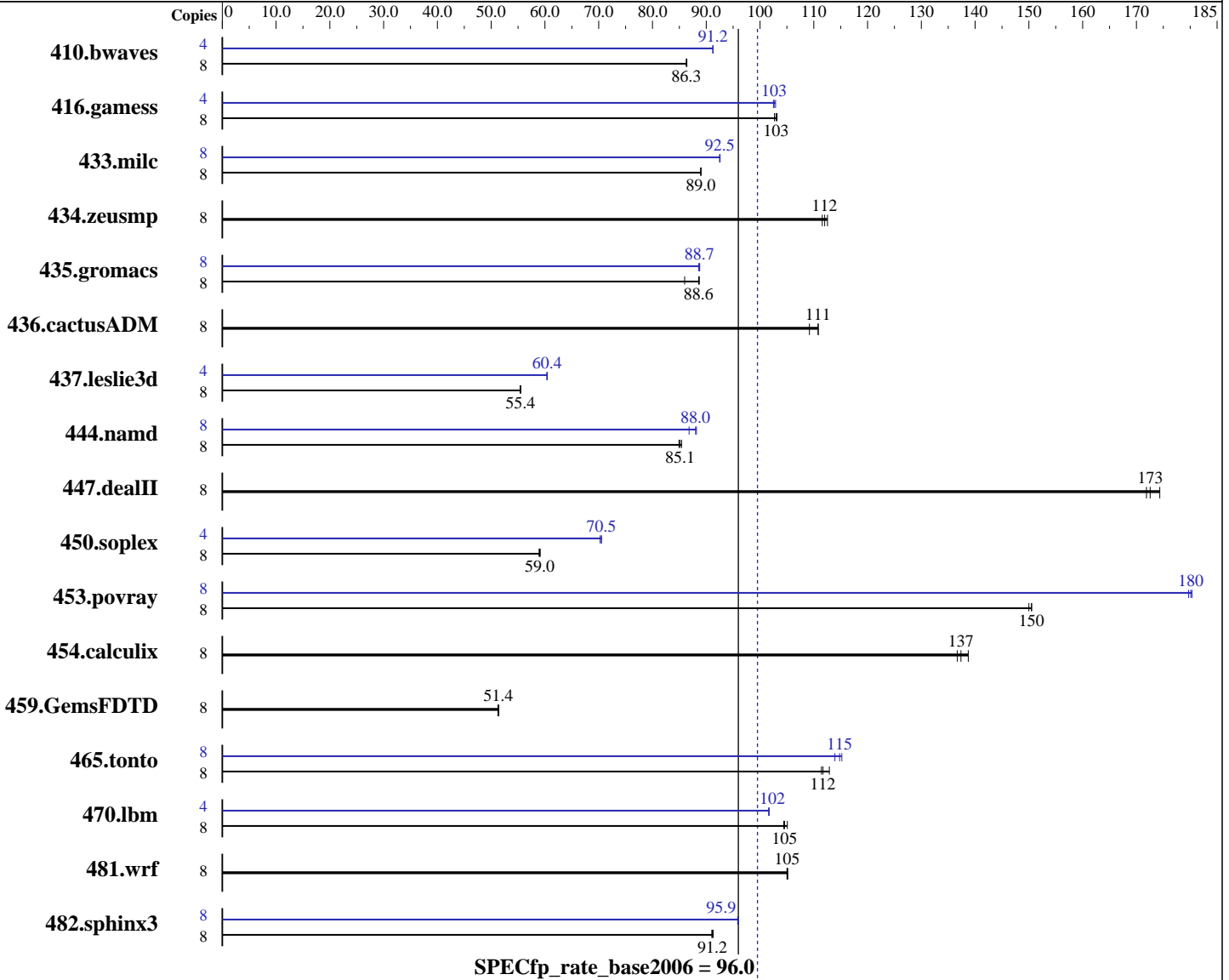
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2011

Hardware Availability: Apr-2011

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon E3-1260L
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1
 Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE
 for applications running on Intel 64
 Version 12.0.1.116 Build 20101116
 Binaries compiled on RHEL5.5 with
 binutils-2.17.50.0.6-14.el5
 Auto Parallel: No
 File System: ext3

Continued on next page



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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4 x 4 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 2000 GB SATA II, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1258	86.4	<u>1260</u>	<u>86.3</u>	1260	86.3	4	<u>596</u>	<u>91.2</u>	596	91.3	596	91.2
416.gamess	8	1518	103	<u>1521</u>	<u>103</u>	1526	103	4	<u>763</u>	<u>103</u>	761	103	764	102
433.milc	8	<u>825</u>	<u>89.0</u>	825	89.0	825	89.0	8	793	92.6	794	92.5	<u>794</u>	<u>92.5</u>
434.zeusmp	8	652	112	<u>650</u>	<u>112</u>	647	113	8	652	112	<u>650</u>	<u>112</u>	647	113
435.gromacs	8	<u>645</u>	<u>88.6</u>	644	88.7	664	86.0	8	645	88.6	<u>644</u>	<u>88.7</u>	643	88.8
436.cactusADM	8	876	109	863	111	<u>863</u>	<u>111</u>	8	876	109	863	111	<u>863</u>	<u>111</u>
437.leslie3d	8	1355	55.5	<u>1356</u>	<u>55.4</u>	1358	55.4	4	622	60.5	<u>622</u>	<u>60.4</u>	623	60.4
444.namd	8	<u>754</u>	<u>85.1</u>	751	85.4	756	84.9	8	739	86.8	728	88.2	<u>729</u>	<u>88.0</u>
447.dealII	8	<u>530</u>	<u>173</u>	533	172	525	174	8	<u>530</u>	<u>173</u>	533	172	525	174
450.soplex	8	1129	59.1	1133	58.9	<u>1130</u>	<u>59.0</u>	4	<u>473</u>	<u>70.5</u>	475	70.3	473	70.5
453.povray	8	<u>283</u>	<u>150</u>	283	151	284	150	8	<u>236</u>	<u>180</u>	236	180	237	180
454.calculix	8	483	137	476	139	<u>480</u>	<u>137</u>	8	483	137	476	139	<u>480</u>	<u>137</u>
459.GemsFDTD	8	<u>1652</u>	<u>51.4</u>	1650	51.4	1656	51.3	8	<u>1652</u>	<u>51.4</u>	1650	51.4	1656	51.3
465.tonto	8	<u>705</u>	<u>112</u>	697	113	707	111	8	<u>686</u>	<u>115</u>	683	115	691	114
470.lbm	8	1046	105	<u>1051</u>	<u>105</u>	1053	104	4	540	102	541	102	<u>541</u>	<u>102</u>
481.wrf	8	850	105	<u>851</u>	<u>105</u>	851	105	8	850	105	<u>851</u>	<u>105</u>	851	105
482.sphinx3	8	1708	91.3	<u>1711</u>	<u>91.2</u>	1713	91.0	8	1627	95.8	1625	96.0	<u>1626</u>	<u>95.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
Hugepages was enabled with the following:
nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 3600 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
```



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Platform Notes

Fan speed set to Full Speed in BIOS Setup.

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -static

Continued on next page



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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32

Continued on next page



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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.xml>

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