



# SPEC<sup>®</sup> CFP2006 Result

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## IBM Corporation

SPECfp<sup>®</sup>\_rate2006 = 134

### IBM System x3650 M3 (Intel Xeon E5606)

SPECfp\_rate\_base2006 = 127

CPU2006 license: 11

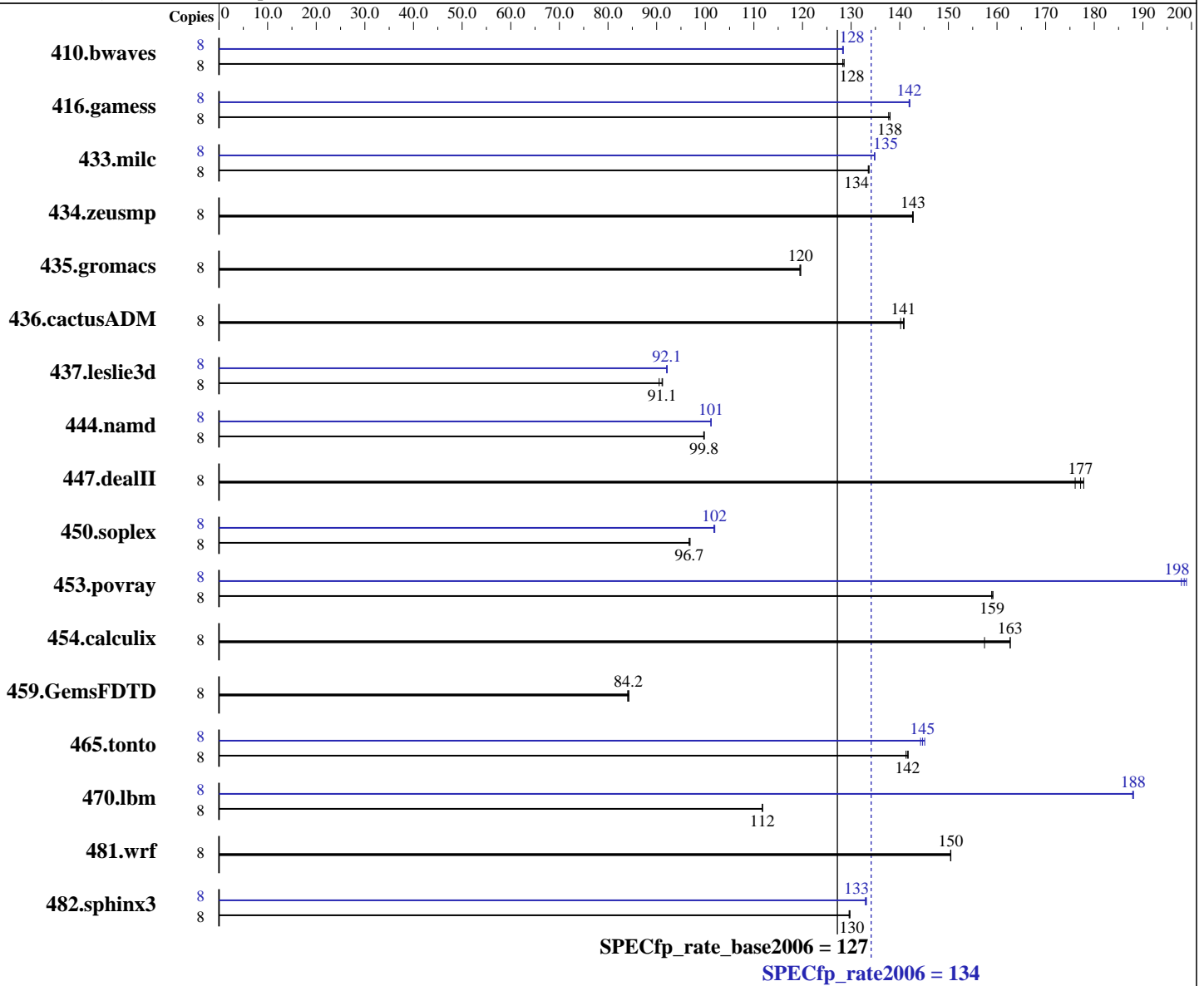
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Mar-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011



#### Hardware

CPU Name: Intel Xeon E5606  
 CPU Characteristics:  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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#### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz)  
Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

### Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	8	<b>847</b>	<b>128</b>	848	128	846	129	8	847	128	<b>847</b>	<b>128</b>	848	128		
416.gamess	8	<b>1135</b>	<b>138</b>	1135	138	1137	138	8	<b>1103</b>	<b>142</b>	1103	142	1104	142		
433.milc	8	550	134	549	134	<b>550</b>	<b>134</b>	8	545	135	<b>545</b>	<b>135</b>	545	135		
434.zeusmp	8	<b>510</b>	<b>143</b>	510	143	510	143	8	<b>510</b>	<b>143</b>	510	143	510	143		
435.gromacs	8	<b>478</b>	<b>120</b>	478	119	478	120	8	<b>478</b>	<b>120</b>	478	119	478	120		
436.cactusADM	8	682	140	<b>679</b>	<b>141</b>	678	141	8	682	140	<b>679</b>	<b>141</b>	678	141		
437.leslie3d	8	831	90.5	<b>825</b>	<b>91.1</b>	824	91.2	8	<b>816</b>	<b>92.1</b>	817	92.0	816	92.2		
444.namd	8	643	99.8	<b>643</b>	<b>99.8</b>	644	99.7	8	634	101	<b>634</b>	<b>101</b>	634	101		
447.dealII	8	520	176	<b>517</b>	<b>177</b>	515	178	8	520	176	<b>517</b>	<b>177</b>	515	178		
450.soplex	8	<b>690</b>	<b>96.7</b>	689	96.9	690	96.7	8	654	102	655	102	<b>655</b>	<b>102</b>		
453.povray	8	<b>268</b>	<b>159</b>	267	159	268	159	8	<b>214</b>	<b>198</b>	214	199	215	198		
454.calculix	8	419	157	406	163	<b>406</b>	<b>163</b>	8	419	157	406	163	<b>406</b>	<b>163</b>		
459.GemsFDTD	8	<b>1008</b>	<b>84.2</b>	1010	84.0	1007	84.3	8	<b>1008</b>	<b>84.2</b>	1010	84.0	1007	84.3		
465.tonto	8	555	142	<b>556</b>	<b>142</b>	557	141	8	<b>544</b>	<b>145</b>	546	144	542	145		
470.lbm	8	<b>984</b>	<b>112</b>	983	112	984	112	8	585	188	585	188	<b>585</b>	<b>188</b>		
481.wrf	8	<b>594</b>	<b>150</b>	594	151	594	150	8	<b>594</b>	<b>150</b>	594	151	594	150		
482.sphinx3	8	1202	130	<b>1203</b>	<b>130</b>	1203	130	8	<b>1172</b>	<b>133</b>	1173	133	1171	133		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

### Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 7200 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```



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## Platform Notes

Power C-State enabled in BIOS  
Data Reuse disabled in BIOS  
Demand Scrub disabled in BIOS

## General Notes

Binaries compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`  
 416.gamess: `-DSPEC_CPU_LP64`  
 433.milc: `-DSPEC_CPU_LP64`  
 434.zeusmp: `-DSPEC_CPU_LP64`  
 435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`  
 436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`  
 437.leslie3d: `-DSPEC_CPU_LP64`  
 444.namd: `-DSPEC_CPU_LP64`  
 447.dealII: `-DSPEC_CPU_LP64`  
 453.povray: `-DSPEC_CPU_LP64`  
 454.calculix: `-DSPEC_CPU_LP64 -nofor_main`  
 459.GemsFDTD: `-DSPEC_CPU_LP64`  
 465.tonto: `-DSPEC_CPU_LP64`

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64

481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

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## Peak Optimization Flags (Continued)

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.xml>

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