



# SPEC® CFP2006 Result

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Acer Incorporated

**SPECfp®\_rate2006 = 262**

Acer AR360 F1 (Intel Xeon X5675)

**SPECfp\_rate\_base2006 = 255**

CPU2006 license: 97

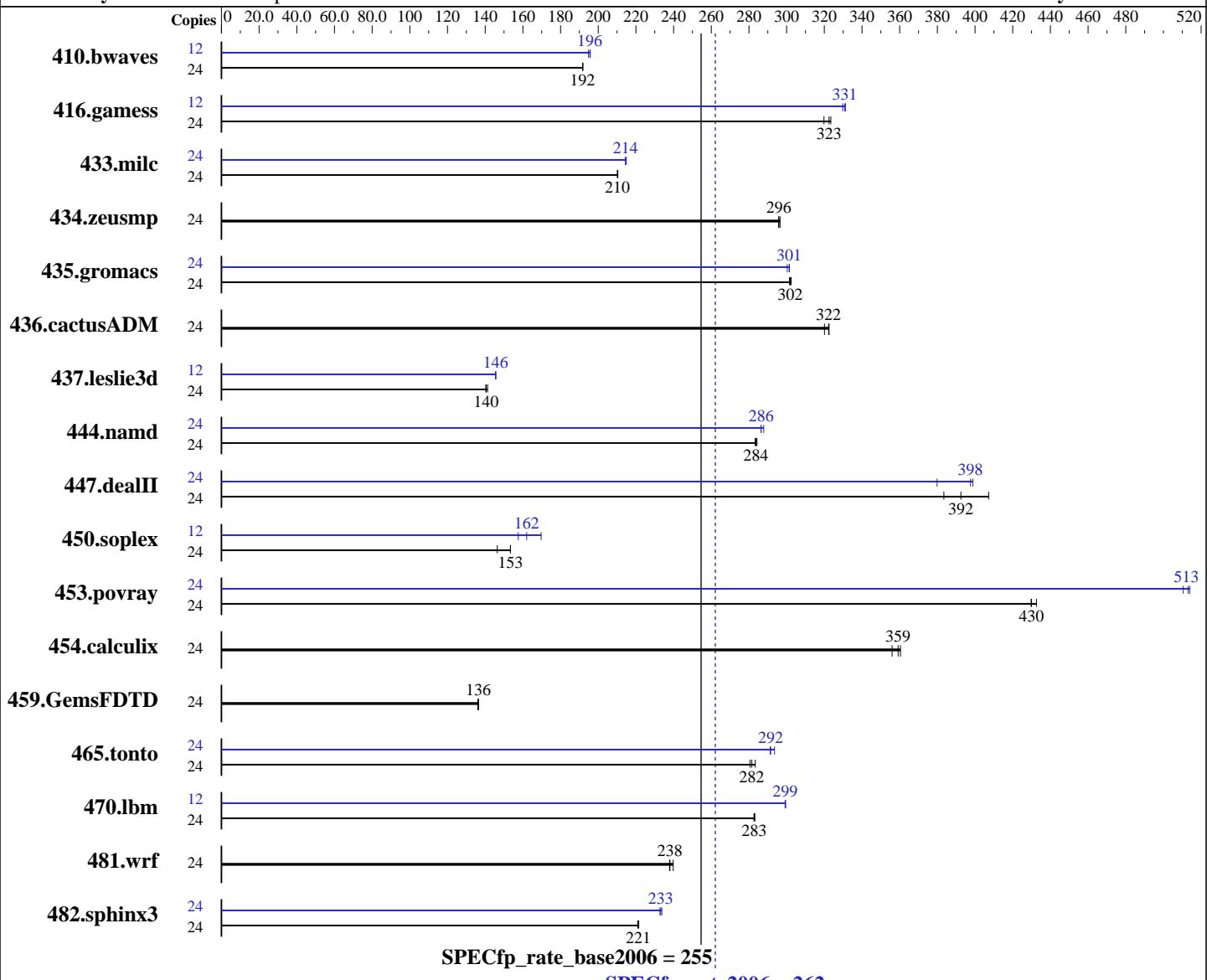
Test date: Mar-2011

Test sponsor: Acer Incorporated

Hardware Availability: Feb-2011

Tested by: Acer Incorporated

Software Availability: Jan-2011



## Hardware

CPU Name: Intel Xeon X5675  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz  
 CPU MHz: 3067  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9,ECC)  
 Disk Subsystem: 1 x 300 GB SATA, 10000 RPM SATA HDD  
 Other Hardware: None

Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1700	192	<b>1701</b>	<b>192</b>	1702	192	12	833	196	<b>834</b>	<b>196</b>	837	195
416.gamess	24	<b>1457</b>	<b>323</b>	1469	320	1453	323	12	713	330	709	331	<b>710</b>	<b>331</b>
433.milc	24	<b>1048</b>	<b>210</b>	1047	210	1048	210	24	1028	214	<b>1027</b>	<b>214</b>	1025	215
434.zeusmp	24	737	296	739	296	<b>738</b>	<b>296</b>	24	737	296	739	296	<b>738</b>	<b>296</b>
435.gromacs	24	568	302	<b>567</b>	<b>302</b>	567	302	24	571	300	568	301	<b>569</b>	<b>301</b>
436.cactusADM	24	889	323	896	320	<b>890</b>	<b>322</b>	24	889	323	896	320	<b>890</b>	<b>322</b>
437.leslie3d	24	1607	140	1597	141	<b>1606</b>	<b>140</b>	12	774	146	776	145	<b>775</b>	<b>146</b>
444.namd	24	679	283	<b>678</b>	<b>284</b>	677	284	24	669	288	672	286	<b>672</b>	<b>286</b>
447.dealII	24	<b>700</b>	<b>392</b>	716	383	674	407	24	<b>691</b>	<b>398</b>	723	380	688	399
450.soplex	24	1367	146	<b>1305</b>	<b>153</b>	1305	153	12	635	158	<b>618</b>	<b>162</b>	590	170
453.povray	24	295	433	297	430	<b>297</b>	<b>430</b>	24	250	510	<b>249</b>	<b>513</b>	248	514
454.calculix	24	556	356	549	360	<b>551</b>	<b>359</b>	24	556	356	549	360	<b>551</b>	<b>359</b>
459.GemsFDTD	24	1871	136	<b>1868</b>	<b>136</b>	1867	136	24	1871	136	<b>1868</b>	<b>136</b>	1867	136
465.tonto	24	842	281	<b>839</b>	<b>282</b>	833	283	24	804	294	<b>810</b>	<b>292</b>	811	291
470.lbm	24	1165	283	1166	283	<b>1166</b>	<b>283</b>	12	551	299	551	299	<b>551</b>	<b>299</b>
481.wrf	24	1118	240	1127	238	<b>1126</b>	<b>238</b>	24	1118	240	1127	238	<b>1126</b>	<b>238</b>
482.sphinx3	24	2117	221	<b>2114</b>	<b>221</b>	2112	221	24	2010	233	<b>2006</b>	<b>233</b>	2001	234

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
 numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
 Large pages were disabled for this run

## Platform Notes

BIOS Settings:  
 Fan speed = full speed (Default = Balanced)  
 Data Reuse = Disabled (Default = Enabled)



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## General Notes

Binaries compiled on RHEL5.5

This result was measured on the Gateway GR360 F1.

The Acer AR360 F1 is electronically equivalent

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll12

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfss/ -Wl,-hugetlbfss-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias  
-B /usr/share/libhugetlbfss/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfss-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-inline-level=0 -scalar-rep -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfss/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfss-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -auto  
-inline-alloc -opt-malloc-options=3  
-B /usr/share/libhugetlbfss/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfss-link=BDT

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>  
<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>  
<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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