



# SPEC® CFP2006 Result

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## Bull SAS

### SPECfp®\_rate2006 = 131

### BL265+ (Intel Xeon E5507, 2.26 GHz)

### SPECfp\_rate\_base2006 = 126

CPU2006 license: 20

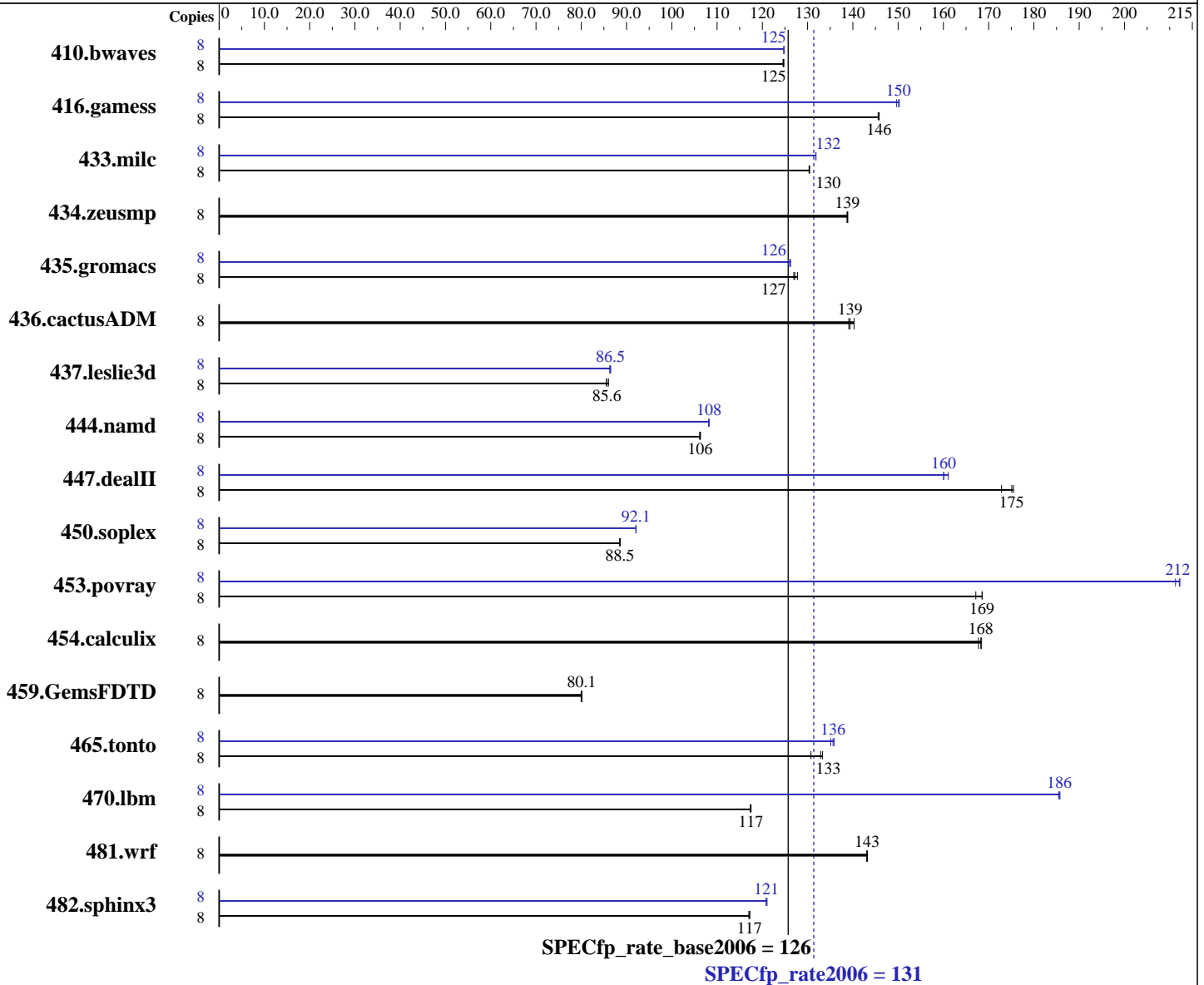
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Feb-2011

Hardware Availability: May-2010

Software Availability: Nov-2010



#### Hardware

CPU Name: Intel Xeon E5507  
 CPU Characteristics:  
 CPU MHz: 2267  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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#### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 4 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)  
Disk Subsystem: 2 x 50 GB SATA, SSD  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

### Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	8	873	125	871	125	<b>872</b>	<b>125</b>	8	<b>871</b>	<b>125</b>	871	125	871	125		
416.gamess	8	<b>1075</b>	<b>146</b>	1076	146	1074	146	8	1042	150	1047	150	<b>1044</b>	<b>150</b>		
433.milc	8	<b>563</b>	<b>130</b>	563	130	564	130	8	<b>557</b>	<b>132</b>	557	132	557	132		
434.zeusmp	8	525	139	524	139	<b>525</b>	<b>139</b>	8	525	139	524	139	<b>525</b>	<b>139</b>		
435.gromacs	8	447	128	450	127	<b>449</b>	<b>127</b>	8	<b>453</b>	<b>126</b>	454	126	452	126		
436.cactusADM	8	682	140	<b>686</b>	<b>139</b>	687	139	8	682	140	<b>686</b>	<b>139</b>	687	139		
437.leslie3d	8	879	85.6	<b>878</b>	<b>85.6</b>	874	86.0	8	<b>870</b>	<b>86.5</b>	872	86.3	869	86.5		
444.namd	8	604	106	603	106	<b>604</b>	<b>106</b>	8	<b>593</b>	<b>108</b>	593	108	593	108		
447.dealII	8	529	173	<b>523</b>	<b>175</b>	522	175	8	572	160	568	161	<b>572</b>	<b>160</b>		
450.soplex	8	753	88.6	<b>754</b>	<b>88.5</b>	754	88.5	8	<b>724</b>	<b>92.1</b>	724	92.1	724	92.1		
453.povray	8	252	169	255	167	<b>253</b>	<b>169</b>	8	201	211	201	212	<b>201</b>	<b>212</b>		
454.calculix	8	<b>392</b>	<b>168</b>	392	168	393	168	8	<b>392</b>	<b>168</b>	392	168	393	168		
459.GemsFDTD	8	1061	80.0	1059	80.2	<b>1060</b>	<b>80.1</b>	8	1061	80.0	1059	80.2	<b>1060</b>	<b>80.1</b>		
465.tonto	8	602	131	<b>592</b>	<b>133</b>	591	133	8	<b>580</b>	<b>136</b>	579	136	583	135		
470.lbm	8	935	118	<b>936</b>	<b>117</b>	937	117	8	592	186	593	185	<b>592</b>	<b>186</b>		
481.wrf	8	625	143	624	143	<b>624</b>	<b>143</b>	8	625	143	624	143	<b>624</b>	<b>143</b>		
482.sphinx3	8	1330	117	<b>1330</b>	<b>117</b>	1332	117	8	<b>1290</b>	<b>121</b>	1288	121	1290	121		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

### Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
Hugepages was enabled with the following:  
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab  
echo 3600 > /proc/sys/vm/nr\_hugepages  
export HUGETLB\_MORECORE=yes  
export LD\_PRELOAD=/usr/lib64/libhugetlbfs.so



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## Platform Notes

Power C-states enabled in BIOS  
Demand Scrub disabled in BIOS

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`482.sphinx3:icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

`450.soplex:icpc -m32`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`  
 416.gamess: `-DSPEC_CPU_LP64`  
 433.milc: `-DSPEC_CPU_LP64`  
 434.zeusmp: `-DSPEC_CPU_LP64`  
 435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`  
 436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`  
 437.leslie3d: `-DSPEC_CPU_LP64`  
 444.namd: `-DSPEC_CPU_LP64`  
 447.dealII: `-DSPEC_CPU_LP64`  
 453.povray: `-DSPEC_CPU_LP64`  
 454.calculix: `-DSPEC_CPU_LP64 -nofor_main`  
 459.GemsFDTD: `-DSPEC_CPU_LP64`  
 465.tonto: `-DSPEC_CPU_LP64`  
 470.lbm: `-DSPEC_CPU_LP64`  
 481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`



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## Peak Optimization Flags

### C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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