



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 263

PRIMERGY RX300 S6, Intel Xeon X5675, 3.07 GHz

SPECfp_rate_base2006 = 256

CPU2006 license: 19

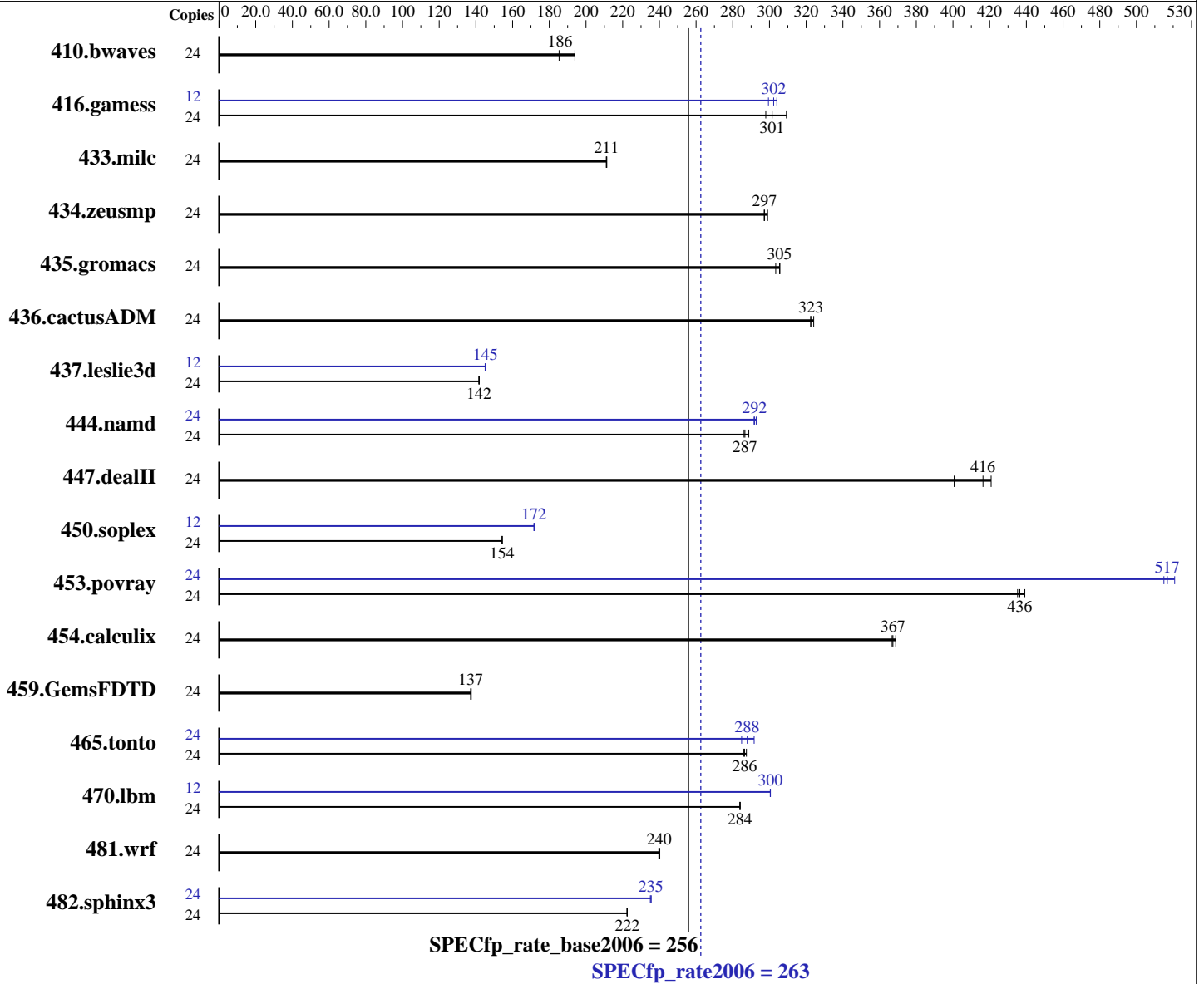
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2010

Hardware Availability: Feb-2011

Software Availability: Nov-2010



Hardware

CPU Name: Intel Xeon X5675
 CPU Characteristics: Intel Turbo Boost Technology up to 3.47 GHz
 CPU MHz: 3067
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64, Version 12.0.0.082 Build 20101006
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
Other Hardware: --

Peak Pointers: 32/64-bit
Other Software: none

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1681	194	<u>1755</u>	<u>186</u>	1760	185	24	1681	194	<u>1755</u>	<u>186</u>	1760	185
416.gamess	24	1520	309	<u>1559</u>	<u>301</u>	1577	298	12	773	304	<u>777</u>	<u>302</u>	785	299
433.milc	24	<u>1043</u>	<u>211</u>	1044	211	1042	211	24	<u>1043</u>	<u>211</u>	1044	211	1042	211
434.zeusmp	24	<u>735</u>	<u>297</u>	731	299	735	297	24	<u>735</u>	<u>297</u>	731	299	735	297
435.gromacs	24	565	303	<u>561</u>	<u>305</u>	561	306	24	565	303	<u>561</u>	<u>305</u>	561	306
436.cactusADM	24	<u>889</u>	<u>323</u>	885	324	889	322	24	<u>889</u>	<u>323</u>	885	324	889	322
437.leslie3d	24	1590	142	<u>1592</u>	<u>142</u>	1593	142	12	777	145	778	145	<u>777</u>	<u>145</u>
444.namd	24	<u>672</u>	<u>287</u>	667	289	673	286	24	660	292	<u>659</u>	<u>292</u>	657	293
447.dealII	24	652	421	685	401	<u>659</u>	<u>416</u>	24	652	421	685	401	<u>659</u>	<u>416</u>
450.soplex	24	1297	154	1297	154	<u>1297</u>	<u>154</u>	12	583	172	583	172	<u>583</u>	<u>172</u>
453.povray	24	293	435	<u>293</u>	<u>436</u>	291	439	24	245	521	248	515	<u>247</u>	<u>517</u>
454.calculix	24	537	369	540	367	<u>539</u>	<u>367</u>	24	537	369	540	367	<u>539</u>	<u>367</u>
459.GemsFDTD	24	1856	137	<u>1856</u>	<u>137</u>	1852	137	24	1856	137	<u>1856</u>	<u>137</u>	1852	137
465.tonto	24	822	287	<u>825</u>	<u>286</u>	826	286	24	<u>820</u>	<u>288</u>	810	292	829	285
470.lbm	24	1162	284	1161	284	<u>1162</u>	<u>284</u>	12	549	301	<u>549</u>	<u>300</u>	549	300
481.wrf	24	1116	240	1118	240	<u>1118</u>	<u>240</u>	24	1116	240	1118	240	<u>1118</u>	<u>240</u>
482.sphinx3	24	<u>2102</u>	<u>222</u>	2102	222	2104	222	24	1986	235	1990	235	<u>1987</u>	<u>235</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Large pages were not enabled for this run

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disable



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General Notes

This result was measured on the PRIMERGY RX300 S6. The PRIMERGY TX300 S6 and the PRIMERGY RX300 S6 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 SP1 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.deallI: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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Peak Optimization Flags (Continued)

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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