



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 117

PRIMERGY TX200 S6, Intel Xeon E5645, 2.40 GHz

SPECfp_rate_base2006 = 114

CPU2006 license: 19

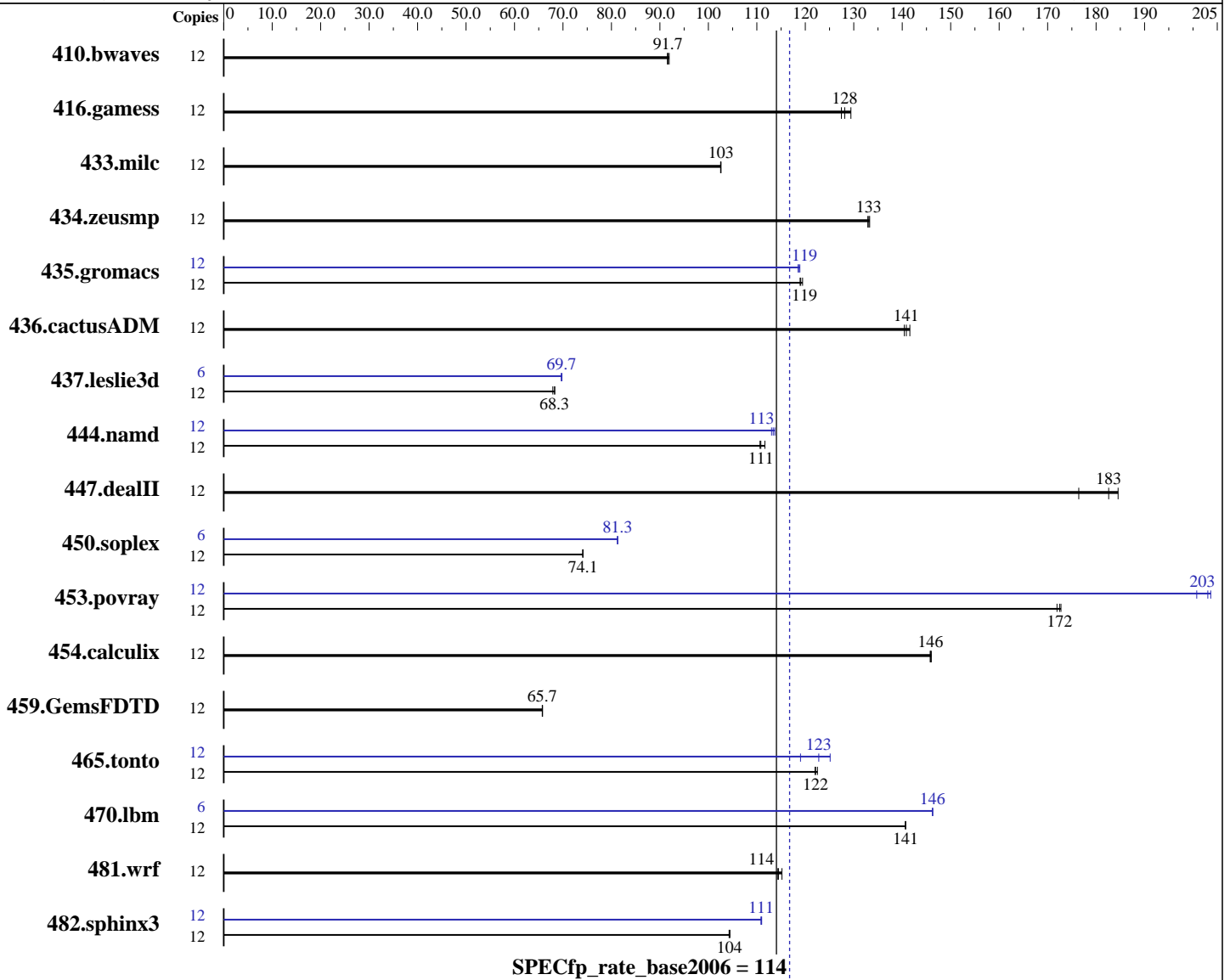
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2010

Hardware Availability: Jul-2010

Software Availability: Nov-2010



Hardware

CPU Name: Intel Xeon E5645
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64, Version 12.0.0.082 Build 20101006
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
Other Hardware: --

Peak Pointers: 32/64-bit
Other Software: none

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	1774	91.9	<u>1779</u>	<u>91.7</u>	1781	91.5	12	1774	91.9	<u>1779</u>	<u>91.7</u>	1781	91.5
416.gamess	12	1844	127	<u>1834</u>	<u>128</u>	1816	129	12	1844	127	<u>1834</u>	<u>128</u>	1816	129
433.milc	12	1074	103	<u>1074</u>	<u>103</u>	1075	103	12	1074	103	<u>1074</u>	<u>103</u>	1075	103
434.zeusmp	12	819	133	<u>820</u>	<u>133</u>	822	133	12	819	133	<u>820</u>	<u>133</u>	822	133
435.gromacs	12	<u>720</u>	<u>119</u>	720	119	717	119	12	<u>722</u>	<u>119</u>	723	119	721	119
436.cactusADM	12	1021	140	<u>1018</u>	<u>141</u>	1013	142	12	1021	140	<u>1018</u>	<u>141</u>	1013	142
437.leslie3d	12	1661	67.9	1651	68.3	<u>1653</u>	<u>68.3</u>	6	<u>809</u>	<u>69.7</u>	808	69.8	810	69.7
444.namd	12	870	111	862	112	<u>868</u>	<u>111</u>	12	846	114	851	113	<u>849</u>	<u>113</u>
447.dealII	12	744	185	778	176	<u>752</u>	<u>183</u>	12	744	185	778	176	<u>752</u>	<u>183</u>
450.soplex	12	1350	74.1	1351	74.1	<u>1350</u>	<u>74.1</u>	6	<u>616</u>	<u>81.3</u>	616	81.3	616	81.3
453.povray	12	<u>370</u>	<u>172</u>	371	172	370	173	12	318	201	313	204	<u>314</u>	<u>203</u>
454.calculix	12	<u>679</u>	<u>146</u>	679	146	678	146	12	<u>679</u>	<u>146</u>	679	146	678	146
459.GemsFDTD	12	<u>1937</u>	<u>65.7</u>	1937	65.7	1935	65.8	12	<u>1937</u>	<u>65.7</u>	1937	65.7	1935	65.8
465.tonto	12	<u>967</u>	<u>122</u>	968	122	964	123	12	992	119	<u>962</u>	<u>123</u>	944	125
470.lbm	12	<u>1172</u>	<u>141</u>	1172	141	1172	141	6	564	146	<u>564</u>	<u>146</u>	564	146
481.wrf	12	<u>1171</u>	<u>114</u>	1172	114	1164	115	12	<u>1171</u>	<u>114</u>	1172	114	1164	115
482.sphinx3	12	2239	104	<u>2241</u>	<u>104</u>	2243	104	12	<u>2109</u>	<u>111</u>	2109	111	2107	111

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Large pages were not enabled for this run

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disable



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General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 SP1 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32

436.cactusADM: basepeak = yes

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Peak Optimization Flags (Continued)

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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