SPEC® CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX620 S6, Intel Xeon X5667, 3.07 GHz

$SPECfp^{®}\_rate2006 =$ 213

$SPECfp\_rate\_base2006 =$ 205

CPU2006 license: 19	**Test date:** Sep-2010
Test sponsor: Fujitsu	**Hardware Availability:** Aug-2010
Tested by: Fujitsu	**Software Availability:** Jan-2010

Copies 0 15.0 30.0 45.0 60.0 75.0 90.0 105 120 135 150 165 180 195 210 225 240 255 270 285 300 315 330 355

Benchmark	Copies	Value
410.bwaves	8	201
	16	196
416.gamess	16	213
433.milc	16	217
	16	216
434.zeusmp	16	228
435.gromacs	16	207
	16	205
436.cactusADM	16	246
437.leslie3d	8	141
	16	134
444.namd	16	199
	16	197
447.dealII	16	336
450.soplex	8	167
	16	157
453.povray	16	351
	16	292
454.calculix	16	258
459.GemsFDTD	8	132
	16	126
465.tonto	16	259
	16	237
470.lbm	8	133
	16	125
481.wrf	16	238
482.sphinx3	16	230
	16	215

SPECfp_rate_base2006 = 205

SPECfp_rate2006 = 213

Hardware

CPU Name:	Intel Xeon X5667
CPU Characteristics:	Intel Turbo Boost Technology up to 3.46 GHz
CPU MHz:	3067
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,3,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)

Continued on next page

SPEC CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX620 S6, Intel Xeon X5667, 3.07 GHz

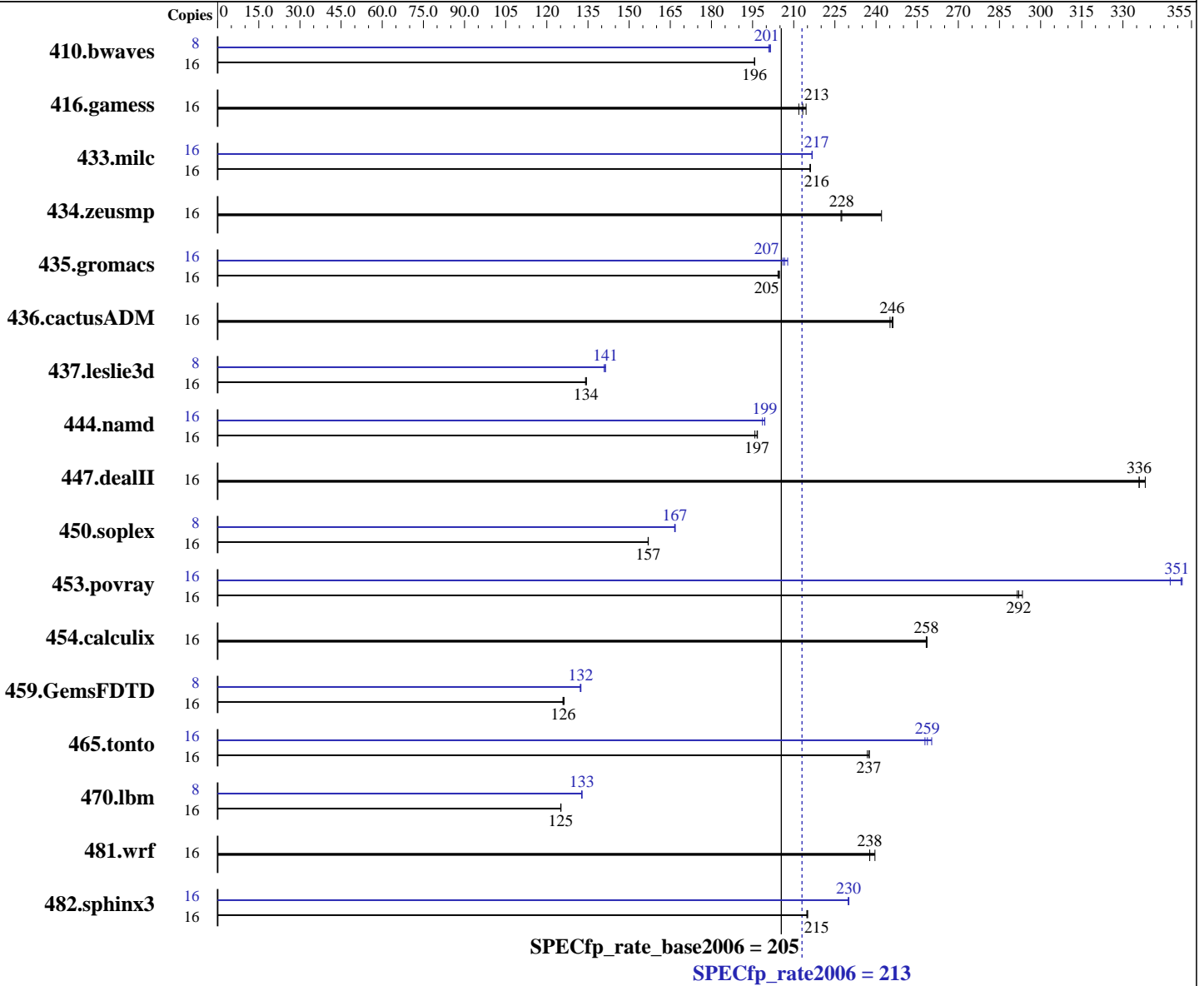| SPECfp_rate2006 =	213
SPECfp_rate_base2006 =	205

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Sep-2010
Hardware Availability: Aug-2010
Software Availability: Jan-2010

L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SAS, 300 GB, 10000 RPM
Other Hardware:	--

Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1111	196	**1111**	**196**	1111	196	8	541	201	539	202	**540**	**201**
416.gamess	16	1479	212	**1469**	**213**	1460	215	16	1479	212	**1469**	**213**	1460	215
433.milc	16	**680**	**216**	680	216	680	216	16	678	217	**678**	**217**	678	217
434.zeusmp	16	**640**	**228**	602	242	641	227	16	**640**	**228**	602	242	641	227
435.gromacs	16	**558**	**205**	559	204	558	205	16	554	206	550	208	**553**	**207**
436.cactusADM	16	**778**	**246**	780	245	777	246	16	**778**	**246**	780	245	777	246
437.leslie3d	16	1122	134	1119	134	**1120**	**134**	8	534	141	532	141	**532**	**141**
444.namd	16	655	196	652	197	**653**	**197**	16	643	199	**643**	**199**	646	199
447.dealII	16	541	338	545	336	**545**	**336**	16	541	338	545	336	**545**	**336**
450.soplex	16	**850**	**157**	850	157	850	157	8	**400**	**167**	401	167	400	167
453.povray	16	290	293	292	291	**291**	**292**	16	242	352	245	347	**242**	**351**
454.calculix	16	510	259	511	258	**511**	**258**	16	510	259	511	258	**511**	**258**
459.GemsFDTD	16	**1347**	**126**	1344	126	1349	126	8	**641**	**132**	641	132	642	132
465.tonto	16	663	238	**663**	**237**	665	237	16	611	258	605	260	**609**	**259**
470.lbm	16	1756	125	**1758**	**125**	1758	125	8	828	133	**828**	**133**	828	133
481.wrf	16	746	240	**752**	**238**	752	238	16	746	240	**752**	**238**	752	238
482.sphinx3	16	1450	215	1452	215	**1451**	**215**	16	1355	230	1357	230	**1356**	**230**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
    BIOS configuration:
    Data Reuse Optimization = Disable
    Performance/Power Setting = Traditional
```

SPEC CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX620 S6, Intel Xeon X5667, 3.07 GHz

SPECfp_rate2006 =	213
SPECfp_rate_base2006 =	205

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Sep-2010
Hardware Availability: Aug-2010
Software Availability: Jan-2010

General Notes

For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
    icc   -m64

C++ benchmarks:
    icpc   -m64

Fortran benchmarks:
    ifort -m64

Benchmarks using both Fortran and C:
    icc   -m64 ifort -m64

Base Portability Flags

       410.bwaves: -DSPEC_CPU_LP64
       416.gamess: -DSPEC_CPU_LP64
         433.milc: -DSPEC_CPU_LP64
       434.zeusmp: -DSPEC_CPU_LP64
      435.gromacs: -DSPEC_CPU_LP64 -nofor_main
    436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
      437.leslie3d: -DSPEC_CPU_LP64
         444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        450.soplex: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
      454.calculix: -DSPEC_CPU_LP64 -nofor_main
     459.GemsFDTD: -DSPEC_CPU_LP64
         465.tonto: -DSPEC_CPU_LP64
          470.lbm: -DSPEC_CPU_LP64
          481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
       482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

Continued on next page

SPEC CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX620 S6, Intel Xeon X5667, 3.07 GHz

SPECfp_rate2006 =	213
SPECfp_rate_base2006 =	205

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Sep-2010
Hardware Availability: Aug-2010
Software Availability: Jan-2010

Base Optimization Flags (Continued)

Fortran benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Benchmarks using both Fortran and C:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc   -m64
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
       433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
      447.dealII: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

SPEC CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX620 S6, Intel Xeon X5667, 3.07 GHz

$SPECfp\_rate2006 =$ 213

$SPECfp\_rate\_base2006 =$ 205

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Sep-2010
Hardware Availability: Aug-2010
Software Availability: Jan-2010

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

Continued on next page

Fujitsu

PRIMERGY BX620 S6, Intel Xeon X5667, 3.07 GHz

$SPECfp\_rate2006 =$ 213

$SPECfp\_rate\_base2006 =$ 205

CPU2006 license: 19	**Test date:** Sep-2010
Test sponsor: Fujitsu	**Hardware Availability:** Aug-2010
Tested by: Fujitsu	**Software Availability:** Jan-2010

Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 14:50:28 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 October 2010.