



SPEC® CINT2006 Result

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Supermicro

Motherboard X8SIT-F (Intel Pentium G6950, 2.80 GHz)

SPECint®_rate2006 = 43.0

SPECint_rate_base2006 = 39.2

CPU2006 license: 001176

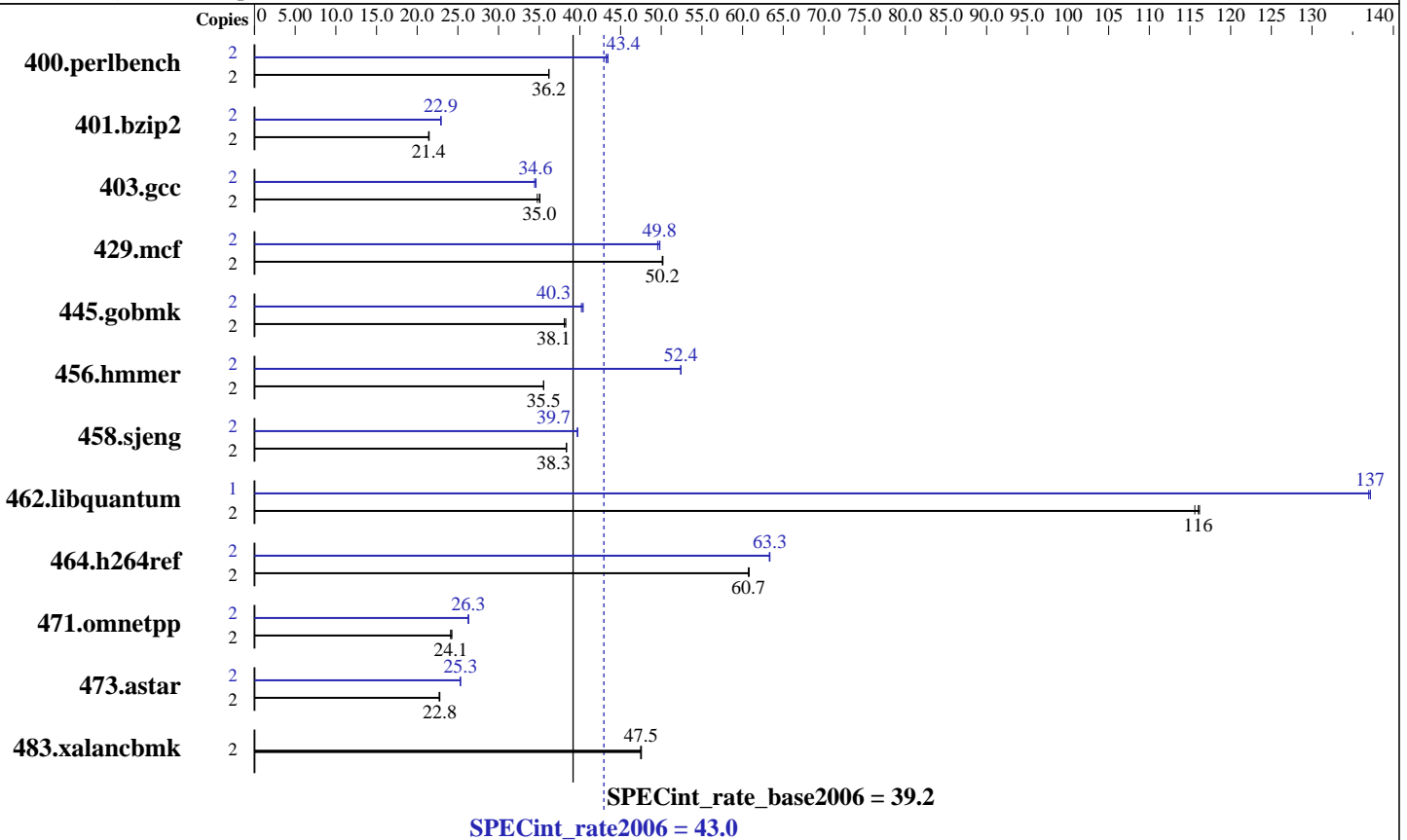
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Aug-2010

Hardware Availability: Jun-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Pentium G6950
 CPU Characteristics:
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 3 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 2Rx8 DDR3-1333 UDIMM, ECC, CL9)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: None



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	540	36.2	540	36.2	540	36.2	2	450	43.5	452	43.2	450	43.4
401.bzip2	2	902	21.4	899	21.5	900	21.4	2	842	22.9	842	22.9	843	22.9
403.gcc	2	459	35.1	463	34.8	460	35.0	2	468	34.4	465	34.6	466	34.6
429.mcf	2	364	50.1	363	50.2	364	50.2	2	366	49.8	368	49.6	366	49.8
445.gobmk	2	550	38.1	551	38.1	548	38.3	2	519	40.4	521	40.3	522	40.2
456.hammer	2	525	35.5	525	35.6	525	35.5	2	356	52.4	356	52.4	356	52.4
458.sjeng	2	631	38.3	631	38.3	631	38.3	2	610	39.7	609	39.7	609	39.7
462.libquantum	2	357	116	357	116	358	116	1	151	137	151	137	151	137
464.h264ref	2	728	60.8	729	60.7	729	60.7	2	699	63.4	699	63.3	699	63.3
471.omnetpp	2	515	24.3	518	24.1	518	24.1	2	475	26.3	475	26.3	476	26.3
473.astar	2	617	22.8	619	22.7	616	22.8	2	555	25.3	555	25.3	555	25.3
483.xalancbmk	2	290	47.6	291	47.5	291	47.5	2	290	47.6	291	47.5	291	47.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

Fan speed set to Full Speed in BIOS Setup.
As tested, the system used a Supermicro CSE-827H-R920B chassis.
The chassis is bundled with a PWS-920P-1R power supply, SNK-P0046P heatsink,
and 4 FAN-00111L4 cooling fans.

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias
403.gcc: -xSSSE3 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3
429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo
-no-prec-div -ansi-alias
456.hmmer: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll4
462.libquantum: -xSSSE3 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -auto-ilp32 -opt-prefetch
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100915.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100915.xml>

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For other inquiries, please contact webmaster@spec.org.

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