



SPEC® CFP2006 Result

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Fujitsu

SPECfp®_rate2006 = 52.8

PRIMERGY TX150 S7, Intel Core i3-550, 3.20 GHz

SPECfp_rate_base2006 = 51.0

CPU2006 license: 19

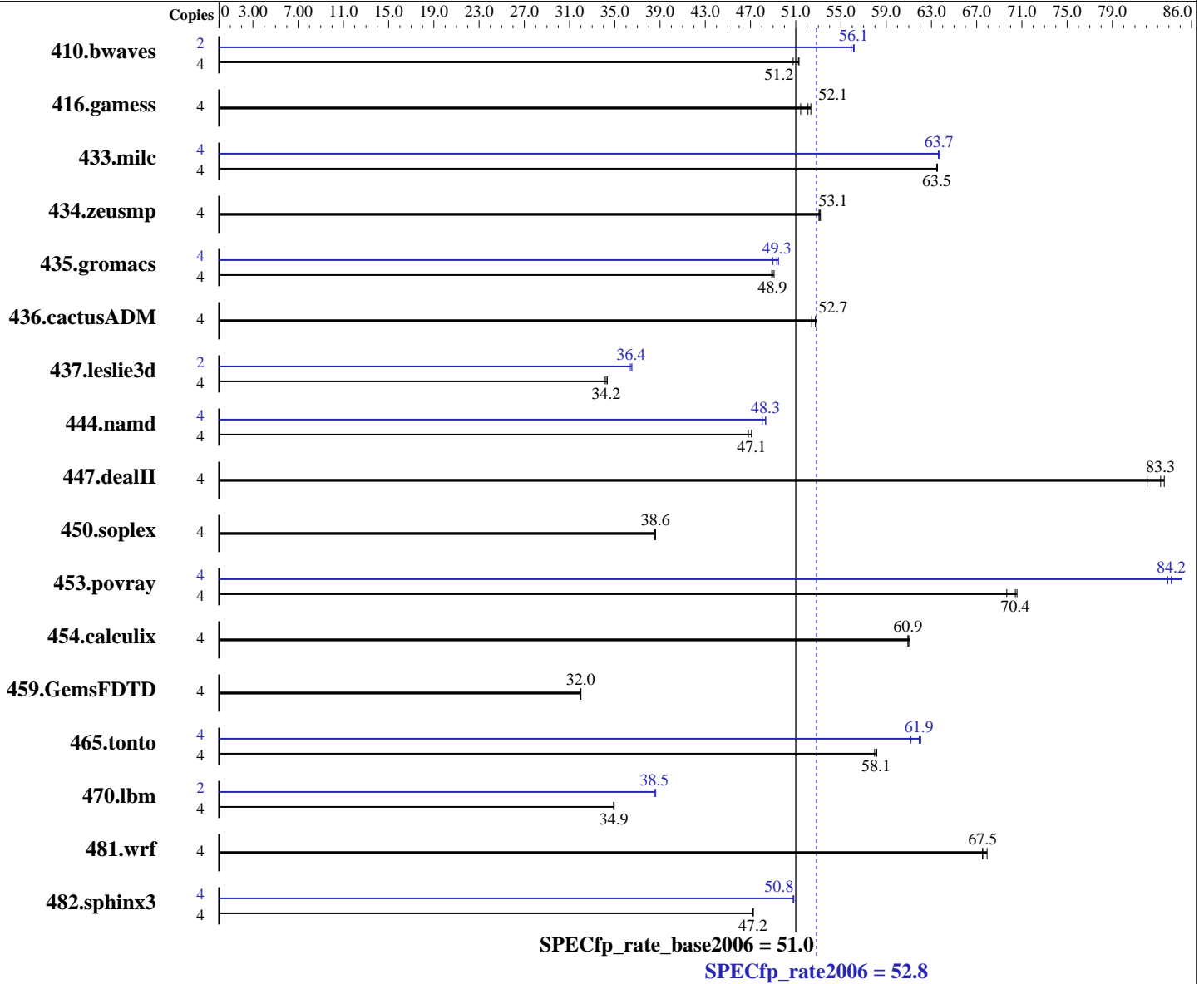
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Core i3-550
 CPU Characteristics:
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC)
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------------|--------------------|--------------------|--------------------|--------------------|--------------------|--------|--------------------|--------------------|--------------------|--------------------|--------------------|--------------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 4 | 1060 | 51.3 | <u>1061</u> | <u>51.2</u> | 1071 | 50.8 | 2 | 486 | 55.9 | 484 | 56.2 | <u>484</u> | <u>56.1</u> |
| 416.gamess | 4 | 1496 | 52.3 | 1523 | 51.4 | <u>1504</u> | <u>52.1</u> | 4 | 1496 | 52.3 | 1523 | 51.4 | <u>1504</u> | <u>52.1</u> |
| 433.milc | 4 | 578 | 63.5 | 578 | 63.5 | <u>578</u> | <u>63.5</u> | 4 | 577 | 63.6 | <u>577</u> | <u>63.7</u> | 576 | 63.7 |
| 434.zeusmp | 4 | 684 | 53.2 | <u>685</u> | <u>53.1</u> | 686 | 53.1 | 4 | 684 | 53.2 | <u>685</u> | <u>53.1</u> | 686 | 53.1 |
| 435.gromacs | 4 | 582 | 49.1 | 584 | 48.9 | <u>584</u> | <u>48.9</u> | 4 | 577 | 49.5 | <u>579</u> | <u>49.3</u> | 583 | 49.0 |
| 436.cactusADM | 4 | 904 | 52.9 | 912 | 52.4 | <u>906</u> | <u>52.7</u> | 4 | 904 | 52.9 | 912 | 52.4 | <u>906</u> | <u>52.7</u> |
| 437.leslie3d | 4 | 1095 | 34.3 | 1103 | 34.1 | <u>1098</u> | <u>34.2</u> | 2 | 518 | 36.3 | 515 | 36.5 | <u>516</u> | <u>36.4</u> |
| 444.namd | 4 | 685 | 46.8 | <u>681</u> | <u>47.1</u> | 681 | 47.1 | 4 | 668 | 48.0 | 664 | 48.3 | <u>664</u> | <u>48.3</u> |
| 447.dealII | 4 | 558 | 82.1 | <u>550</u> | <u>83.3</u> | 547 | 83.6 | 4 | 558 | 82.1 | <u>550</u> | <u>83.3</u> | 547 | 83.6 |
| 450.soplex | 4 | 866 | 38.5 | 864 | 38.6 | <u>864</u> | <u>38.6</u> | 4 | 866 | 38.5 | 864 | 38.6 | <u>864</u> | <u>38.6</u> |
| 453.povray | 4 | 305 | 69.7 | 302 | 70.6 | <u>302</u> | <u>70.4</u> | 4 | 254 | 83.9 | <u>253</u> | <u>84.2</u> | 250 | 85.2 |
| 454.calculix | 4 | 540 | 61.1 | 542 | 60.9 | <u>542</u> | <u>60.9</u> | 4 | 540 | 61.1 | 542 | 60.9 | <u>542</u> | <u>60.9</u> |
| 459.GemsFDTD | 4 | 1329 | 31.9 | <u>1327</u> | <u>32.0</u> | 1326 | 32.0 | 4 | 1329 | 31.9 | <u>1327</u> | <u>32.0</u> | 1326 | 32.0 |
| 465.tonto | 4 | <u>677</u> | <u>58.1</u> | 679 | 58.0 | 677 | 58.2 | 4 | 634 | 62.1 | 643 | 61.2 | <u>636</u> | <u>61.9</u> |
| 470.lbm | 4 | 1575 | 34.9 | 1573 | 34.9 | <u>1574</u> | <u>34.9</u> | 2 | 712 | 38.6 | <u>714</u> | <u>38.5</u> | 714 | 38.5 |
| 481.wrf | 4 | 658 | 67.9 | 662 | 67.5 | <u>662</u> | <u>67.5</u> | 4 | 658 | 67.9 | 662 | 67.5 | <u>662</u> | <u>67.5</u> |
| 482.sphinx3 | 4 | 1650 | 47.3 | <u>1651</u> | <u>47.2</u> | 1651 | 47.2 | 4 | <u>1535</u> | <u>50.8</u> | 1533 | 50.8 | 1535 | 50.8 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502



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Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>



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