



SPEC® CINT2006 Result

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Supermicro

(Test Sponsor: Advanced Micro Devices)

Supermicro A+ Server 2042G-6RF,
AMD Opteron 6128 HE

SPECint®_rate2006 = 507

SPECint_rate_base2006 = 440

CPU2006 license: 49

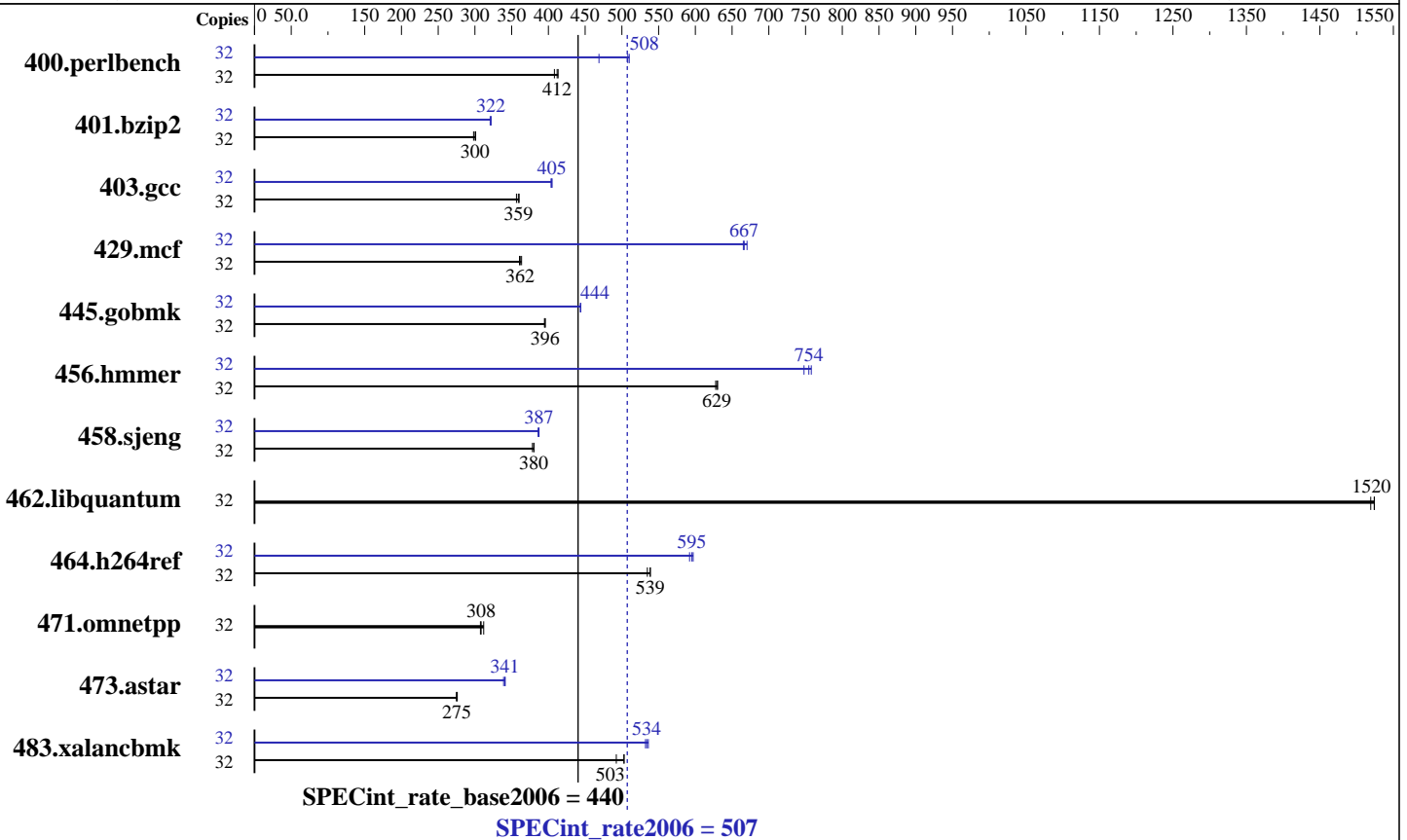
Test sponsor: Advanced Micro Devices

Tested by: Advanced Micro Devices

Test date: Aug-2010

Hardware Availability: Mar-2010

Software Availability: Jul-2010



Hardware

CPU Name: AMD Opteron 6128 HE
 CPU Characteristics:
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip
 CPU(s) orderable: 2,4 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 4 cores
 Other Cache: None
 Memory: 128 GB (32 x 4 GB, DDR3-1333, CL9, Reg, Dual Rank)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.5,
 Kernel 2.6.18-194.9.1.el5
 Compiler: x86 Open64 4.2.4 Compiler Suite (from AMD)
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: binutils 2.18
 SmartHeap 8.1 32-bit Library for Linux



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	766	408	<u>759</u>	<u>412</u>	757	413	32	667	469	613	510	<u>615</u>	<u>508</u>
401.bzip2	32	1036	298	1026	301	<u>1029</u>	<u>300</u>	32	<u>960</u>	<u>322</u>	963	321	960	322
403.gcc	32	723	357	715	360	<u>717</u>	<u>359</u>	32	638	404	<u>637</u>	<u>405</u>	636	405
429.mcf	32	<u>807</u>	<u>362</u>	809	361	803	363	32	439	665	<u>438</u>	<u>667</u>	435	670
445.gobmk	32	851	395	848	396	<u>849</u>	<u>396</u>	32	757	443	<u>756</u>	<u>444</u>	756	444
456.hammer	32	476	628	474	630	<u>474</u>	<u>629</u>	32	399	748	<u>396</u>	<u>754</u>	394	758
458.sjeng	32	1018	380	1024	378	<u>1019</u>	<u>380</u>	32	1003	386	<u>1001</u>	<u>387</u>	1000	387
462.libquantum	32	<u>435</u>	<u>1520</u>	435	1520	436	1520	32	<u>435</u>	<u>1520</u>	435	1520	436	1520
464.h264ref	32	1325	535	<u>1315</u>	<u>539</u>	1315	539	32	1196	592	1186	597	<u>1190</u>	<u>595</u>
471.omnetpp	32	641	312	<u>648</u>	<u>308</u>	650	307	32	641	312	<u>648</u>	<u>308</u>	650	307
473.astar	32	817	275	<u>816</u>	<u>275</u>	814	276	32	663	339	658	341	<u>659</u>	<u>341</u>
483.xalancbmk	32	448	492	<u>439</u>	<u>503</u>	439	503	32	<u>414</u>	<u>534</u>	412	536	415	532

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set vm/nr_hugepages=28672 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages

General Notes

Environment variables set by runspec before the start of the run:

HUGETLB_LIMIT = "896"

LD_LIBRARY_PATH = "/root/work/cpu2006/amd1002mc-rate-libs-revC/64:/root/work/cpu2006/amd1002mc-rate-libs-revC/32"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
<http://developer.amd.com/cpu/open64>

Base Compiler Invocation

C benchmarks:
openc

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Base Compiler Invocation (Continued)

C++ benchmarks:
openCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-march=barcelona -mso -Ofast -CG:local_sched_alg=1
-INLINE:aggressive=on -IPA:plimit=8000 -IPA:small_pu=100
-HP:bdt=2m:heap=2m

C++ benchmarks:

-march=barcelona -mso -Ofast -m32 -INLINE:aggressive=on
-CG:cmp_peep=on -L/root/work/libraries/SmartHeap-8.1/lib -lsmarheap

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -IPA:plimit=20000 -LNO:opt=0
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
-CG:local_sched_alg=1 -CG:unroll_fb_req=on
-HP:bdt=2m:heap=2m

401.bzip2: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:alias=disjoint
-OPT:goto=off -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m

403.gcc: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:trip_count=256
-LNO:prefetch_ahead=10 -CG:cmp_peep=on -m32
-HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200

429.mcf: -march=barcelona -mso -O3 -ipa -INLINE:aggressive=on
-CG:gcm=off -GRA:prioritize_by_density=on -m32
-HP:bdt=2m:heap=2m

445.gobmk: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:alias=restrict
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -ipa -IPA:plimit=750
-IPA:min_hotness=300 -IPA:pu_reorder=1 -LNO:prefetch=1
-LNO:ignore_feedback=off -CG:p2align=on
-CG:unroll_fb_req=on -HP:bdt=2m:heap=2m

456.hmmer: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=0
-OPT:alias=disjoint -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on
-CG:local_sched_alg=1 -CG:cflow=0
-CG:push_pop_int_saved_regs=off -CG:cmp_peep=on
-HP:bdt=2m:heap=2m

458.sjeng: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -ipa -LNO:ignore_feedback=off
-LNO:full_unroll=10 -LNO:fusion=0 -LNO:fission=2
-IPA:pu_reorder=2 -CG:ptr_load_use=0

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Peak Optimization Flags (Continued)

458.sjeng (continued):

-OPT:unroll_times_max=8 -INLINE:aggressive=on

462.libquantum: basepeak = yes

464.h264ref: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -IPA:plimit=20000
-OPT:alias=disjoint -LNO:prefetch=0 -CG:ptr_load_use=0
-CG:push_pop_int_saved_regs=off

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -TENV:frame_pointer=off
-WOPT:if_conv=0 -GRA:optimize_boundary=on
-OPT:alias=disjoint -INLINE:aggressive=on
-IPA:small_pu=3000 -IPA:plimit=3000 -m32
-HP:bdt=2m:heap=2m

483.xalancbmk: -march=barcelona -mso -Ofast -INLINE:aggressive=on -m32
-CG:cmp_peep=on -GRA:unspill=on -TENV:frame_pointer=off
-fno-emit-exceptions
-L/root/work/libraries/SmartHeap-8.1/lib -lsmarheap

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml>

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For other inquiries, please contact webmaster@spec.org.

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