



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 52.1

PRIMERGY TX100 S2, Intel Core i3-540, 3.06 GHz

SPECfp_rate_base2006 = 50.3

CPU2006 license: 19

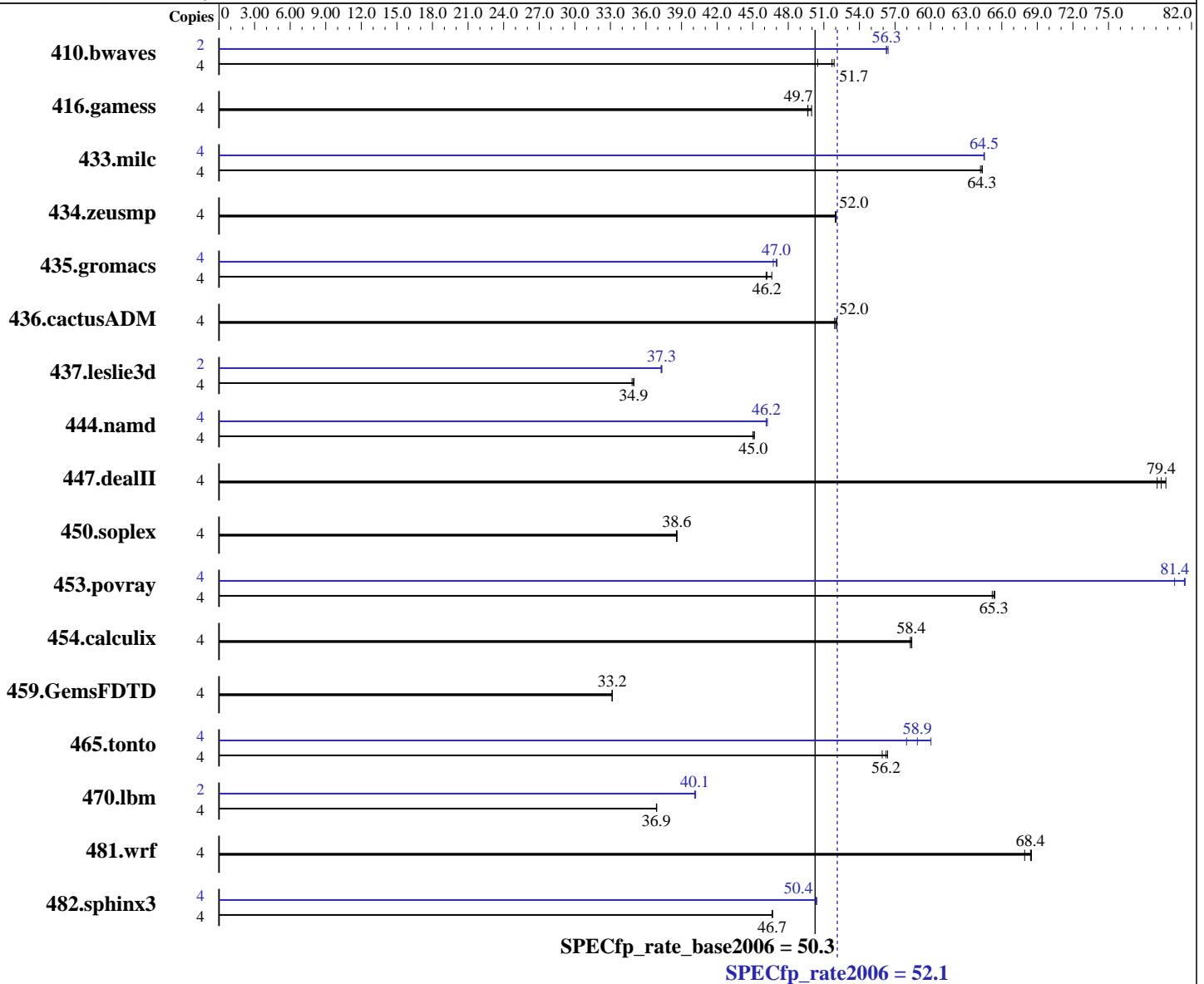
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2010

Hardware Availability: Jul-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Core i3-540
 CPU Characteristics: 3067
 CPU MHz: Integrated
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC)
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1077	50.5	1048	51.9	<u>1052</u>	<u>51.7</u>	2	483	56.3	<u>483</u>	<u>56.3</u>	482	56.4
416.gamess	4	1567	50.0	1578	49.6	<u>1577</u>	<u>49.7</u>	4	1567	50.0	1578	49.6	<u>1577</u>	<u>49.7</u>
433.milc	4	570	64.4	572	64.2	<u>571</u>	<u>64.3</u>	4	569	64.5	<u>569</u>	<u>64.5</u>	569	64.5
434.zeusmp	4	699	52.0	<u>700</u>	<u>52.0</u>	700	52.0	4	699	52.0	<u>700</u>	<u>52.0</u>	700	52.0
435.gromacs	4	619	46.1	613	46.6	<u>618</u>	<u>46.2</u>	4	607	47.1	611	46.7	<u>608</u>	<u>47.0</u>
436.cactusADM	4	921	51.9	<u>919</u>	<u>52.0</u>	917	52.1	4	921	51.9	<u>919</u>	<u>52.0</u>	917	52.1
437.leslie3d	4	1074	35.0	<u>1076</u>	<u>34.9</u>	1080	34.8	2	503	37.3	<u>504</u>	<u>37.3</u>	504	37.3
444.namd	4	712	45.0	<u>712</u>	<u>45.0</u>	711	45.2	4	694	46.2	695	46.1	<u>695</u>	<u>46.2</u>
447.dealII	4	<u>576</u>	<u>79.4</u>	573	79.8	578	79.1	4	<u>576</u>	<u>79.4</u>	573	79.8	578	79.1
450.soplex	4	<u>864</u>	<u>38.6</u>	865	38.6	864	38.6	4	<u>864</u>	<u>38.6</u>	865	38.6	864	38.6
453.povray	4	326	65.2	<u>326</u>	<u>65.3</u>	325	65.4	4	264	80.6	261	81.5	<u>261</u>	<u>81.4</u>
454.calculix	4	565	58.4	566	58.3	<u>565</u>	<u>58.4</u>	4	565	58.4	566	58.3	<u>565</u>	<u>58.4</u>
459.GemsFDTD	4	1281	33.1	1280	33.2	<u>1280</u>	<u>33.2</u>	4	1281	33.1	1280	33.2	<u>1280</u>	<u>33.2</u>
465.tonto	4	698	56.4	<u>700</u>	<u>56.2</u>	704	55.9	4	656	60.0	679	58.0	<u>668</u>	<u>58.9</u>
470.lbm	4	1489	36.9	1490	36.9	<u>1490</u>	<u>36.9</u>	2	684	40.2	<u>685</u>	<u>40.1</u>	685	40.1
481.wrf	4	652	68.5	<u>653</u>	<u>68.4</u>	658	67.9	4	652	68.5	<u>653</u>	<u>68.4</u>	658	67.9
482.sphinx3	4	1670	46.7	1671	46.6	<u>1671</u>	<u>46.7</u>	4	1548	50.4	1547	50.4	<u>1548</u>	<u>50.4</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502



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Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

Continued on next page



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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>



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