



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/R140b-4
(Intel Xeon X7560)

SPECfp_rate2006 = 280

SPECfp_rate_base2006 = 272

CPU2006 license: 9006

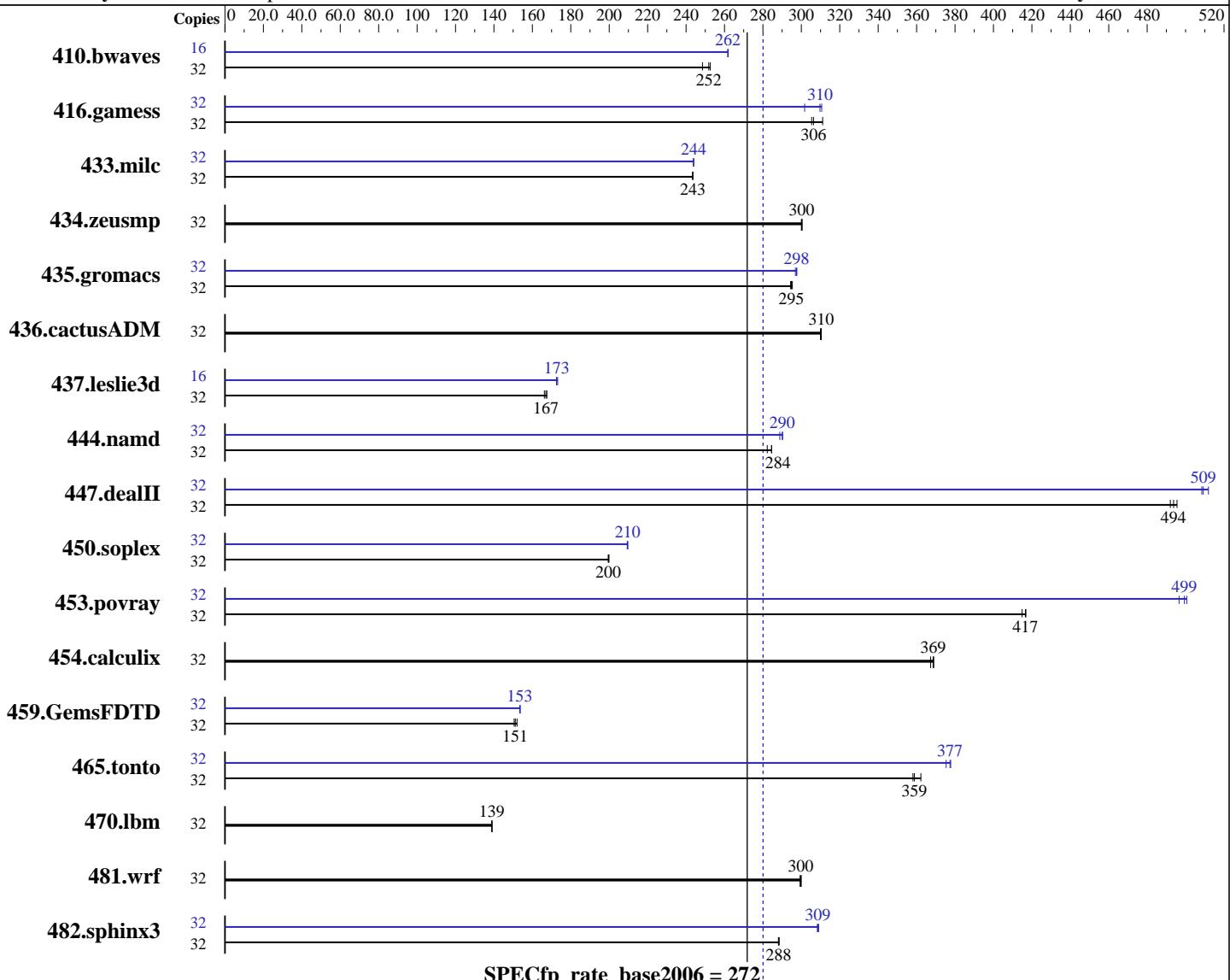
Test date: Jul-2010

Test sponsor: NEC Corporation

Hardware Availability: Aug-2010

Tested by: NEC Corporation

Software Availability: Dec-2009



Hardware

CPU Name: Intel Xeon X7560
CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz: 2267
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)

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L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (32 x 4 GB PC3-10600R, 2 rank, CL9, ECC, running at 1066 MHz)
Disk Subsystem: 1x300 GB SAS, 10000 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	1722	253	1750	249	<u>1727</u>	<u>252</u>	16	831	262	<u>831</u>	<u>262</u>	830	262
416.gamess	32	<u>2045</u>	<u>306</u>	2052	305	2014	311	32	2017	311	2077	302	<u>2023</u>	<u>310</u>
433.milc	32	<u>1206</u>	<u>243</u>	1207	243	1206	243	32	1204	244	<u>1204</u>	<u>244</u>	1205	244
434.zeusmp	32	971	300	<u>970</u>	<u>300</u>	969	300	32	971	300	<u>970</u>	<u>300</u>	969	300
435.gromacs	32	<u>775</u>	<u>295</u>	774	295	776	294	32	770	297	<u>768</u>	<u>298</u>	768	298
436.cactusADM	32	1232	310	<u>1232</u>	<u>310</u>	1234	310	32	1232	310	<u>1232</u>	<u>310</u>	1234	310
437.leslie3d	32	<u>1801</u>	<u>167</u>	1809	166	1795	168	16	<u>871</u>	<u>173</u>	869	173	872	173
444.namd	32	<u>902</u>	<u>284</u>	902	285	909	282	32	884	290	889	289	<u>885</u>	<u>290</u>
447.dealII	32	744	492	739	496	<u>741</u>	<u>494</u>	32	720	508	<u>719</u>	<u>509</u>	715	512
450.soplex	32	1335	200	<u>1337</u>	<u>200</u>	1337	200	32	1273	210	1275	209	<u>1273</u>	<u>210</u>
453.povray	32	408	417	<u>409</u>	<u>417</u>	410	415	32	<u>341</u>	<u>499</u>	340	501	343	497
454.calculix	32	716	369	719	367	<u>716</u>	<u>369</u>	32	716	369	719	367	<u>716</u>	<u>369</u>
459.GemsFDTD	32	<u>2247</u>	<u>151</u>	2233	152	2255	151	32	<u>2212</u>	<u>153</u>	2209	154	2213	153
465.tonto	32	869	362	<u>877</u>	<u>359</u>	879	358	32	839	375	<u>834</u>	<u>377</u>	834	378
470.lbm	32	3164	139	3165	139	<u>3164</u>	<u>139</u>	32	3164	139	3165	139	<u>3164</u>	<u>139</u>
481.wrf	32	<u>1192</u>	<u>300</u>	1192	300	1194	299	32	<u>1192</u>	<u>300</u>	1192	300	1194	299
482.sphinx3	32	<u>2164</u>	<u>288</u>	2162	288	2165	288	32	2023	308	<u>2021</u>	<u>309</u>	2018	309

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS setting:
Performance/Watt: Traditional



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Base Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
450.soplex: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias -scalar-rep-

450.soplex: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xsSE4 .2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll12 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xsSE4 .2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll12 -Ob0

465.tonto: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll14 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-R140.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-R140.xml>

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