



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 757

ProLiant DL580 G7
(2.27 GHz, Intel Xeon X7560)

SPECint_rate_base2006 = 703

CPU2006 license: 3

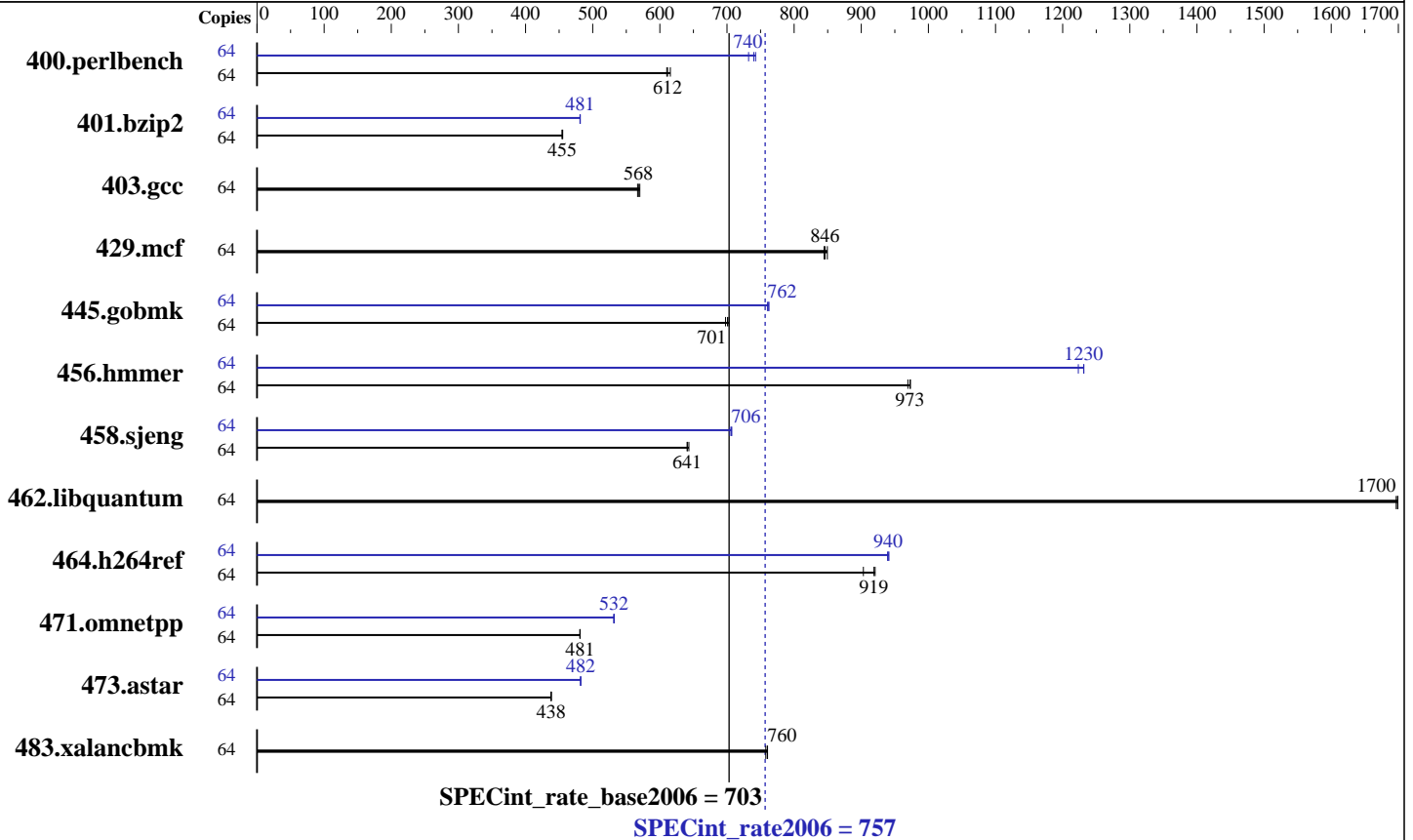
Test date: Jul-2010

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2010

Tested by: Hewlett-Packard Company

Software Availability: Mar-2010



Hardware

CPU Name: Intel Xeon X7560
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 24 MB I+D on chip per chip
 Other Cache: None
 Memory: 1 TB (64 x 16 GB 4Rx4 PC3-8500R CL7)
 Disk Subsystem: 2 x 146 GB 15 K SAS
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.5
 Advanced Platform, Kernel 2.6.18-194.el5
 Compiler: Intel C++ Compiler 11.1 for Linux
 Build 20090827 Package ID: l_cproc_p_11.1.056
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.17.50.0.6-12.el5



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	64	<u>1023</u>	<u>612</u>	1024	610	1016	616	64	<u>845</u>	<u>740</u>	842	743	854	732
401.bzip2	64	1356	455	<u>1359</u>	<u>455</u>	1359	454	64	1283	481	<u>1283</u>	<u>481</u>	1284	481
403.gcc	64	<u>907</u>	<u>568</u>	904	570	909	567	64	<u>907</u>	<u>568</u>	904	570	909	567
429.mcf	64	<u>690</u>	<u>846</u>	691	845	687	850	64	<u>690</u>	<u>846</u>	691	845	687	850
445.gobmk	64	962	698	<u>958</u>	<u>701</u>	955	703	64	883	760	<u>881</u>	<u>762</u>	881	762
456.hammer	64	616	969	614	973	<u>614</u>	<u>973</u>	64	488	1220	485	1230	<u>485</u>	<u>1230</u>
458.sjeng	64	1209	641	<u>1208</u>	<u>641</u>	1204	643	64	<u>1096</u>	<u>706</u>	1099	704	1095	707
462.libquantum	64	<u>781</u>	<u>1700</u>	781	1700	782	1700	64	<u>781</u>	<u>1700</u>	781	1700	782	1700
464.h264ref	64	1538	921	<u>1541</u>	<u>919</u>	1568	903	64	1508	939	1505	941	<u>1506</u>	<u>940</u>
471.omnetpp	64	831	481	832	481	<u>831</u>	<u>481</u>	64	<u>752</u>	<u>532</u>	752	532	753	531
473.astar	64	<u>1025</u>	<u>438</u>	1025	438	1026	438	64	<u>932</u>	<u>482</u>	932	482	932	482
483.xalancbmk	64	<u>581</u>	<u>760</u>	583	758	581	760	64	<u>581</u>	<u>760</u>	583	758	581	760

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Power Profile set to Maximum Performance
Thermal Configuration set to Increased Cooling

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.1/056/bin/intel64/icc
456.hmmer: /opt/intel/Compiler/11.1/056/bin/intel64/icc
458.sjeng: /opt/intel/Compiler/11.1/056/bin/intel64/icc

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmarheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmarheap

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page



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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 10:50:40 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 August 2010.