



SPEC® CINT2006 Result

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ASUSTeK Computer Inc.

ASUS TS300-E6 (P7F-E) server system (Intel Xeon X3440)

SPECint®2006 = 29.8

SPECint_base2006 = 25.8

CPU2006 license: 9016

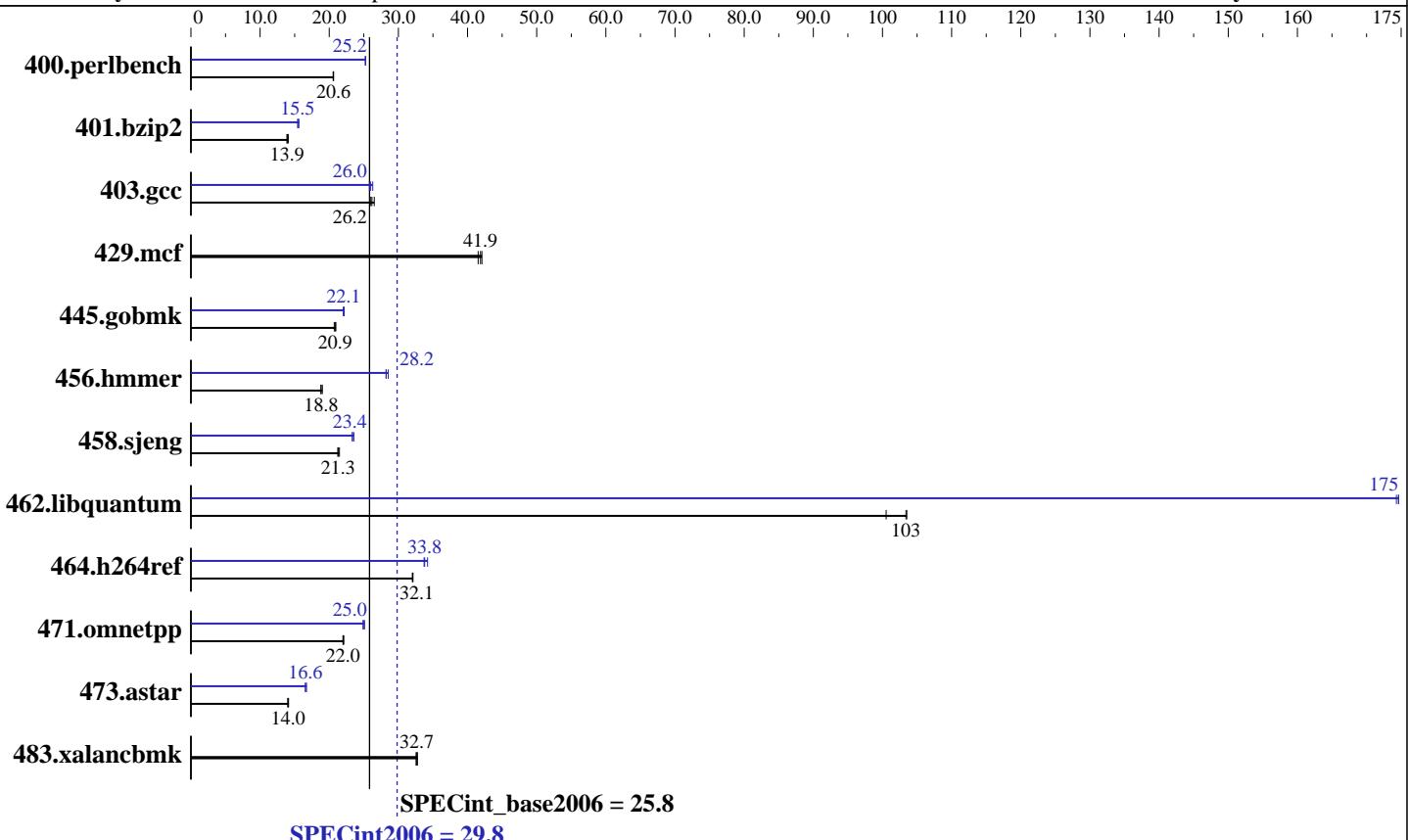
Test sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test date: Jul-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X3440
CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz
CPU MHz: 2533
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4 x 4 GB PC3-10600R, CL=9)
Disk Subsystem: HITACHI HDP725050GLA380 1 x 500 GB SATAII, 7200 RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel: Yes
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	474	20.6	474	20.6	475	20.6	387	25.2	387	25.3	387	25.2
401.bzip2	694	13.9	686	14.1	693	13.9	624	15.5	624	15.5	618	15.6
403.gcc	309	26.0	304	26.5	308	26.2	312	25.8	310	26.0	306	26.3
429.mcf	217	42.1	218	41.9	220	41.5	217	42.1	218	41.9	220	41.5
445.gobmk	506	20.7	503	20.9	501	21.0	475	22.1	476	22.0	473	22.2
456.hmmer	491	19.0	496	18.8	496	18.8	327	28.5	330	28.2	331	28.2
458.sjeng	569	21.3	563	21.5	569	21.3	518	23.4	514	23.6	519	23.3
462.libquantum	200	103	200	104	206	101	119	175	119	175	119	174
464.h264ref	689	32.1	690	32.1	691	32.0	655	33.8	656	33.7	647	34.2
471.omnetpp	284	22.0	284	22.0	283	22.1	251	24.9	250	25.0	249	25.1
473.astar	501	14.0	500	14.0	497	14.1	420	16.7	424	16.6	424	16.6
483.xalancbmk	211	32.8	212	32.6	211	32.7	211	32.8	212	32.6	211	32.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter

Component Notes

Tested system case compliance with Intel EEB 3.61 spec
 SSI Server Power Supply 650W or higher
 System was configured with ASPEED AST2050 VGA (on board VGA)

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
 icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:

`icpc -m32`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -inline-calloc
-opt-malloc-options=3`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m32`

401.bzip2: `icc -m64`

456.hmmr: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`



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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
  -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
  -prof-use(pass 2) -auto-ilp32 -opt-prefetch

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
  -opt-malloc-options=3

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
  -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
  -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
  -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
  -par-runtime-control -opt-prefetch -inline-calloc
  -opt-malloc-options=3

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
  -prof-use(pass 2) -unroll12 -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
  -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
  -L/spec/cpu2006.1.1/lib -lsmartheap
```

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Peak Optimization Flags (Continued)

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
           -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64
```

```
483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=__alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20100609.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20100609.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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