SPEC® CINT2006 Result
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IBM Corporation
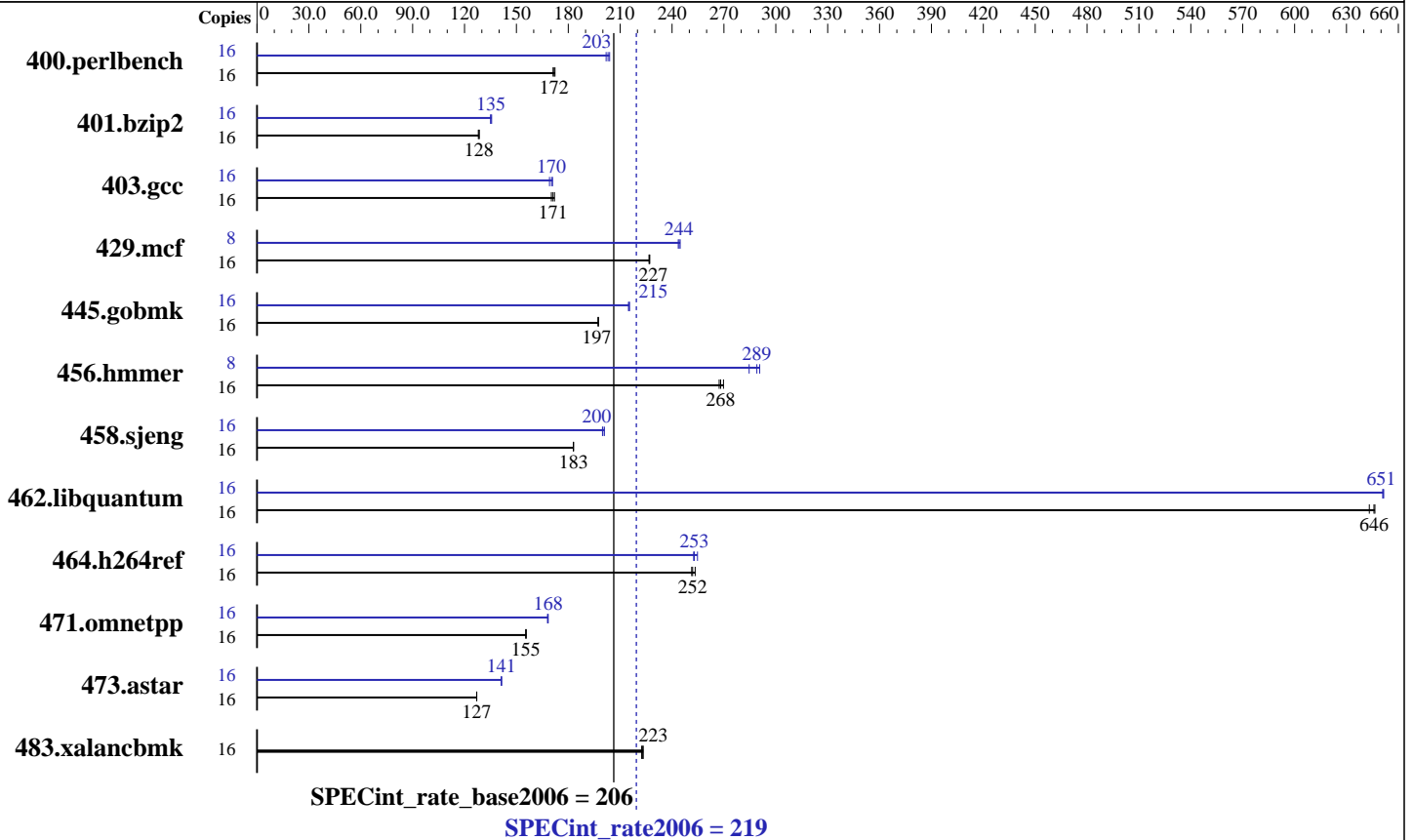
IBM System x iDataPlex dx360 M3 (Intel Xeon L5530)

SPECint®_rate2006 =   219

SPECint_rate_base2006 =   206

CPU2006 license: 11   **Test date:**   Jun-2010
Test sponsor: IBM Corporation   **Hardware Availability:** Jun-2010
Tested by: IBM Corporation   **Software Availability:** Jan-2010

SPECint_rate_base2006 = 206

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Hardware

CPU Name:	Intel Xeon L5530
CPU Characteristics:	Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB PC3-10600R CL9, 2 Rank)
Disk Subsystem:	1 x 250 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	**910**	**172**	907	172	913	171	16	**770**	**203**	767	204	774	202
401.bzip2	16	**1202**	**128**	1206	128	1202	128	16	1138	136	**1142**	**135**	1143	135
403.gcc	16	**752**	**171**	749	172	757	170	16	**756**	**170**	762	169	754	171
429.mcf	16	642	227	**643**	**227**	644	227	8	298	245	300	244	**299**	**244**
445.gobmk	16	850	197	**851**	**197**	851	197	16	781	215	779	215	**780**	**215**
456.hmmer	16	**557**	**268**	559	267	553	270	8	257	291	262	285	**258**	**289**
458.sjeng	16	**1058**	**183**	1058	183	1058	183	16	964	201	969	200	**968**	**200**
462.libquantum	16	513	646	**513**	**646**	515	643	16	509	651	509	652	**509**	**651**
464.h264ref	16	**1406**	**252**	1409	251	1398	253	16	1390	255	**1400**	**253**	1402	252
471.omnetpp	16	642	156	**643**	**155**	643	155	16	**594**	**168**	595	168	594	168
473.astar	16	**884**	**127**	884	127	884	127	16	794	141	795	141	**794**	**141**
483.xalancbmk	16	**495**	**223**	494	223	496	222	16	**495**	**223**	494	223	496	222

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Platform Notes

      Turbo Mode Enable
      Turbo Boost set to Traditional
      CPU C State Enable

General Notes

      Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
      'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Base Compiler Invocation

C benchmarks:
      icc -m32

C++ benchmarks:
      icpc -m32

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Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
    -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:

```
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
    icc  -m32
```

```
       401.bzip2: icc -m64
```

```
      456.hmmer: icc -m64
```

```
       458.sjeng: icc -m64
```

```
    462.libquantum: icc -m64
```

C++ benchmarks (except as noted below):

```
    icpc -m32
```

```
       473.astar: icpc -m64
```

Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
       401.bzip2: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

        456.hmmer: `-DSPEC_CPU_LP64`
        458.sjeng: `-DSPEC_CPU_LP64`
    462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
        473.astar: `-DSPEC_CPU_LP64`
    483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
                `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
                `-prof-use`(pass 2) `-ansi-alias`

      401.bzip2: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
                `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
                `-prof-use`(pass 2) `-opt-prefetch -ansi-alias -auto-ilp32`

        403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -static`

        429.mcf: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

      445.gobmk: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2) `-O2`
                `-ipo -no-prec-div -ansi-alias`

      456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
                `-ansi-alias -auto-ilp32`

      458.sjeng: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
                `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
                `-prof-use`(pass 2) `-unroll4 -auto-ilp32`

    462.libquantum: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32`
                `-opt-prefetch`

      464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
                `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
                `-prof-use`(pass 2) `-unroll2 -ansi-alias`

C++ benchmarks:

      471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
                `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
                `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
                `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

        473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
                `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
                `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`

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Peak Optimization Flags (Continued)

473.astar (continued):
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100804.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100804.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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