SPEC® CINT2006 Result
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Fujitsu

PRIMERGY BX924 S2, Intel Xeon X5677, 3.46 GHz

$SPECint^®\_rate2006 =$ 153

$SPECint\_rate\_base2006 =$ 145

CPU2006 license:	19	**Test date:**	Jun-2010
Test sponsor:	Fujitsu	**Hardware Availability:**	Jun-2010
Tested by:	Fujitsu	**Software Availability:**	Jan-2010

Copies 0 15.0 30.0 45.0 60.0 75.0 90.0 105 120 135 150 165 180 195 210 225 240 255 270 285 300 315 330 345 360 375 390 415

400.perlbench — 8: 143, 8: 120

401.bzip2 — 8: 98.8, 8: 93.4

403.gcc — 8: 121

429.mcf — 4: 163, 8: 163

445.gobmk — 8: 151, 8: 139

456.hmmer — 4: 208, 8: 191

458.sjeng — 8: 141, 8: 129

462.libquantum — 8: 413

464.h264ref — 8: 189, 8: 182

471.omnetpp — 8: 114, 8: 105

473.astar — 8: 98.5, 8: 89.4

483.xalancbmk — 8: 156

SPECint_rate_base2006 = 145

SPECint_rate2006 = 153

Hardware

CPU Name:	Intel Xeon X5677
CPU Characteristics:	Intel Turbo Boost Technology up to 3.73 GHz
CPU MHz:	3467
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SAS, 300 GB, 10000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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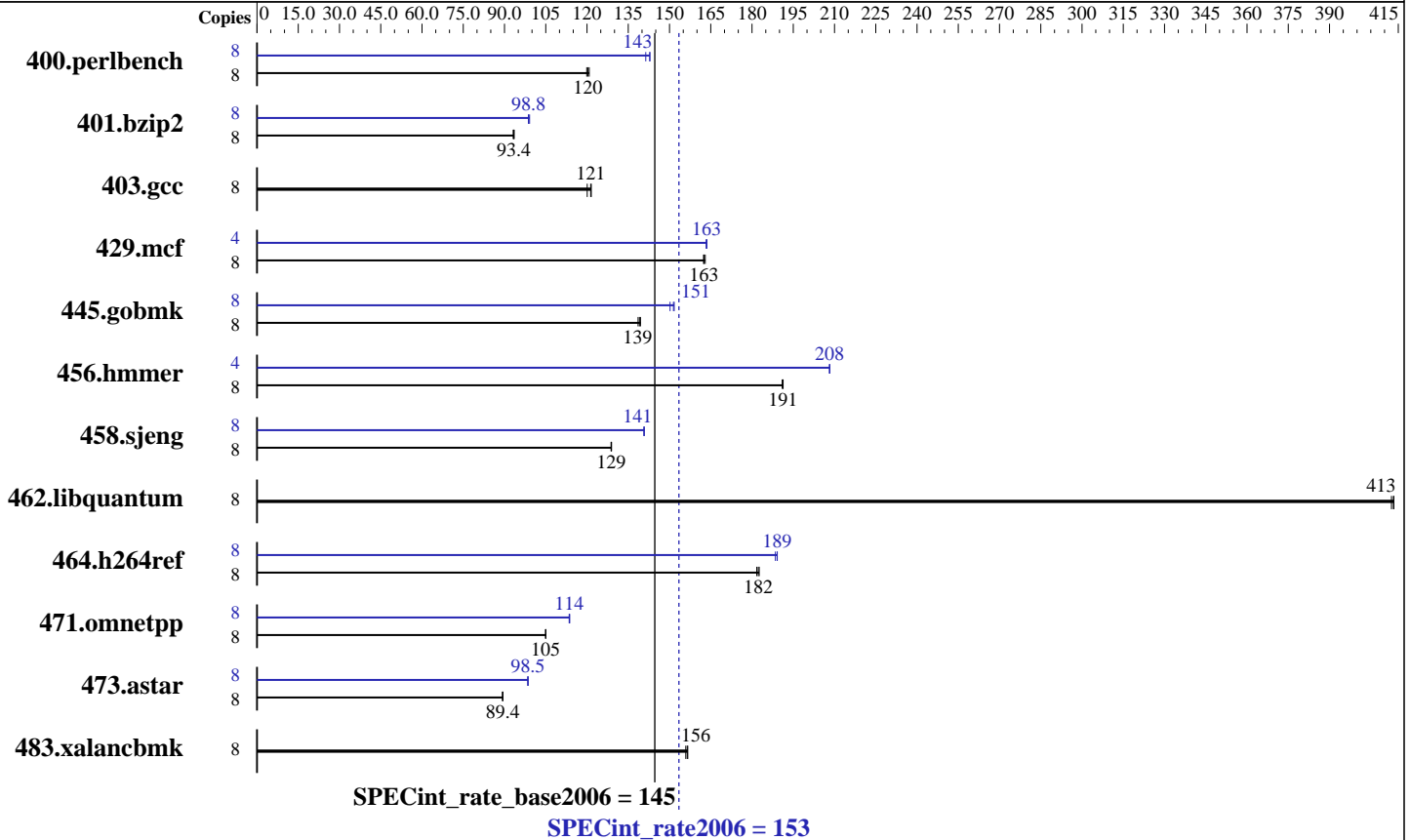
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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	652	120	647	121	**650**	**120**	8	553	141	**547**	**143**	547	143
401.bzip2	8	829	93.1	825	93.5	**827**	**93.4**	8	779	99.1	**781**	**98.8**	782	98.7
403.gcc	8	530	122	536	120	**531**	**121**	8	530	122	536	120	**531**	**121**
429.mcf	8	448	163	**449**	**163**	449	162	4	223	163	**223**	**163**	223	164
445.gobmk	8	606	139	602	139	**603**	**139**	8	553	152	559	150	**554**	**151**
456.hmmer	8	390	191	391	191	**390**	**191**	4	179	208	**179**	**208**	179	208
458.sjeng	8	**751**	**129**	752	129	751	129	8	688	141	688	141	**688**	**141**
462.libquantum	8	402	413	401	413	**401**	**413**	8	402	413	401	413	**401**	**413**
464.h264ref	8	970	183	974	182	**971**	**182**	8	939	188	**936**	**189**	936	189
471.omnetpp	8	476	105	476	105	**476**	**105**	8	440	114	440	114	**440**	**114**
473.astar	8	628	89.4	**628**	**89.4**	629	89.2	8	**570**	**98.5**	570	98.5	570	98.5
483.xalancbmk	8	352	157	354	156	**353**	**156**	8	352	157	354	156	**353**	**156**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    BIOS configuration:
    Data Reuse Optimization = Disable
    Performance/Power Setting = Traditional

General Notes

    For information about Fujitsu please visit: http://www.fujitsu.com
    Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
     icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks (except as noted below):
    icpc -m32

    473.astar: icpc -m64

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Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
        401.bzip2: -DSPEC_CPU_LP64
        456.hmmer: -DSPEC_CPU_LP64
        458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
        473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

        401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

         403.gcc: basepeak = yes

         429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

       445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
               -ipo -no-prec-div -ansi-alias

        456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
               -ansi-alias -auto-ilp32

        458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll4 -auto-ilp32

    462.libquantum: basepeak = yes

       464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
       471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

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Peak Optimization Flags (Continued)

473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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