



# SPEC® CFP2006 Result

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## Fujitsu

### SPECfp®\_rate2006 = 204

PRIMERGY BX920 S2, Intel Xeon L5640, 2.27 GHz

### SPECfp\_rate\_base2006 = 196

CPU2006 license: 19

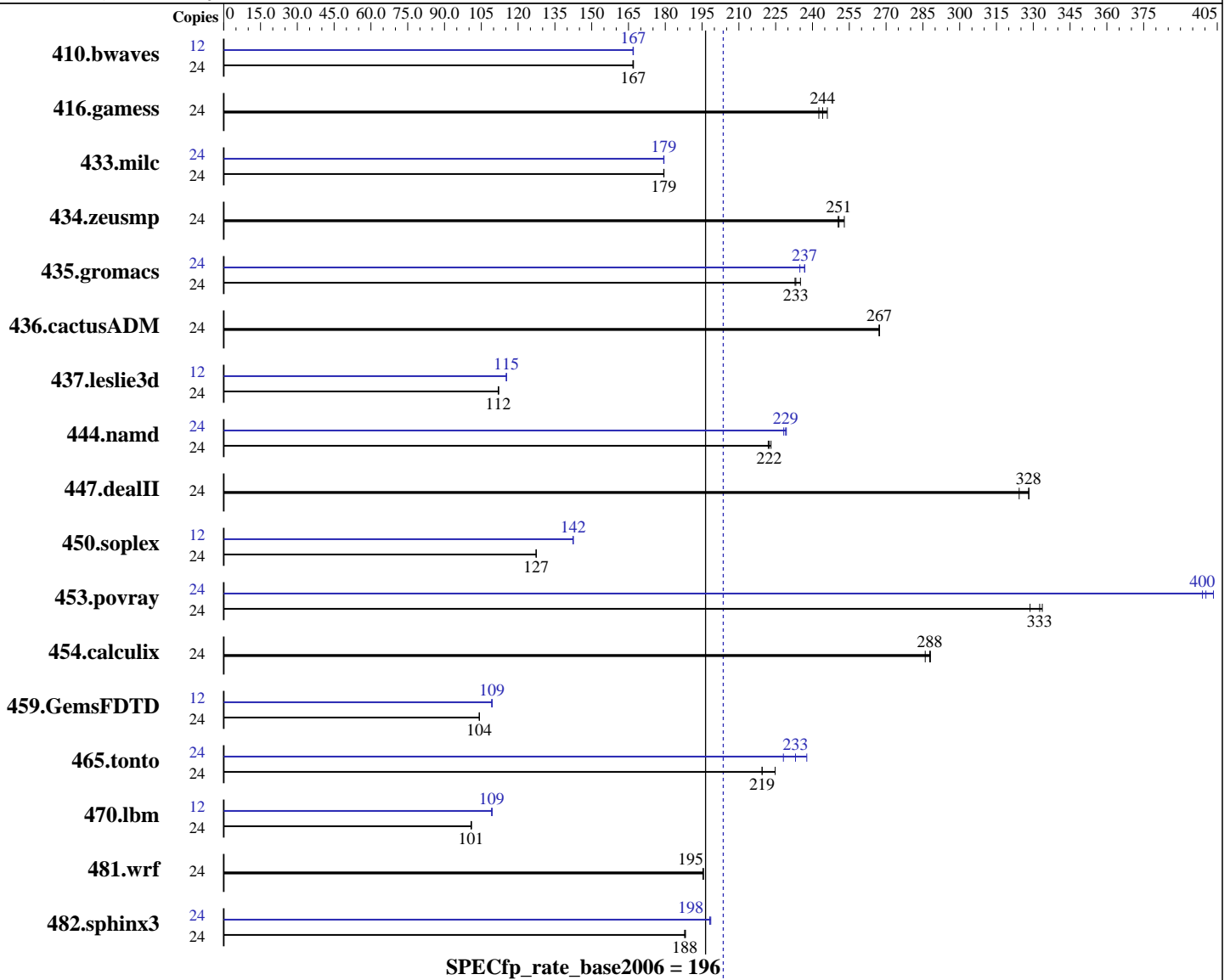
Test date: Jun-2010

Test sponsor: Fujitsu

Hardware Availability: May-2010

Tested by: Fujitsu

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon L5640  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2267  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3

Continued on next page



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L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (6x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC)  
Disk Subsystem: 1 x SSD SATA, 64 GB  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	<b>1954</b>	<b>167</b>	1953	167	1956	167	12	<b>977</b>	<b>167</b>	977	167	978	167
416.gamess	24	1937	243	1910	246	<b>1925</b>	<b>244</b>	24	1937	243	1910	246	<b>1925</b>	<b>244</b>
433.milc	24	1228	179	<b>1228</b>	<b>179</b>	1228	179	24	<b>1228</b>	<b>179</b>	1228	179	1228	179
434.zeusmp	24	<b>871</b>	<b>251</b>	872	250	863	253	24	<b>871</b>	<b>251</b>	872	250	863	253
435.gromacs	24	<b>735</b>	<b>233</b>	736	233	729	235	24	724	237	<b>724</b>	<b>237</b>	730	235
436.cactusADM	24	<b>1073</b>	<b>267</b>	1073	267	1074	267	24	<b>1073</b>	<b>267</b>	1073	267	1074	267
437.leslie3d	24	2012	112	2016	112	<b>2016</b>	<b>112</b>	12	979	115	<b>979</b>	<b>115</b>	979	115
444.namd	24	863	223	867	222	<b>866</b>	<b>222</b>	24	843	228	839	229	<b>840</b>	<b>229</b>
447.dealII	24	847	324	<b>837</b>	<b>328</b>	836	328	24	847	324	<b>837</b>	<b>328</b>	836	328
450.soplex	24	1571	127	1572	127	<b>1572</b>	<b>127</b>	12	<b>703</b>	<b>142</b>	703	142	702	142
453.povray	24	<b>384</b>	<b>333</b>	388	329	383	334	24	320	399	316	403	<b>319</b>	<b>400</b>
454.calculix	24	692	286	687	288	<b>688</b>	<b>288</b>	24	692	286	687	288	<b>688</b>	<b>288</b>
459.GemsFDTD	24	<b>2444</b>	<b>104</b>	2444	104	2444	104	12	1163	109	<b>1165</b>	<b>109</b>	1165	109
465.tonto	24	1051	225	<b>1076</b>	<b>219</b>	1076	219	24	994	238	<b>1013</b>	<b>233</b>	1035	228
470.lbm	24	3264	101	<b>3270</b>	<b>101</b>	3271	101	12	1507	109	<b>1509</b>	<b>109</b>	1509	109
481.wrf	24	1371	196	<b>1372</b>	<b>195</b>	1372	195	24	1371	196	<b>1372</b>	<b>195</b>	1372	195
482.sphinx3	24	2485	188	<b>2489</b>	<b>188</b>	2489	188	24	2361	198	2356	199	<b>2360</b>	<b>198</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Data Reuse Optimization = Disable  
Performance/Power Setting = Traditional



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## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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**Hardware Availability:** May-2010

**Tested by:** Fujitsu

**Software Availability:** Jan-2010

## Peak Optimization Flags

### C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto -inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

Continued on next page



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## Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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