



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint®2006 = 38.3

PRIMERGY RX200 S6, Intel Xeon X5660, 2.80 GHz

SPECint_base2006 = 35.4

CPU2006 license: 19

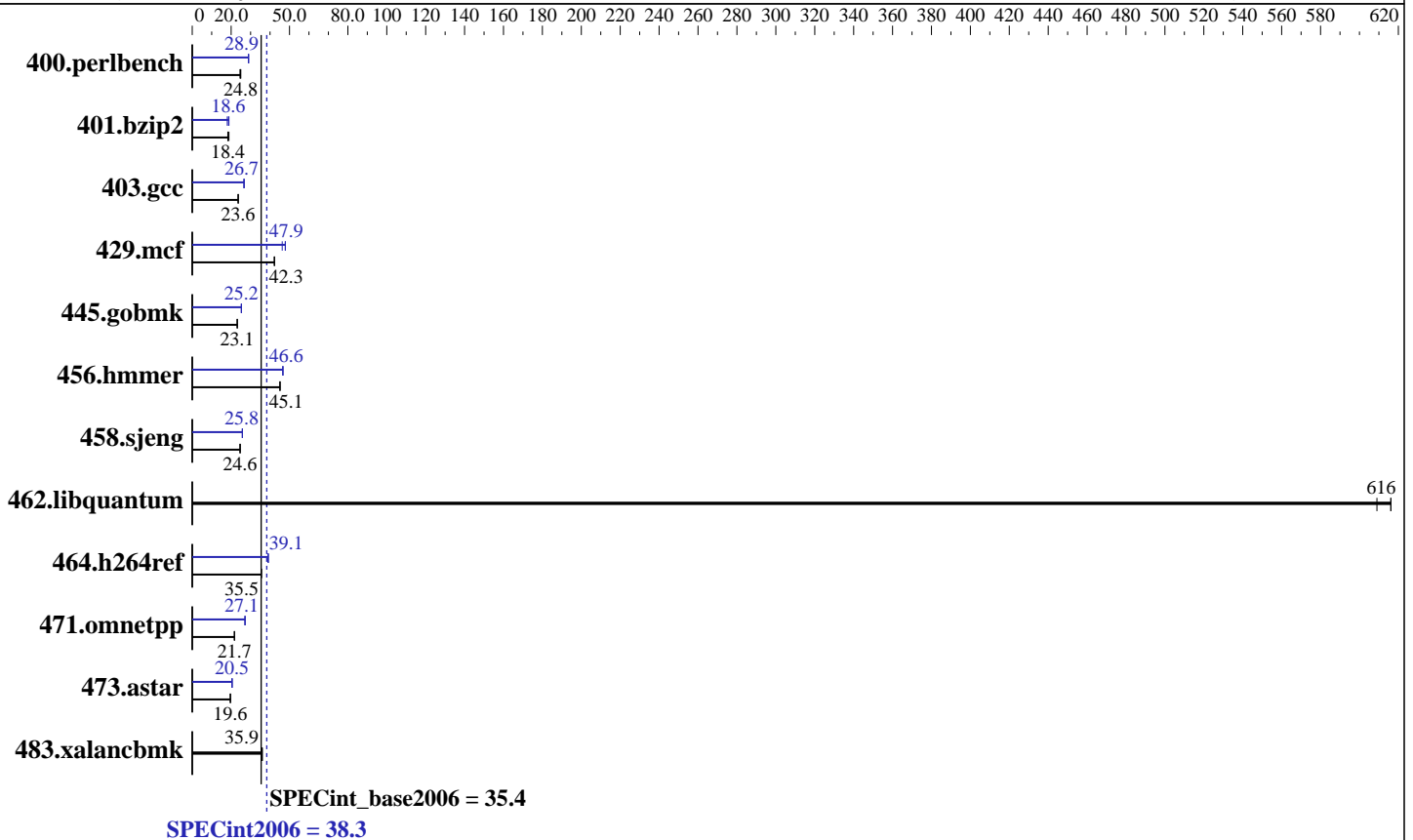
Test date: Jun-2010

Test sponsor: Fujitsu

Hardware Availability: Oct-2010

Tested by: Fujitsu

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5660
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
 Disk Subsystem: 1 x SATA, 160 GB, 5400 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = **38.3**

PRIMERGY RX200 S6, Intel Xeon X5660, 2.80 GHz

SPECint_base2006 = **35.4**

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jun-2010
Hardware Availability: Oct-2010
Software Availability: Jan-2010

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	398	24.5	<u>393</u>	<u>24.8</u>	391	25.0	<u>337</u>	<u>29.0</u>	338	28.9	<u>338</u>	<u>28.9</u>
401.bzip2	511	18.9	528	18.3	<u>524</u>	<u>18.4</u>	511	18.9	<u>519</u>	<u>18.6</u>	536	18.0
403.gcc	341	23.6	<u>341</u>	<u>23.6</u>	341	23.6	<u>302</u>	<u>26.7</u>	303	26.6	302	26.7
429.mcf	216	42.3	216	42.2	<u>216</u>	<u>42.3</u>	<u>191</u>	<u>47.9</u>	197	46.3	191	47.9
445.gobmk	453	23.1	453	23.2	<u>453</u>	<u>23.1</u>	<u>416</u>	<u>25.2</u>	414	25.3	416	25.2
456.hammer	206	45.2	207	45.1	<u>207</u>	<u>45.1</u>	200	46.6	<u>200</u>	<u>46.6</u>	200	46.6
458.sjeng	<u>492</u>	<u>24.6</u>	492	24.6	491	24.6	<u>469</u>	<u>25.8</u>	469	25.8	470	25.8
462.libquantum	34.0	609	33.6	616	<u>33.6</u>	<u>616</u>	34.0	609	33.6	616	<u>33.6</u>	<u>616</u>
464.h264ref	623	35.5	623	35.5	<u>623</u>	<u>35.5</u>	<u>567</u>	<u>39.1</u>	567	39.0	566	39.1
471.omnetpp	<u>288</u>	<u>21.7</u>	288	21.7	288	21.7	230	27.1	<u>230</u>	<u>27.1</u>	230	27.2
473.astar	357	19.7	359	19.6	<u>358</u>	<u>19.6</u>	344	20.4	<u>343</u>	<u>20.5</u>	342	20.6
483.xalancbmk	193	35.8	191	36.0	<u>192</u>	<u>35.9</u>	193	35.8	191	36.0	<u>192</u>	<u>35.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Intel HT Technology = Disable
Data Reuse Optimization = Disable

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 38.3

PRIMERGY RX200 S6, Intel Xeon X5660, 2.80 GHz

SPECint_base2006 = 35.4

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jun-2010
Hardware Availability: Oct-2010
Software Availability: Jan-2010

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 38.3

PRIMERGY RX200 S6, Intel Xeon X5660, 2.80 GHz

SPECint_base2006 = 35.4

CPU2006 license: 19

Test date: Jun-2010

Test sponsor: Fujitsu

Hardware Availability: Oct-2010

Tested by: Fujitsu

Software Availability: Jan-2010

Peak Compiler Invocation (Continued)

471.omnetpp: icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)
 -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 38.3

PRIMERGY RX200 S6, Intel Xeon X5660, 2.80 GHz

SPECint_base2006 = 35.4

CPU2006 license: 19

Test date: Jun-2010

Test sponsor: Fujitsu

Hardware Availability: Oct-2010

Tested by: Fujitsu

Software Availability: Jan-2010

Peak Optimization Flags (Continued)

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
           -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 11:27:38 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 July 2010.