



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp®2006 = 25.7

PowerEdge T610 (Intel Xeon E5503, 2.00 GHz)

SPECfp\_base2006 = 23.8

CPU2006 license: 55

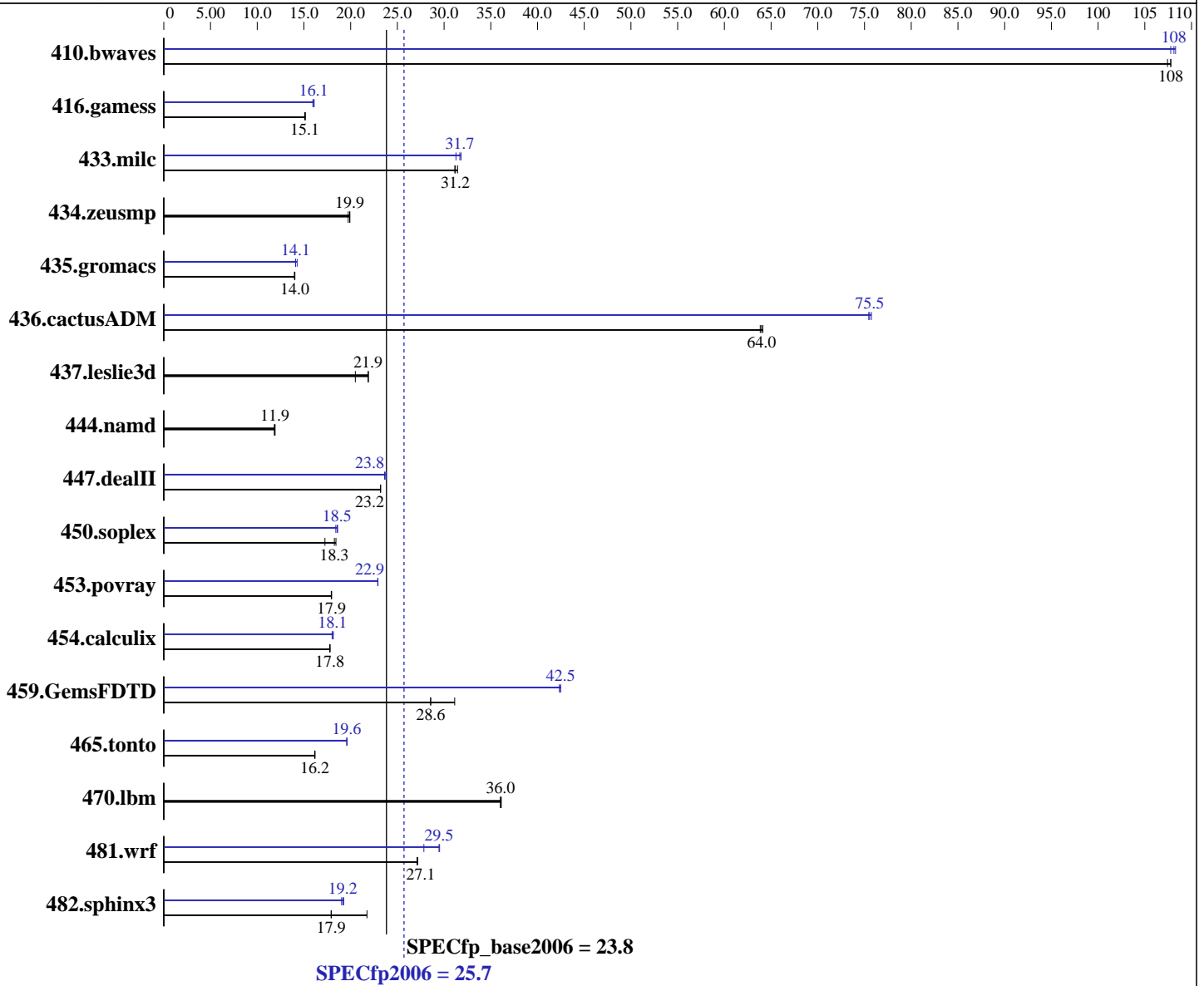
Test date: Jun-2010

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Dec-2009



### Hardware

CPU Name: Intel Xeon E5503  
 CPU Characteristics:  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smpp  
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)

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L3 Cache: 4 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (12 x 4 GB DDR3-1333 DR RDIMM, CL9, ECC, downclocked to 800 MHz)  
Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	126	107	<b>126</b>	<b>108</b>	126	108	126	108	125	108	<b>126</b>	<b>108</b>
416.gamess	<b>1296</b>	<b>15.1</b>	1292	15.1	1297	15.1	1227	16.0	1217	16.1	<b>1220</b>	<b>16.1</b>
433.milc	295	31.1	292	31.4	<b>294</b>	<b>31.2</b>	<b>290</b>	<b>31.7</b>	289	31.8	294	31.3
434.zeusmp	457	19.9	<b>457</b>	<b>19.9</b>	461	19.7	457	19.9	<b>457</b>	<b>19.9</b>	461	19.7
435.gromacs	511	14.0	509	14.0	<b>509</b>	<b>14.0</b>	<b>506</b>	<b>14.1</b>	507	14.1	500	14.3
436.cactusADM	186	64.1	187	63.8	<b>187</b>	<b>64.0</b>	158	75.7	<b>158</b>	<b>75.5</b>	158	75.4
437.leslie3d	429	21.9	<b>430</b>	<b>21.9</b>	459	20.5	429	21.9	<b>430</b>	<b>21.9</b>	459	20.5
444.namd	<b>674</b>	<b>11.9</b>	674	11.9	678	11.8	<b>674</b>	<b>11.9</b>	674	11.9	678	11.8
447.dealII	493	23.2	<b>493</b>	<b>23.2</b>	493	23.2	<b>481</b>	<b>23.8</b>	481	23.8	484	23.6
450.soplex	<b>457</b>	<b>18.3</b>	453	18.4	484	17.2	<b>450</b>	<b>18.5</b>	448	18.6	453	18.4
453.povray	296	18.0	<b>297</b>	<b>17.9</b>	297	17.9	232	22.9	<b>232</b>	<b>22.9</b>	232	22.9
454.calculix	<b>465</b>	<b>17.8</b>	465	17.7	463	17.8	458	18.0	455	18.1	<b>457</b>	<b>18.1</b>
459.GemsFDTD	341	31.1	<b>371</b>	<b>28.6</b>	372	28.6	250	42.5	251	42.3	<b>250</b>	<b>42.5</b>
465.tonto	608	16.2	609	16.2	<b>608</b>	<b>16.2</b>	502	19.6	<b>502</b>	<b>19.6</b>	502	19.6
470.lbm	380	36.1	<b>381</b>	<b>36.0</b>	381	36.0	380	36.1	<b>381</b>	<b>36.0</b>	381	36.0
481.wrf	412	27.1	411	27.2	<b>412</b>	<b>27.1</b>	378	29.5	<b>379</b>	<b>29.5</b>	401	27.8
482.sphinx3	<b>1087</b>	<b>17.9</b>	1088	17.9	896	21.7	1012	19.3	<b>1017</b>	<b>19.2</b>	1024	19.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS Settings:

Power Management = Maximum Performance (Default = Active Power Controller)

## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
KMP\_STACKSIZE set to 200M

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## General Notes (Continued)

The Dell PowerEdge T610 and the Bull NovaScale T840 F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge T610 model.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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## Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
-unroll2`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32`

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## Peak Optimization Flags (Continued)

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.xml>



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