SPEC® CINT2006 Result
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Fujitsu

PRIMERGY BX922 S2, Intel Xeon X5660, 2.80 GHz

SPECint®_rate2006 = 180

SPECint_rate_base2006 = 170

CPU2006 license: 19	**Test date:** Jun-2010
Test sponsor: Fujitsu	**Hardware Availability:** Oct-2010
Tested by: Fujitsu	**Software Availability:** Jan-2010

SPECint_rate_base2006 = 170

SPECint_rate2006 = 180

Hardware

CPU Name:	Intel Xeon X5660
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	6 cores, 1 chip, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SSD SATA, 64 GB
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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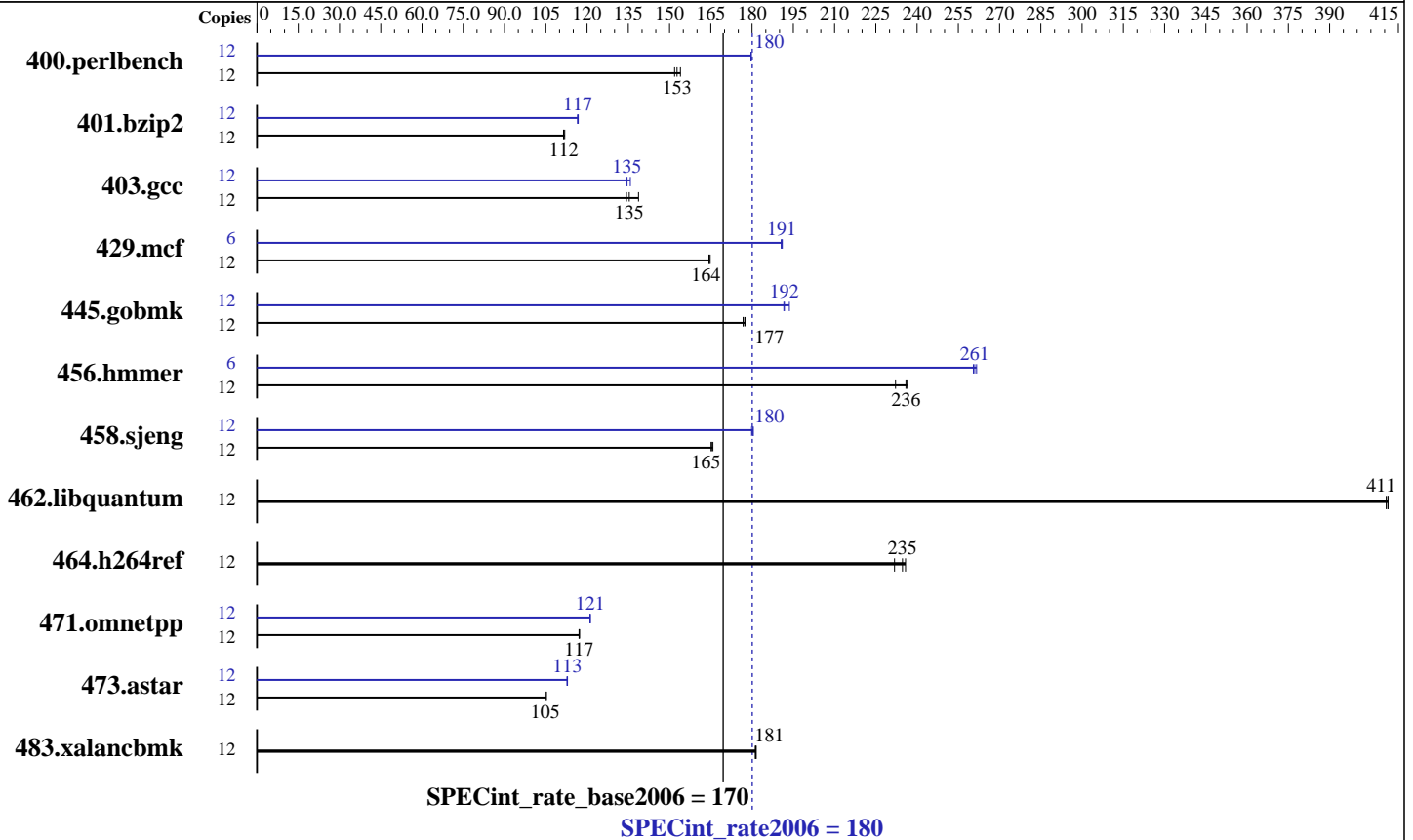|	
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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	772	152	762	154	**768**	**153**		12	653	180	**653**	**180**	652	180
401.bzip2	12	1035	112	1039	111	**1038**	**112**		12	**993**	**117**	993	117	991	117
403.gcc	12	719	134	696	139	**714**	**135**		12	719	134	712	136	**718**	**135**
429.mcf	12	**665**	**164**	664	165	666	164		6	287	191	286	191	**287**	**191**
445.gobmk	12	712	177	**712**	**177**	709	177		12	657	192	650	194	**656**	**192**
456.hmmer	12	474	236	**474**	**236**	482	232		6	214	262	215	260	**214**	**261**
458.sjeng	12	**878**	**165**	879	165	876	166		12	**805**	**180**	807	180	805	180
462.libquantum	12	606	411	605	411	**605**	**411**		12	606	411	605	411	**605**	**411**
464.h264ref	12	1146	232	1126	236	**1131**	**235**		12	1146	232	1126	236	**1131**	**235**
471.omnetpp	12	639	117	**640**	**117**	641	117		12	619	121	**619**	**121**	619	121
473.astar	12	**803**	**105**	803	105	800	105		12	746	113	747	113	**747**	**113**
483.xalancbmk	12	**457**	**181**	456	182	457	181		12	**457**	**181**	456	182	457	181

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
     BIOS configuration:
     Data Reuse Optimization = Disabled
     Performance/Power Setting = Traditional
```

General Notes

```
     For information about Fujitsu please visit: http://www.fujitsu.com
     Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
```

Base Compiler Invocation

C benchmarks:
```
      icc  -m32
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc   -m32

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks (except as noted below):
    icpc -m32

    473.astar: icpc -m64

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Peak Portability Flags

```
   400.perlbench: -DSPEC_CPU_LINUX_IA32
       401.bzip2: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
       458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
        473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
   400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -ansi-alias

       401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

         403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

         429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

       445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                  -ipo -no-prec-div -ansi-alias

       456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                  -ansi-alias -auto-ilp32

       458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll4 -auto-ilp32

    462.libquantum: basepeak = yes

      464.h264ref: basepeak = yes
```

C++ benchmarks:

```
     471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                  -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

        473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
                  -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml

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For other inquiries, please contact webmaster@spec.org.

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