



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp[®]_rate2006 = 555

PowerEdge R910 (Intel Xeon X7560, 2.27 GHz)

SPECfp_rate_base2006 = 539

CPU2006 license: 55

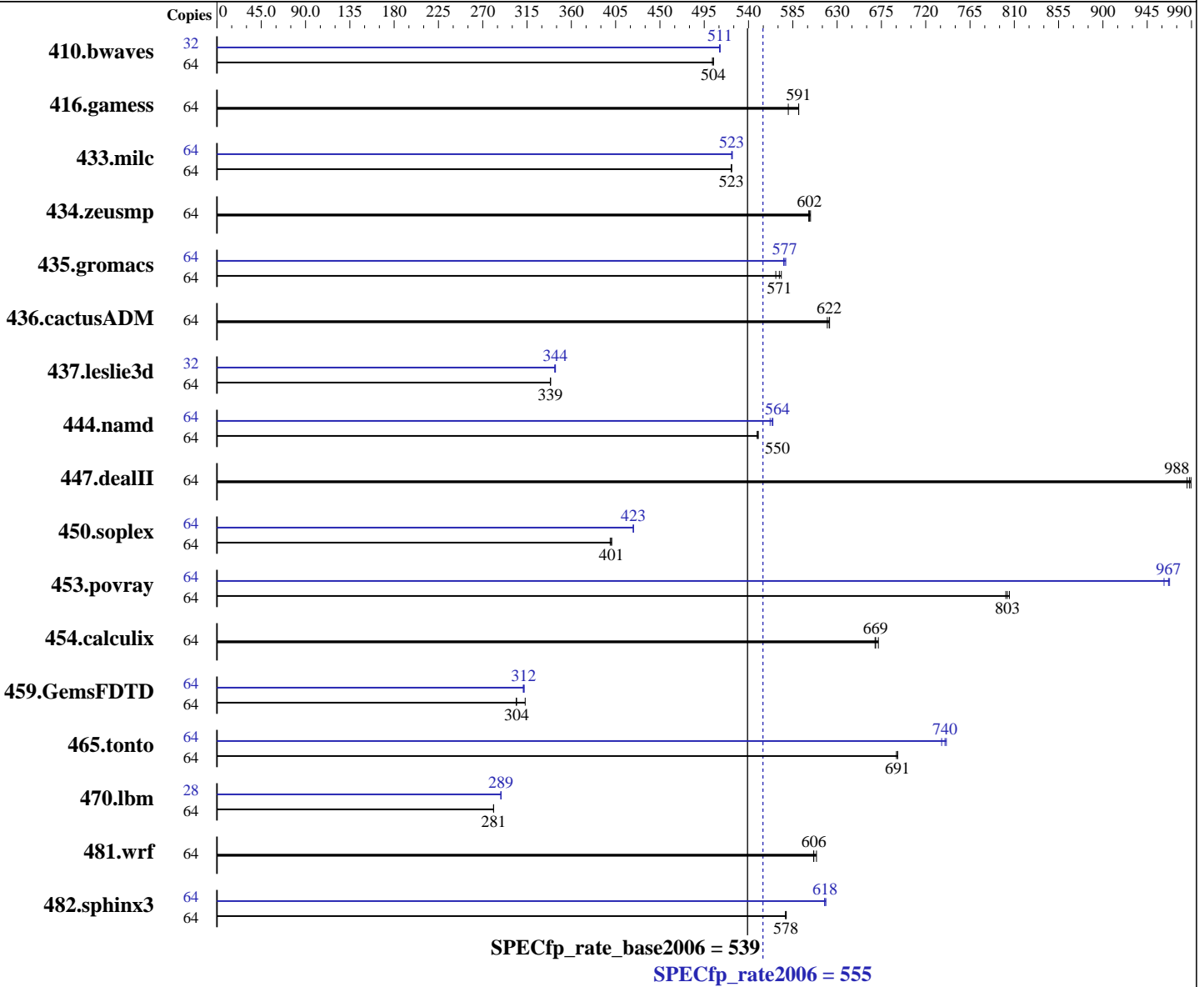
Test date: Feb-2010

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Dec-2009



Hardware

CPU Name: Intel Xeon X7560
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz
 CPU MHz: 2266
 FPU: Integrated
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux 5 (x86_64) Update 4 errata kernel (RHEL 5.4.z) kernel-2.6.18-164.9.1.el5.x86_64
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: No
 File System: ext3

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 555

PowerEdge R910 (Intel Xeon X7560, 2.27 GHz)

SPECfp_rate_base2006 = 539

CPU2006 license: 55

Test date: Feb-2010

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Dec-2009

L3 Cache: 24 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (64 x 4 GB DDR3-1066 QR RDIMM, CL7, ECC)
 Disk Subsystem: 1 x 300 GB 10000 RPM SAS 6Gb
 Other Hardware: None

System State: Run level 3 (multi-user mode)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	1725	504	1728	503	1724	504	32	852	511	851	511	851	511
416.gamess	64	2159	580	2121	591	2120	591	64	2159	580	2121	591	2120	591
433.milc	64	1124	523	1124	523	1124	523	64	1123	523	1122	524	1123	523
434.zeusmp	64	966	603	968	602	969	601	64	966	603	968	602	969	601
435.gromacs	64	805	568	800	571	797	573	64	792	577	791	578	794	576
436.cactusADM	64	1233	620	1229	622	1230	622	64	1233	620	1229	622	1230	622
437.leslie3d	64	1776	339	1775	339	1776	339	32	877	343	875	344	875	344
444.namd	64	934	550	935	549	934	550	64	913	562	909	565	910	564
447.dealII	64	743	986	740	990	741	988	64	743	986	740	990	741	988
450.soplex	64	1336	399	1331	401	1332	401	64	1262	423	1262	423	1262	423
453.povray	64	424	803	425	801	423	805	64	352	967	352	968	354	962
454.calculix	64	786	672	790	669	789	669	64	786	672	790	669	789	669
459.GemsFDTD	64	2234	304	2231	304	2169	313	64	2175	312	2180	312	2182	311
465.tonto	64	911	692	912	690	911	691	64	855	736	850	741	851	740
470.lbm	64	3130	281	3128	281	3128	281	28	1334	288	1333	289	1333	289
481.wrf	64	1179	606	1179	606	1174	609	64	1179	606	1179	606	1174	609
482.sphinx3	64	2160	578	2159	578	2157	578	64	2020	618	2016	619	2021	617

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

vm.zone_reclaim_mode = 1 in /etc/sysctl.conf file
BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 555

PowerEdge R910 (Intel Xeon X7560, 2.27 GHz)

SPECfp_rate_base2006 = 539

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Feb-2010
Hardware Availability: Mar-2010
Software Availability: Dec-2009

General Notes

The Dell PowerEdge R910 and the Bull NovaScale R480 F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge R910 model.

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 555

PowerEdge R910 (Intel Xeon X7560, 2.27 GHz)

SPECfp_rate_base2006 = 539

CPU2006 license: 55

Test date: Feb-2010

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Dec-2009

Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 555

PowerEdge R910 (Intel Xeon X7560, 2.27 GHz)

SPECfp_rate_base2006 = 539

CPU2006 license: 55

Test date: Feb-2010

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Dec-2009

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = 555

PowerEdge R910 (Intel Xeon X7560, 2.27 GHz)

SPECfp_rate_base2006 = 539

CPU2006 license: 55

Test date: Feb-2010

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Dec-2009

Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 13:15:10 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 July 2010.