SPEC® CINT2006 Result

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NEC Corporation

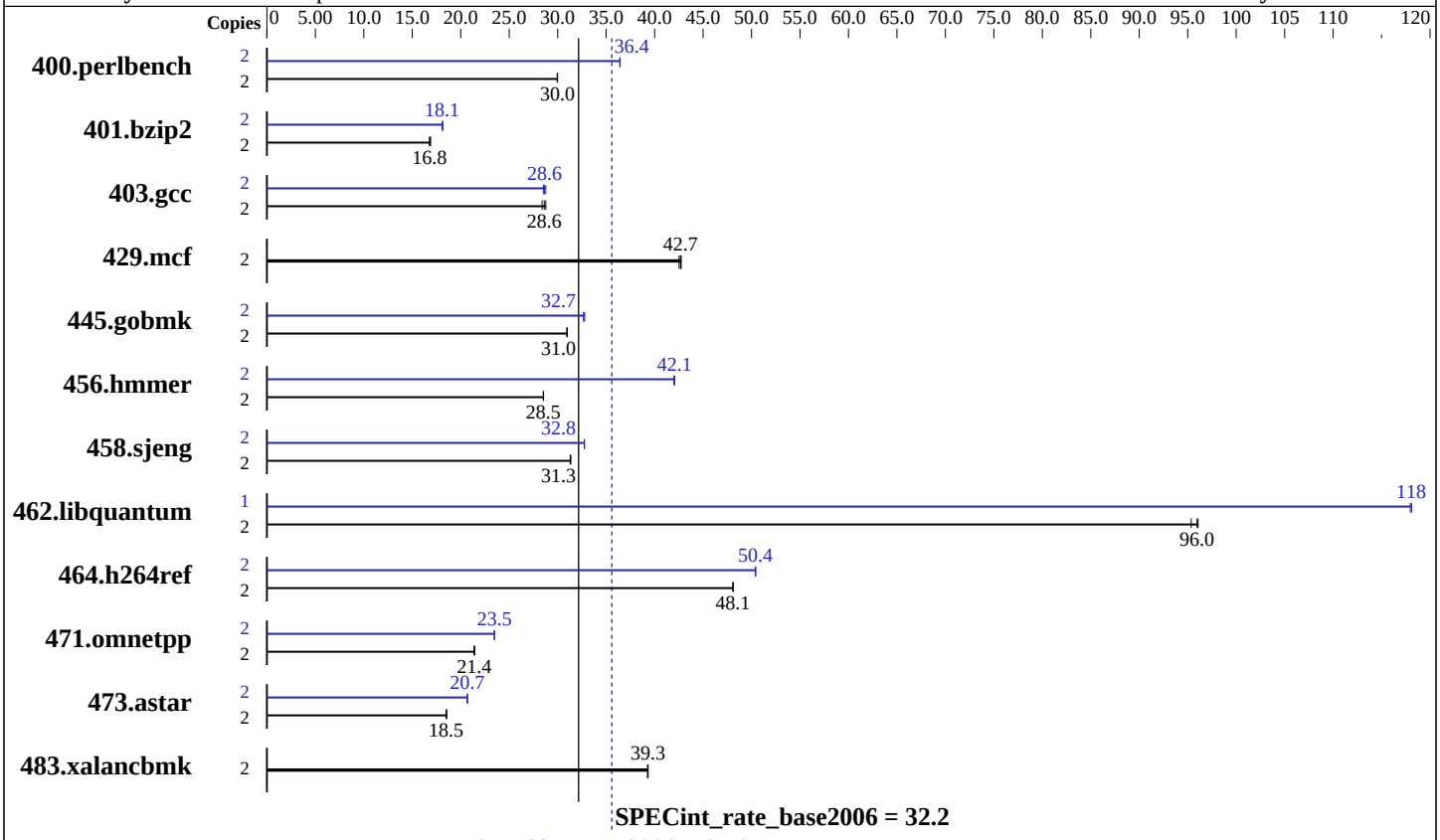
Express5800/E110b-1
(Intel Celeron G1101)

SPECint®_rate2006 = 35.6

SPECint_rate_base2006 = 32.2

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Jun-2010
Hardware Availability: Apr-2010
Software Availability: Dec-2009



Hardware

CPU Name:	Intel Celeron G1101
CPU Characteristics:	
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	2 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB PC3-10600E, 2 rank, CL9, ECC, running at 1066 MHz)
Disk Subsystem:	1x160 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	652	29.9	652	30.0	**652**	**30.0**	2	536	36.5	**536**	**36.4**	537	36.4
401.bzip2	2	1142	16.9	**1150**	**16.8**	1150	16.8	2	1063	18.2	**1067**	**18.1**	1067	18.1
403.gcc	2	**563**	**28.6**	567	28.4	559	28.8	2	559	28.8	564	28.5	**562**	**28.6**
429.mcf	2	427	42.7	**427**	**42.7**	429	42.5	2	427	42.7	**427**	**42.7**	429	42.5
445.gobmk	2	678	30.9	**677**	**31.0**	677	31.0	2	642	32.7	**642**	**32.7**	640	32.8
456.hmmer	2	**654**	**28.5**	654	28.5	654	28.5	2	444	42.1	**444**	**42.1**	444	42.0
458.sjeng	2	772	31.4	**773**	**31.3**	773	31.3	2	739	32.8	739	32.8	**739**	**32.8**
462.libquantum	2	435	95.4	431	96.1	**432**	**96.0**	1	**175**	**118**	175	118	176	118
464.h264ref	2	**920**	**48.1**	920	48.1	921	48.1	2	878	50.4	**878**	**50.4**	878	50.4
471.omnetpp	2	583	21.4	585	21.4	**584**	**21.4**	2	532	23.5	**532**	**23.5**	533	23.5
473.astar	2	759	18.5	**759**	**18.5**	757	18.5	2	**679**	**20.7**	680	20.6	678	20.7
483.xalancbmk	2	351	39.3	351	39.3	**351**	**39.3**	2	351	39.3	351	39.3	**351**	**39.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset has been used to bind processes to cores except
 for 462.libquantum peak
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
Default BIOS settings were used.
```

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3 -opt-prefetch`

C++ benchmarks:
`-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

462.libquantum: `icc -m64`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`

Continued on next page

Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll4

462.libquantum: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -parallel -auto-ilp32 -opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib64 -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100609.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100609.xml`

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For other inquiries, please contact webmaster@spec.org.

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