



SPEC® CINT2006 Result

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Itautec

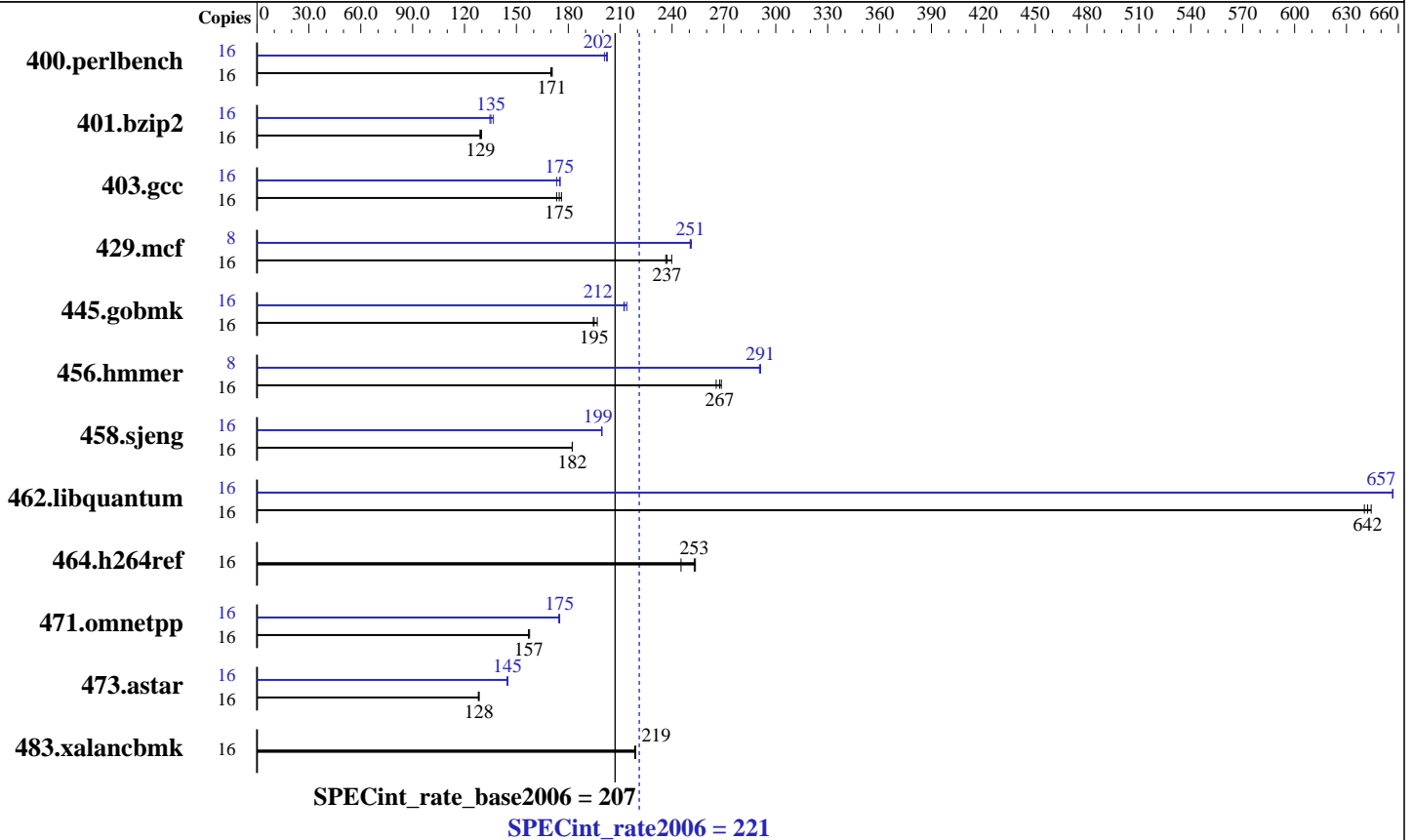
SPECint®_rate2006 = 221

Servidor Itautec MX203 (Intel Xeon E5620)

SPECint_rate_base2006 = 207

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jun-2010
Hardware Availability: Apr-2010
Software Availability: Apr-2010



Hardware

CPU Name: Intel Xeon E5620
 CPU Characteristics: Intel Turbo Boost Technology up to 2.66 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4GB, DDR3-1066, Dual Rank, CL 7, ECC)
 Disk Subsystem: 1 x 160 GB SATA-2, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
 Compiler: Intel C++ Professional Compiler 11.1 for Linux Build 20100414 Package ID: l_cproc_p_11.1.072
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	920	170	916	171	916	171	16	774	202	778	201	772	203
401.bzip2	16	1194	129	1189	130	1198	129	16	1129	137	1142	135	1146	135
403.gcc	16	732	176	743	173	737	175	16	743	173	736	175	735	175
429.mcf	16	617	236	609	240	615	237	8	291	251	291	251	291	250
445.gobmk	16	853	197	863	194	861	195	16	791	212	790	212	785	214
456.hammer	16	562	266	558	267	556	269	8	257	291	256	291	256	291
458.sjeng	16	1062	182	1062	182	1062	182	16	972	199	971	199	970	200
462.libquantum	16	516	642	518	640	515	644	16	505	657	505	657	505	657
464.h264ref	16	1444	245	1397	254	1400	253	16	1444	245	1397	254	1400	253
471.omnetpp	16	637	157	637	157	634	158	16	571	175	572	175	573	174
473.astar	16	876	128	878	128	874	128	16	776	145	777	145	774	145
483.xalancbmk	16	505	218	505	219	504	219	16	505	218	505	219	504	219

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

General Notes

This result was measured on the Servidor Itaotec MX223.
The Servidor Itaotec MX223 and the Servidor Itaotec MX203 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-prefetch
464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8/lib -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itaotec-Intel-ic11.1-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itaotec-Intel-ic11.1-linux64-revE.xml>

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