



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 74.7

Acer AT150 F1(Intel Xeon E5502)

SPECint_rate_base2006 = 68.8

CPU2006 license: 97

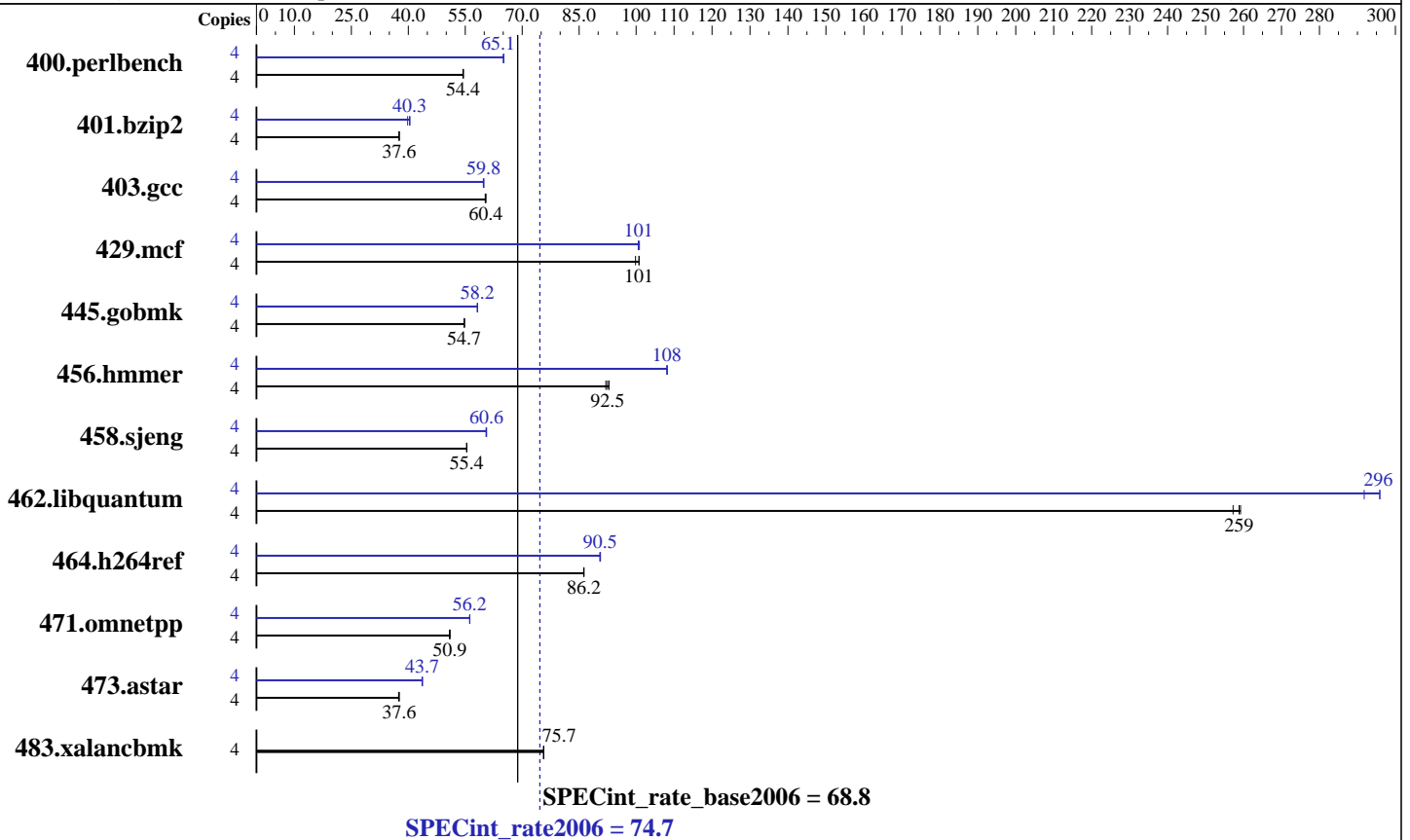
Test date: Apr-2010

Test sponsor: Acer Incorporated

Hardware Availability: May-2010

Tested by: Acer Incorporated

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon E5502
 CPU Characteristics: 1866
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (12 x 2GB DDR3-1333 RDIMM, running at 800 MHz)
 Disk Subsystem: 1000 GB SATAII, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	719	54.4	717	54.5	<u>718</u>	<u>54.4</u>	4	<u>600</u>	<u>65.1</u>	601	65.1	600	65.1
401.bzip2	4	1027	37.6	1027	37.6	<u>1027</u>	<u>37.6</u>	4	<u>957</u>	<u>40.3</u>	955	40.4	969	39.8
403.gcc	4	<u>533</u>	<u>60.4</u>	534	60.3	533	60.4	4	537	59.9	<u>538</u>	<u>59.8</u>	539	59.8
429.mcf	4	365	99.8	362	101	<u>362</u>	<u>101</u>	4	<u>363</u>	<u>101</u>	362	101	363	101
445.gobmk	4	766	54.8	<u>766</u>	<u>54.7</u>	767	54.7	4	<u>721</u>	<u>58.2</u>	721	58.2	721	58.2
456.hammer	4	402	92.9	406	92.0	<u>404</u>	<u>92.5</u>	4	345	108	<u>345</u>	<u>108</u>	345	108
458.sjeng	4	<u>874</u>	<u>55.4</u>	874	55.4	874	55.4	4	799	60.6	<u>799</u>	<u>60.6</u>	800	60.5
462.libquantum	4	322	257	320	259	<u>320</u>	<u>259</u>	4	280	296	284	292	<u>280</u>	<u>296</u>
464.h264ref	4	1027	86.2	<u>1027</u>	<u>86.2</u>	1026	86.3	4	<u>978</u>	<u>90.5</u>	977	90.6	978	90.5
471.omnetpp	4	<u>491</u>	<u>50.9</u>	490	51.0	492	50.8	4	445	56.2	445	56.2	<u>445</u>	<u>56.2</u>
473.astar	4	747	37.6	750	37.4	<u>747</u>	<u>37.6</u>	4	<u>643</u>	<u>43.7</u>	642	43.7	644	43.6
483.xalancbmk	4	365	75.6	365	75.7	<u>365</u>	<u>75.7</u>	4	365	75.6	365	75.7	<u>365</u>	<u>75.7</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

This result was measured on the Gateway GT150 F1.

The Acer AT150 F1 and Gateway GT150 F1 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

Continued on next page



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Peak Optimization Flags (Continued)

473.astar (continued):

`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmarthearp64`

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For other inquiries, please contact webmaster@spec.org.

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