



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R610
(Intel Xeon E5530, 2.40 GHz)

SPECfp[®]2006 = 35.9

SPECfp_base2006 = 33.7

CPU2006 license: 55

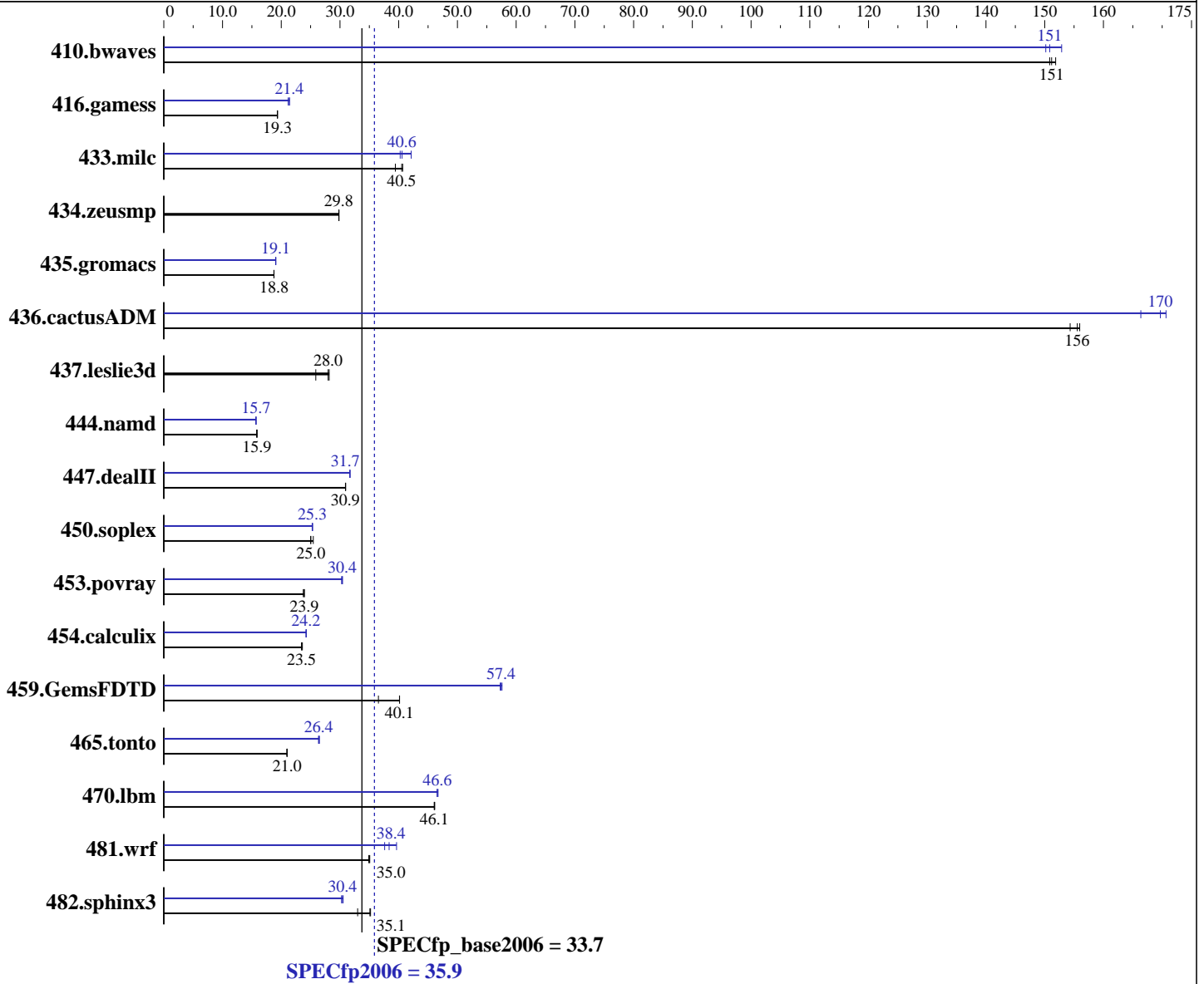
Test sponsor: Dell Inc.

Tested by: Bull SAS

Test date: May-2010

Hardware Availability: Mar-2009

Software Availability: Dec-2009



Hardware

CPU Name: Intel Xeon E5530
 CPU Characteristics: Intel Turbo Boost Technology up to 2.66 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: I_cproc_p_11.1.064, I_cprof_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R610
(Intel Xeon E5530, 2.40 GHz)

SPECfp2006 = **35.9**

SPECfp_base2006 = **33.7**

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Bull SAS

Test date: May-2010

Hardware Availability: Mar-2009

Software Availability: Dec-2009

L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB PC3-10600R, 2 Rank, CL9-9-9, ECC, running at 1066 MHz)
Disk Subsystem: 1 x 73 GB SAS, 10000 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	90.1	151	89.5	152	89.9	151	90.1	151	90.5	150	88.9	153
416.gamess	1012	19.3	1013	19.3	1011	19.4	915	21.4	925	21.2	915	21.4
433.milc	227	40.5	226	40.7	233	39.4	218	42.1	228	40.3	226	40.6
434.zeusmp	305	29.8	305	29.8	305	29.8	305	29.8	305	29.8	305	29.8
435.gromacs	381	18.7	380	18.8	380	18.8	374	19.1	374	19.1	375	19.0
436.cactusADM	77.4	154	76.6	156	76.8	156	70.0	171	71.8	166	70.4	170
437.leslie3d	363	25.9	336	28.0	334	28.2	363	25.9	336	28.0	334	28.2
444.namd	507	15.8	505	15.9	505	15.9	511	15.7	511	15.7	511	15.7
447.dealII	369	31.0	370	30.9	370	30.9	361	31.7	361	31.7	361	31.7
450.soplex	328	25.5	333	25.0	333	25.0	329	25.3	330	25.3	330	25.3
453.povray	222	23.9	222	23.9	224	23.8	176	30.3	175	30.4	175	30.4
454.calculix	351	23.5	352	23.5	350	23.6	341	24.2	340	24.2	340	24.3
459.GemsFDTD	264	40.1	290	36.5	264	40.1	184	57.6	185	57.4	185	57.3
465.tonto	470	20.9	468	21.0	468	21.0	373	26.4	373	26.4	371	26.5
470.lbm	298	46.1	298	46.1	298	46.0	295	46.5	295	46.6	294	46.7
481.wrf	319	35.0	320	34.9	318	35.1	297	37.6	291	38.4	282	39.6
482.sphinx3	590	33.0	555	35.1	554	35.2	638	30.5	641	30.4	644	30.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
The Dell PowerEdge R610 and
the Bull NovaScale R440 F2 models are electronically equivalent.
The results have been measured on a Bull NovaScale R440 F2 model.

Base Compiler Invocation

C benchmarks:
icc -m64

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R610
(Intel Xeon E5530, 2.40 GHz)

SPECfp2006 = 35.9

SPECfp_base2006 = 33.7

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Bull SAS

Test date: May-2010
Hardware Availability: Mar-2009
Software Availability: Dec-2009

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R610
(Intel Xeon E5530, 2.40 GHz)

SPECfp2006 = 35.9

SPECfp_base2006 = 33.7

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Bull SAS

Test date: May-2010
Hardware Availability: Mar-2009
Software Availability: Dec-2009

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R610
(Intel Xeon E5530, 2.40 GHz)

SPECfp2006 = 35.9

SPECfp_base2006 = 33.7

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Bull SAS

Test date: May-2010

Hardware Availability: Mar-2009

Software Availability: Dec-2009

Peak Optimization Flags (Continued)

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100511.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100511.xml>



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R610
(Intel Xeon E5530, 2.40 GHz)

SPECfp2006 = 35.9

SPECfp_base2006 = 33.7

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Bull SAS

Test date: May-2010

Hardware Availability: Mar-2009

Software Availability: Dec-2009

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 08:12:26 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 June 2010.